



# SPEC® CINT2006 Result

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Itautec

SPECint®\_rate2006 = 103

Servidor Itautec MX203 (Intel Xeon E5520)

SPECint\_rate\_base2006 = 95.9

CPU2006 license: 9001

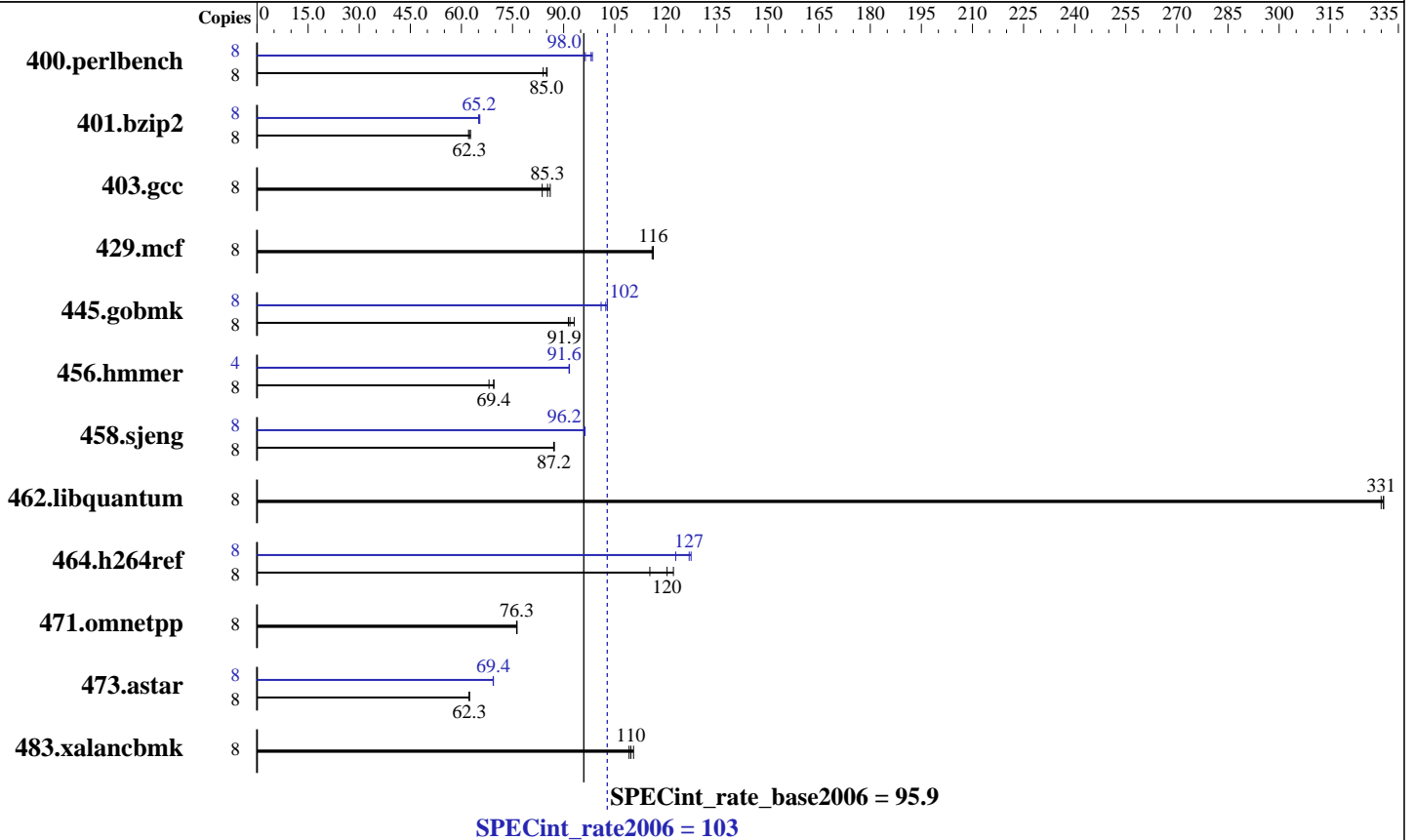
Test date: Dec-2009

Test sponsor: Itautec

Hardware Availability: Mar-2009

Tested by: Itautec

Software Availability: Feb-2009



## Hardware

CPU Name: Intel Xeon E5520  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.53 GHz  
 CPU MHz: 2267  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 12 GB (3 x 4GB DDR3-1066, CL 7, ECC)  
 Disk Subsystem: 1 x 160 GB SATA-2, 7200RPM  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2 with patch Linux kernel 20090119, Kernel 2.6.16.60-0.34-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l\_cproc\_p\_11.0.081  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	<b>920</b>	<b>85.0</b>	931	84.0	918	85.2	8	<b>797</b>	<b>98.0</b>	794	98.4	811	96.4
401.bzip2	8	1244	62.1	<b>1238</b>	<b>62.3</b>	1232	62.7	8	<b>1184</b>	<b>65.2</b>	1187	65.1	1180	65.4
403.gcc	8	748	86.1	769	83.7	<b>755</b>	<b>85.3</b>	8	748	86.1	769	83.7	<b>755</b>	<b>85.3</b>
429.mcf	8	629	116	627	116	<b>627</b>	<b>116</b>	8	629	116	627	116	<b>627</b>	<b>116</b>
445.gobmk	8	901	93.1	<b>914</b>	<b>91.9</b>	918	91.4	8	816	103	<b>820</b>	<b>102</b>	831	101
456.hammer	8	1073	69.5	1096	68.1	<b>1075</b>	<b>69.4</b>	4	407	91.6	<b>407</b>	<b>91.6</b>	407	91.7
458.sjeng	8	1112	87.1	1109	87.3	<b>1110</b>	<b>87.2</b>	8	<b>1006</b>	<b>96.2</b>	1005	96.3	1008	96.0
462.libquantum	8	502	330	501	331	<b>501</b>	<b>331</b>	8	502	330	501	331	<b>501</b>	<b>331</b>
464.h264ref	8	1449	122	<b>1471</b>	<b>120</b>	1535	115	8	1390	127	<b>1396</b>	<b>127</b>	1440	123
471.omnetpp	8	655	76.3	656	76.2	<b>655</b>	<b>76.3</b>	8	655	76.3	656	76.2	<b>655</b>	<b>76.3</b>
473.astar	8	901	62.3	902	62.3	<b>901</b>	<b>62.3</b>	8	810	69.3	<b>810</b>	<b>69.4</b>	809	69.4
483.xalanbmk	8	500	111	<b>503</b>	<b>110</b>	505	109	8	500	111	<b>503</b>	<b>110</b>	505	109

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## General Notes

This result was measured on the Servidor Itautec MX223.  
The Servidor Itautec MX203 and the Servidor Itautec MX223 are electronically equivalent.  
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.

## Base Compiler Invocation

C benchmarks:  
icc  
  
C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalanbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/richard/sh/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/081/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/081/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/081/bin/intel64/icc

C++ benchmarks (except as noted below):

icpc

473.astar: /opt/intel/Compiler/11.0/081/bin/intel64/icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32  
-Wl,-z,muldefs  
-L/home/richard/sh/SmartHeap\_8.1/lib -lsmartheap64

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.0-int-linux64-revA.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.0-int-linux64-revA.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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