SPEC® CINT2006 Result
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Dell Inc.

SPECint®_rate2006 = 339

PowerEdge M710 (Intel Xeon X5650, 2.66 GHz)

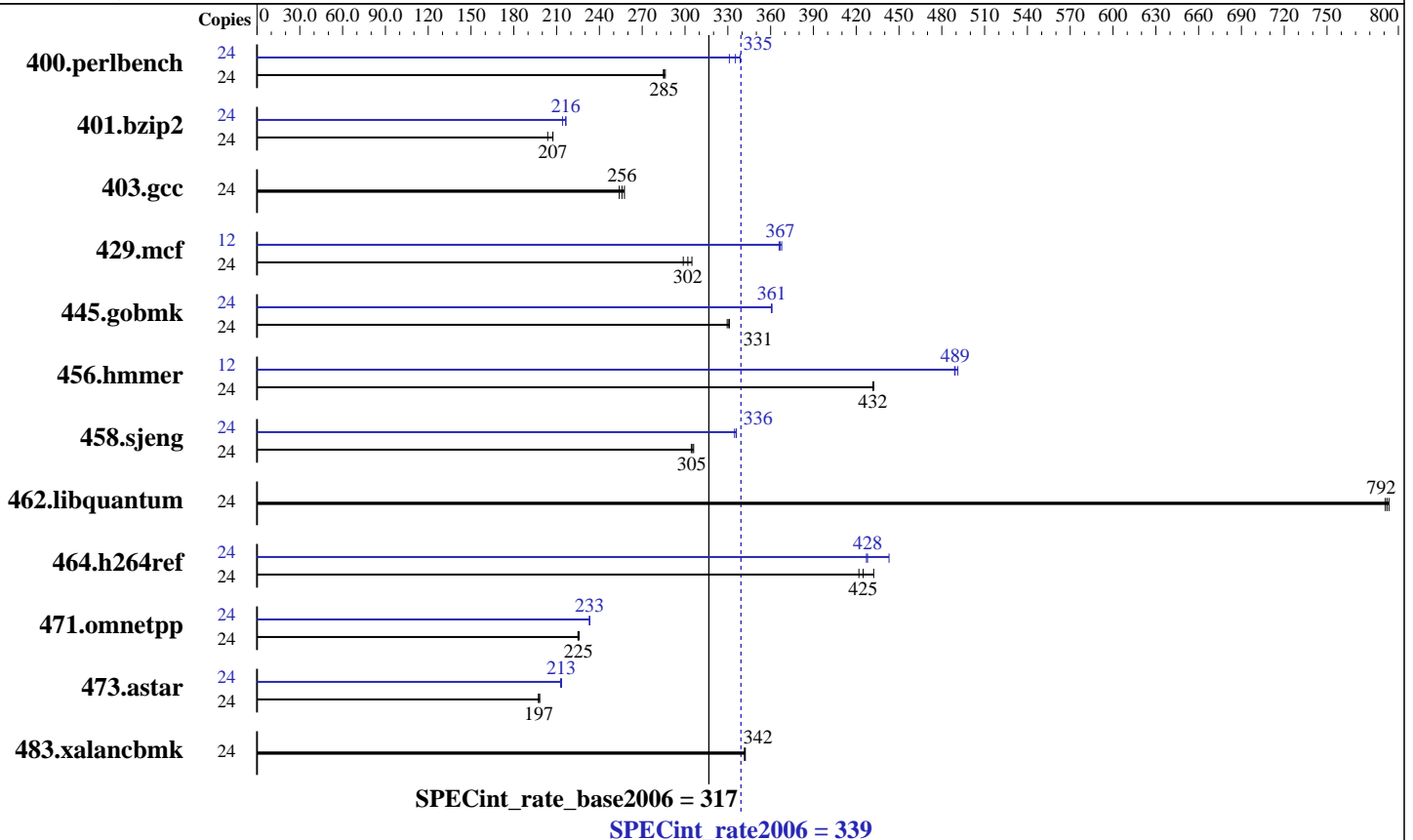SPECint_rate_base2006 = 317

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Apr-2010
Hardware Availability: Mar-2010
Software Availability: Dec-2009

Benchmark	Copies	Base	Peak
400.perlbench	24 / 24	285	335
401.bzip2	24 / 24	207	216
403.gcc	24	256	
429.mcf	24 / 12	302	367
445.gobmk	24 / 24	331	361
456.hmmer	24 / 12	432	489
458.sjeng	24 / 24	305	336
462.libquantum	24	792	
464.h264ref	24 / 24	425	428
471.omnetpp	24 / 24	225	233
473.astar	24 / 24	197	213
483.xalancbmk	24	342	

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Hardware

CPU Name: Intel Xeon X5650
CPU Characteristics: Intel Turbo Boost Technology up to 3.06 GHz
CPU MHz: 2667
FPU: Integrated
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB DDR3-1333 DR RDIMM, CL9, ECC)
Disk Subsystem: 1 x 146 GB 15000 RPM SAS
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-smp
Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1
Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel: No
File System: ext3
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V8.1

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Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	819	286	823	285	**822**	**285**	24	708	331	692	339	**699**	**335**
401.bzip2	24	1137	204	**1117**	**207**	1116	207	24	1081	214	**1070**	**216**	1069	217
403.gcc	24	750	258	761	254	**755**	**256**	24	750	258	761	254	**755**	**256**
429.mcf	24	733	299	**725**	**302**	718	305	12	299	366	298	368	**299**	**367**
445.gobmk	24	**761**	**331**	760	331	764	330	24	698	361	**698**	**361**	698	361
456.hmmer	24	**519**	**432**	518	432	519	432	12	228	491	**229**	**489**	229	489
458.sjeng	24	949	306	**951**	**305**	954	304	24	864	336	**865**	**336**	868	335
462.libquantum	24	629	791	627	793	**628**	**792**	24	629	791	627	793	**628**	**792**
464.h264ref	24	1229	432	**1250**	**425**	1259	422	24	**1241**	**428**	1243	427	1199	443
471.omnetpp	24	**666**	**225**	667	225	664	226	24	644	233	643	233	**644**	**233**
473.astar	24	850	198	854	197	**853**	**197**	24	**790**	**213**	792	213	789	213
483.xalancbmk	24	**484**	**342**	485	342	484	342	24	**484**	**342**	485	342	484	342

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
     numactl was used to bind copies to the cores

Operating System Notes

    'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

    BIOS Settings:
    Power Management = Maximum Performance (Default = Active Power Controller)
    Data Reuse = Disabled (Default = Enabled)

General Notes

    Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

    C benchmarks:
        icc -m32

    C++ benchmarks:
        icpc -m32

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Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m32
```

```
401.bzip2: icc -m64
```

```
456.hmmer: icc -m64
```

```
458.sjeng: icc -m64
```

C++ benchmarks (except as noted below):
```
icpc -m32
```

```
473.astar: icpc -m64
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
```

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Peak Portability Flags (Continued)

```
462.libquantum: -DSPEC_CPU_LINUX
     473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -ansi-alias

    401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

      403.gcc: basepeak = yes

      429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

    445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
               -ipo -no-prec-div -ansi-alias

    456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
               -ansi-alias -auto-ilp32

    458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

   464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
   471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
               -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

     473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
               -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64
```

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.xml

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For other inquiries, please contact webmaster@spec.org.

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