



# SPEC<sup>®</sup> CINT2006 Result

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## Fujitsu

SPECint<sup>®</sup>\_rate2006 = 146

PRIMERGY RX200 S6, Intel Xeon L5609, 1.87 GHz

SPECint\_rate\_base2006 = 135

CPU2006 license: 19

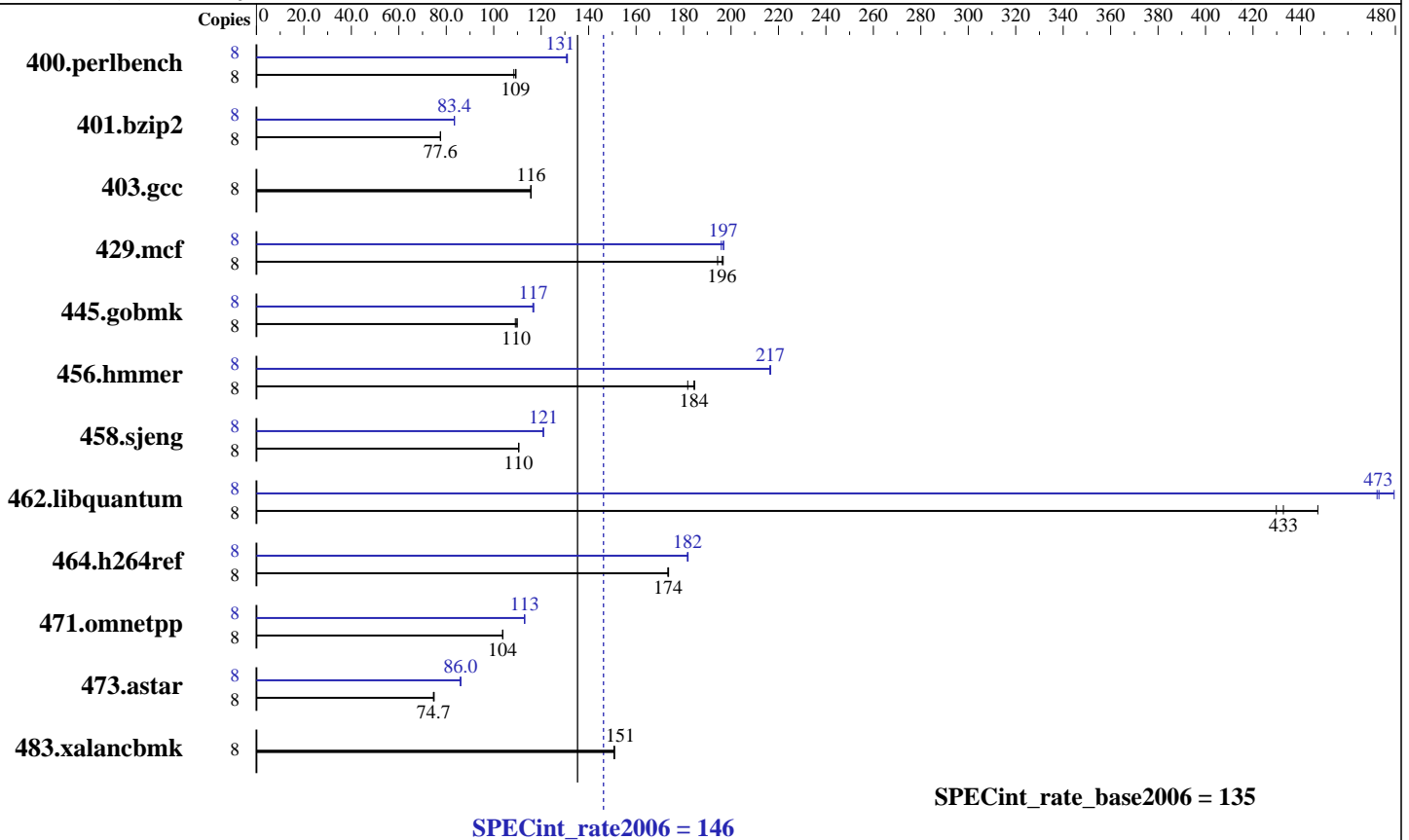
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: May-2010

Hardware Availability: Oct-2010

Software Availability: Jan-2010



### Hardware

CPU Name: Intel Xeon L5609  
 CPU Characteristics:  
 CPU MHz: 1867  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)  
 Disk Subsystem: 1 x SATA, 160 GB, 5400 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-default  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l\_cproc\_p\_11.1.064  
 Auto Parallel: No  
 File System: ext3  
 System State: Multi-User Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	715	109	<b>715</b>	<b>109</b>	720	108	8	<b>597</b>	<b>131</b>	596	131	598	131
401.bzip2	8	<b>995</b>	<b>77.6</b>	995	77.6	996	77.5	8	925	83.5	926	83.4	<b>926</b>	<b>83.4</b>
403.gcc	8	556	116	557	116	<b>556</b>	<b>116</b>	8	556	116	557	116	<b>556</b>	<b>116</b>
429.mcf	8	375	194	<b>372</b>	<b>196</b>	371	197	8	372	196	<b>371</b>	<b>197</b>	370	197
445.gobmk	8	763	110	<b>766</b>	<b>110</b>	769	109	8	<b>719</b>	<b>117</b>	718	117	720	116
456.hammer	8	<b>405</b>	<b>184</b>	411	182	404	185	8	344	217	345	216	<b>345</b>	<b>217</b>
458.sjeng	8	876	110	875	111	<b>876</b>	<b>110</b>	8	801	121	800	121	<b>800</b>	<b>121</b>
462.libquantum	8	386	430	371	447	<b>383</b>	<b>433</b>	8	<b>350</b>	<b>473</b>	346	479	351	472
464.h264ref	8	1019	174	1021	173	<b>1020</b>	<b>174</b>	8	<b>974</b>	<b>182</b>	973	182	975	182
471.omnetpp	8	<b>482</b>	<b>104</b>	482	104	482	104	8	<b>443</b>	<b>113</b>	442	113	443	113
473.astar	8	750	74.9	753	74.6	<b>752</b>	<b>74.7</b>	8	653	85.9	652	86.1	<b>653</b>	<b>86.0</b>
483.xalancbmk	8	366	151	<b>366</b>	<b>151</b>	366	151	8	366	151	<b>366</b>	<b>151</b>	366	151

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

The system automatically configures the memory to run at 1067 MHz.  
BIOS configuration:  
Data Reuse Optimization = Disable

## General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502  
For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:  
icc -m32  
  
C++ benchmarks:  
icpc -m32



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.icl11.1/libic11.1-32bit -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

456.hmmcr: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias  
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32  
403.gcc: basepeak = yes  
429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch  
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias  
456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32  
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32  
462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32  
-opt-prefetch  
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap  
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs

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## Peak Optimization Flags (Continued)

473.astar (continued):

-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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