



SPEC[®] CFP2006 Result

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Fujitsu

SPECfp[®]_rate2006 = 53.2

PRIMERGY RX100 S6, Intel Core i3-550, 3.20 GHz

SPECfp_rate_base2006 = 51.2

CPU2006 license: 19

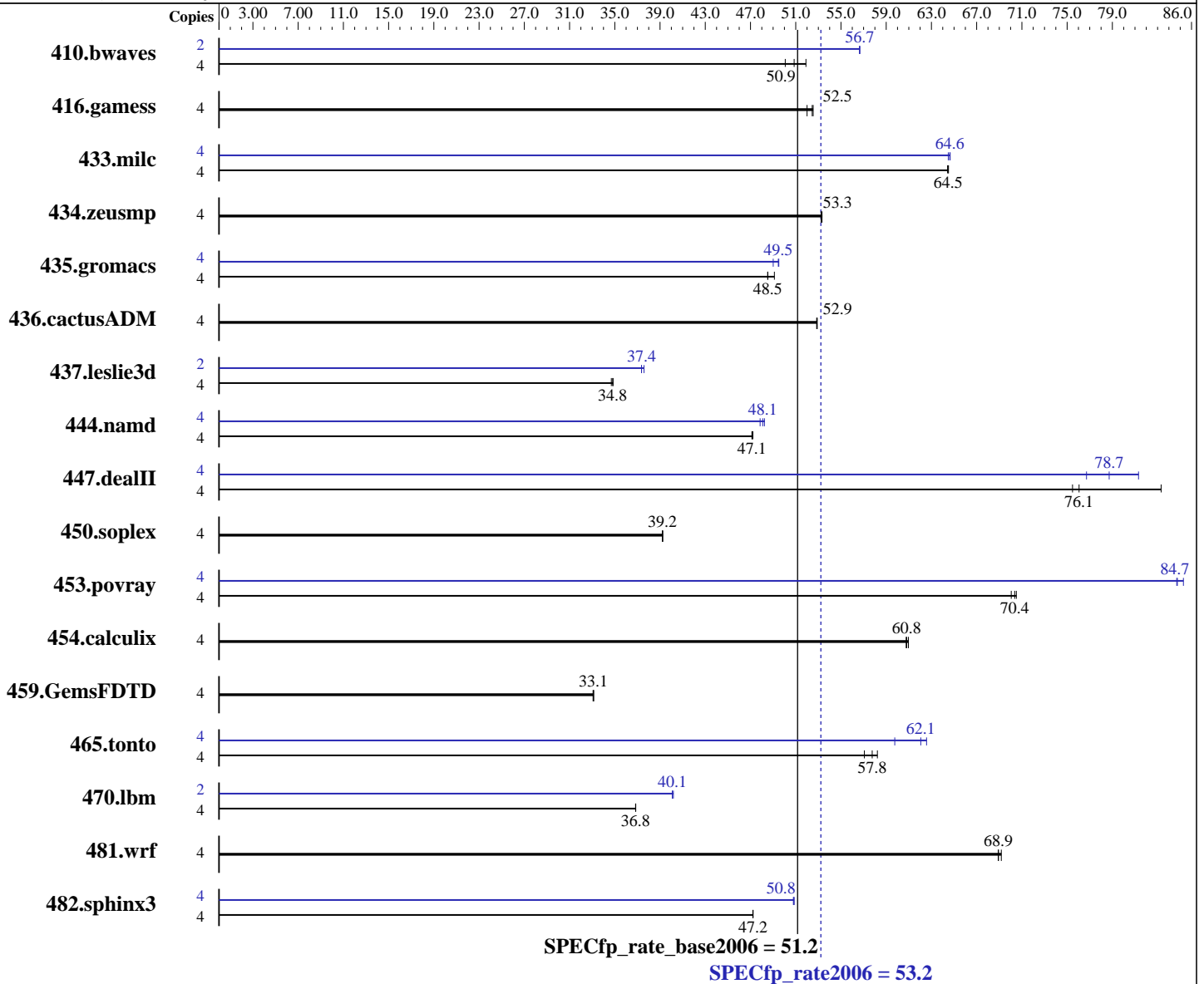
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Sep-2010

Hardware Availability: Aug-2010

Software Availability: Jan-2010



Hardware

CPU Name: Intel Core i3-550
 CPU Characteristics:
 CPU MHz: 3200
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
 Auto Parallel: No
 File System: ext3
 System State: Run Level 3 (multi user)

Continued on next page



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L3 Cache: 4 MB I+D on chip per chip
Other Cache: None
Memory: 16 GB (4x4 GB PC3-10600E, 2 rank, CL9-9-9, ECC)
Disk Subsystem: 1 x SSD SATA, 64 GB
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	1086	50.1	<u>1069</u>	<u>50.9</u>	1047	51.9	2	480	56.6	480	56.7	<u>480</u>	<u>56.7</u>
416.gamess	4	1491	52.5	<u>1493</u>	<u>52.5</u>	1506	52.0	4	1491	52.5	<u>1493</u>	<u>52.5</u>	1506	52.0
433.milc	4	<u>570</u>	<u>64.5</u>	569	64.5	570	64.4	4	568	64.7	<u>568</u>	<u>64.6</u>	569	64.5
434.zeusmp	4	684	53.2	<u>683</u>	<u>53.3</u>	683	53.3	4	684	53.2	<u>683</u>	<u>53.3</u>	683	53.3
435.gromacs	4	<u>588</u>	<u>48.5</u>	589	48.5	582	49.1	4	577	49.5	583	49.0	<u>577</u>	<u>49.5</u>
436.cactusADM	4	904	52.9	<u>904</u>	<u>52.9</u>	904	52.9	4	904	52.9	<u>904</u>	<u>52.9</u>	904	52.9
437.leslie3d	4	<u>1080</u>	<u>34.8</u>	1084	34.7	1079	34.8	2	<u>503</u>	<u>37.4</u>	500	37.6	503	37.4
444.namd	4	<u>681</u>	<u>47.1</u>	681	47.1	679	47.2	4	<u>667</u>	<u>48.1</u>	670	47.9	665	48.2
447.dealII	4	549	83.3	<u>602</u>	<u>76.1</u>	606	75.5	4	596	76.7	<u>581</u>	<u>78.7</u>	563	81.3
450.soplex	4	850	39.2	<u>851</u>	<u>39.2</u>	851	39.2	4	850	39.2	<u>851</u>	<u>39.2</u>	851	39.2
453.povray	4	304	70.1	302	70.5	<u>302</u>	<u>70.4</u>	4	251	84.7	<u>251</u>	<u>84.7</u>	250	85.3
454.calculix	4	541	61.0	<u>543</u>	<u>60.8</u>	543	60.8	4	541	61.0	<u>543</u>	<u>60.8</u>	543	60.8
459.GemsFDTD	4	1283	33.1	1281	33.1	<u>1282</u>	<u>33.1</u>	4	1283	33.1	1281	33.1	<u>1282</u>	<u>33.1</u>
465.tonto	4	<u>681</u>	<u>57.8</u>	676	58.2	690	57.1	4	<u>634</u>	<u>62.1</u>	659	59.8	629	62.6
470.lbm	4	1491	36.9	<u>1492</u>	<u>36.8</u>	1492	36.8	2	684	40.2	<u>685</u>	<u>40.1</u>	686	40.1
481.wrf	4	<u>648</u>	<u>68.9</u>	648	68.9	646	69.2	4	<u>648</u>	<u>68.9</u>	648	68.9	646	69.2
482.sphinx3	4	1651	47.2	<u>1651</u>	<u>47.2</u>	1651	47.2	4	<u>1534</u>	<u>50.8</u>	1533	50.9	1535	50.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502



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Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -opt-prefetch

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -ansi-alias -auto-ilp32

Continued on next page



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Peak Optimization Flags (Continued)

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep-

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.xml>

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Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 14:34:14 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 12 October 2010.