SPEC® CINT2006 Result
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Fujitsu

PRIMERGY TX200 S6, Intel Xeon X5650, 2.66 GHz

$SPECint®\_rate2006 =$ 343

$SPECint\_rate\_base2006 =$ 321

CPU2006 license: 19	**Test date:** Sep-2010
Test sponsor: Fujitsu	**Hardware Availability:** Jul-2010
Tested by: Fujitsu	**Software Availability:** Jan-2010

Copies graph:

- 400.perlbench: 24 — 350; 24 — 295
- 401.bzip2: 24 — 210
- 403.gcc: 24 — 256
- 429.mcf: 12 — 368; 24 — 304
- 445.gobmk: 24 — 371; 24 — 340
- 456.hmmer: 12 — 498; 24 — 441
- 458.sjeng: 24 — 345; 24 — 315
- 462.libquantum: 24 — 792
- 464.h264ref: 24 — 435; 24 — 423
- 471.omnetpp: 24 — 234; 24 — 225
- 473.astar: 24 — 216; 24 — 200
- 483.xalancbmk: 24 — 345

SPECint_rate_base2006 = 321

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Hardware

CPU Name:	Intel Xeon X5650
CPU Characteristics:	Intel Turbo Boost Technology up to 3.06 GHz
CPU MHz:	2667
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (12x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC)
Disk Subsystem:	1 x SATA, 160 GB, 5.4 krpm
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel:	No
File System:	ext3
System State:	Multi-User Run Level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1

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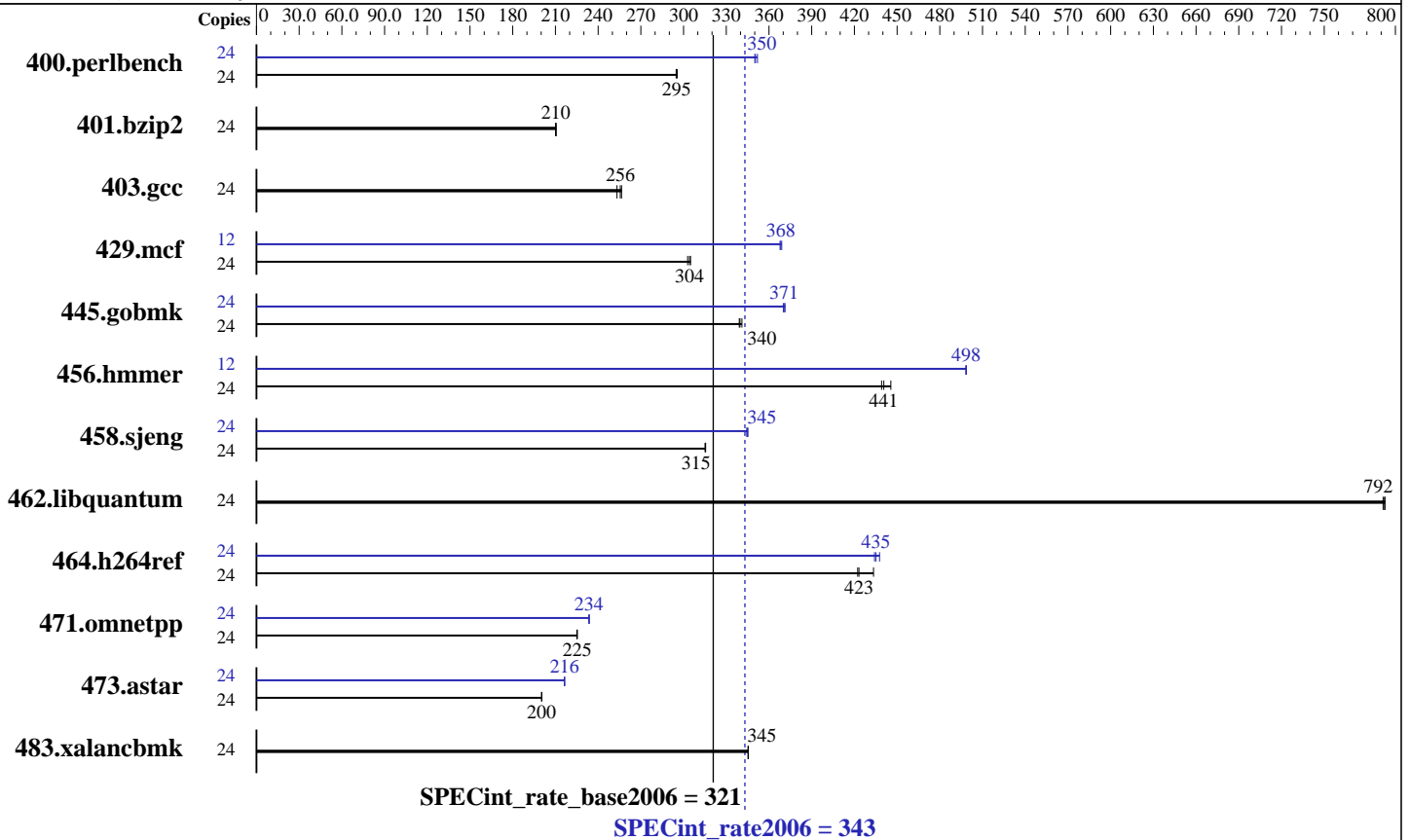
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Results Table

Benchmark	Copies	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Copies	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
400.perlbench	24	**795**	**295**	793	296	795	295	24	666	352	670	350	**669**	**350**
401.bzip2	24	**1102**	**210**	1100	211	1102	210	24	**1102**	**210**	1100	211	1102	210
403.gcc	24	763	253	753	256	**756**	**256**	24	763	253	753	256	**756**	**256**
429.mcf	24	**720**	**304**	718	305	723	303	12	297	369	**297**	**368**	298	368
445.gobmk	24	**741**	**340**	742	339	738	341	24	680	370	**679**	**371**	678	371
456.hmmer	24	510	439	503	446	**508**	**441**	12	**225**	**498**	225	499	225	498
458.sjeng	24	**921**	**315**	920	316	922	315	24	841	345	843	344	**842**	**345**
462.libquantum	24	628	791	627	793	**628**	**792**	24	628	791	627	793	**628**	**792**
464.h264ref	24	1225	433	1258	422	**1255**	**423**	24	1214	438	**1220**	**435**	1223	434
471.omnetpp	24	666	225	667	225	**666**	**225**	24	**642**	**234**	642	234	642	234
473.astar	24	841	200	**841**	**200**	841	200	24	779	216	**779**	**216**	778	216
483.xalancbmk	24	479	345	480	345	**479**	**345**	24	479	345	480	345	**479**	**345**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
     numactl was used to bind copies to the cores

Operating System Notes

     'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

    BIOS configuration:
    Data Reuse Optimization = Disable

General Notes

    Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502
    For information about Fujitsu please visit: http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:
    icc -m32

C++ benchmarks:
    icpc -m32

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Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

    -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
    -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc   -m32

    456.hmmer: icc -m64

    458.sjeng: icc -m64

C++ benchmarks (except as noted below):
    icpc -m32

    473.astar: icpc -m64

Peak Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    456.hmmer: -DSPEC_CPU_LP64
    458.sjeng: -DSPEC_CPU_LP64
    462.libquantum: -DSPEC_CPU_LINUX
    473.astar: -DSPEC_CPU_LP64
    483.xalancbmk: -DSPEC_CPU_LINUX

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Peak Optimization Flags

C benchmarks:

  400.perlbench: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
               `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2)
               `-prof-use`(pass 2) `-ansi-alias`

     401.bzip2: basepeak = yes

      403.gcc: basepeak = yes

      429.mcf: `-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

    445.gobmk: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-prof-use`(pass 2) `-O2`
               `-ipo -no-prec-div -ansi-alias`

    456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2`
               `-ansi-alias -auto-ilp32`

     458.sjeng: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
               `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2)
               `-prof-use`(pass 2) `-unroll4 -auto-ilp32`

  462.libquantum: basepeak = yes

    464.h264ref: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
               `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2)
               `-prof-use`(pass 2) `-unroll2 -ansi-alias`

C++ benchmarks:

   471.omnetpp: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
               `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
               `-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs`
               `-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap`

     473.astar: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
               `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
               `-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs`
               `-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64`

  483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

      403.gcc: `-Dalloca=_alloca`

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The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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