



SPEC® CFP2006 Result

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Hewlett-Packard Company

ProLiant DL120 G7
(3.50 GHz, Intel Xeon E3-1280)

SPECfp_rate2006 = 115

SPECfp_rate_base2006 = 111

CPU2006 license: 3

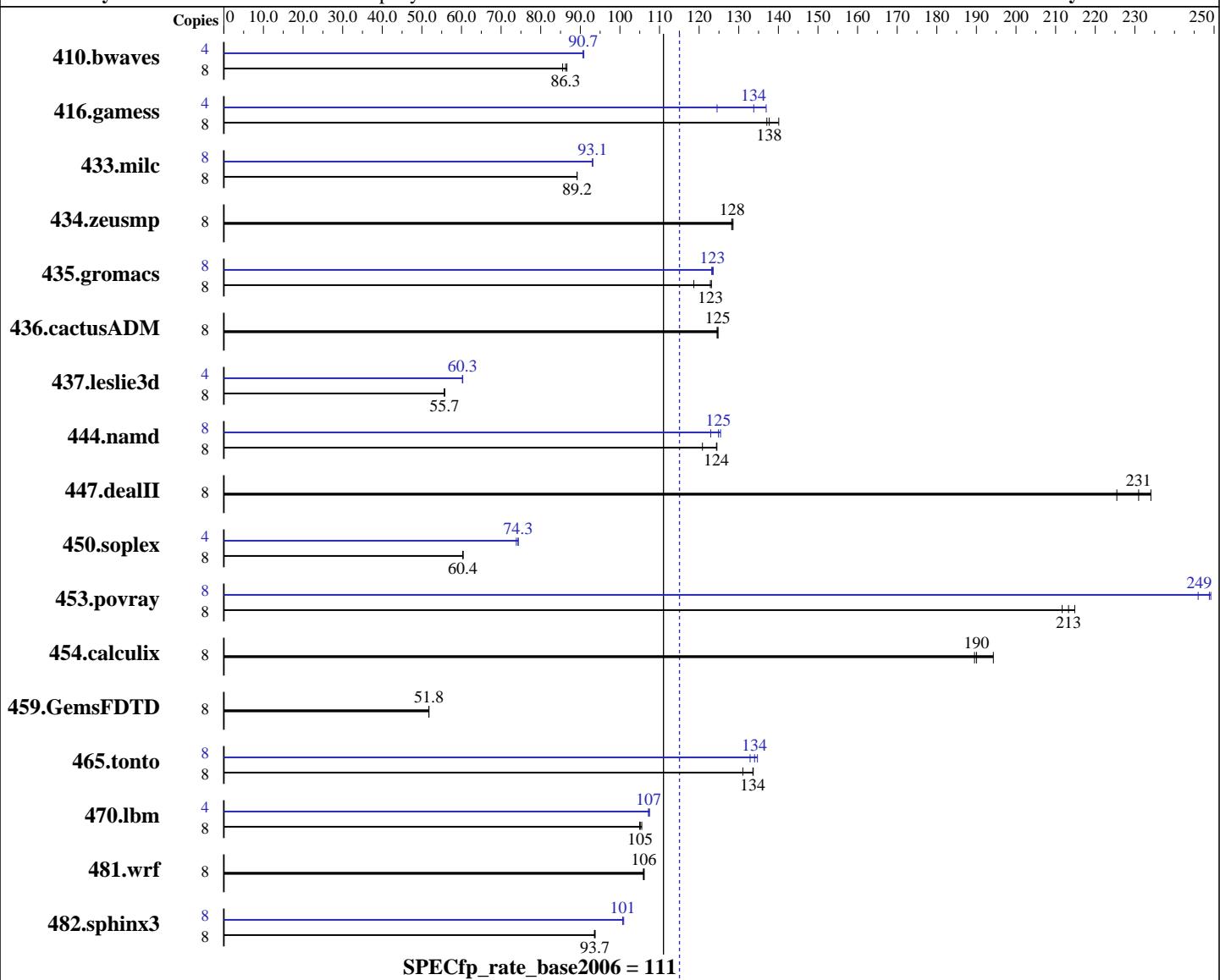
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Mar-2011

Hardware Availability: Apr-2011

Software Availability: Mar-2010



Hardware

CPU Name: Intel Xeon E3-1280
CPU Characteristics: Intel Turbo Boost Technology up to 3.90 GHz
CPU MHz: 3500
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116
Auto Parallel: No
File System: ext3
System State: Run level 3 (multi-user)
Base Pointers: 64-bit

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 16 GB (4 x 4 GB 2Rx8 PC3-10600E-9, ECC)
Disk Subsystem: 1 x 160 GB 7.2 K SATA
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	1271	85.5	1255	86.6	<u>1260</u>	<u>86.3</u>	4	<u>599</u>	<u>90.7</u>	598	90.9	599	90.7
416.gamess	8	<u>1138</u>	<u>138</u>	1118	140	1143	137	4	629	125	572	137	<u>585</u>	<u>134</u>
433.milc	8	824	89.2	<u>824</u>	<u>89.2</u>	824	89.2	8	<u>789</u>	<u>93.1</u>	789	93.1	789	93.1
434.zeusmp	8	566	129	568	128	<u>567</u>	<u>128</u>	8	566	129	568	128	<u>567</u>	<u>128</u>
435.gromacs	8	<u>465</u>	<u>123</u>	464	123	481	119	8	464	123	462	124	<u>463</u>	<u>123</u>
436.cactusADM	8	<u>767</u>	<u>125</u>	768	125	766	125	8	<u>767</u>	<u>125</u>	768	125	766	125
437.leslie3d	8	1349	55.8	1352	55.6	<u>1349</u>	<u>55.7</u>	4	623	60.3	624	60.3	<u>624</u>	<u>60.3</u>
444.namd	8	515	124	531	121	<u>516</u>	<u>124</u>	8	<u>514</u>	<u>125</u>	512	125	522	123
447.dealII	8	391	234	406	225	<u>396</u>	<u>231</u>	8	391	234	406	225	<u>396</u>	<u>231</u>
450.soplex	8	1105	60.4	<u>1105</u>	<u>60.4</u>	1106	60.3	4	452	73.8	<u>449</u>	<u>74.3</u>	449	74.3
453.povray	8	198	215	201	212	<u>200</u>	<u>213</u>	8	171	249	173	246	<u>171</u>	<u>249</u>
454.calculix	8	<u>347</u>	<u>190</u>	348	190	340	194	8	<u>347</u>	<u>190</u>	348	190	340	194
459.GemsFDTD	8	1640	51.8	<u>1640</u>	<u>51.8</u>	1639	51.8	8	1640	51.8	<u>1640</u>	<u>51.8</u>	1639	51.8
465.tonto	8	601	131	<u>589</u>	<u>134</u>	589	134	8	<u>588</u>	<u>134</u>	584	135	592	133
470.lbm	8	1042	106	<u>1046</u>	<u>105</u>	1047	105	4	513	107	511	107	<u>512</u>	<u>107</u>
481.wrf	8	<u>843</u>	<u>106</u>	844	106	842	106	8	<u>843</u>	<u>106</u>	844	106	842	106
482.sphinx3	8	1663	93.7	1667	93.6	<u>1665</u>	<u>93.7</u>	8	<u>1548</u>	<u>101</u>	1548	101	1544	101

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

```
'nodev /mnt/hugepages hugetlbfs defaults 0 0' added to /etc/fstab
'unlimit -s unlimited' was used to set the stacksize to unlimited prior to run
echo 3600 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```



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Platform Notes

BIOS configuration:

HP Power Profile set to Maximum Performance
Thermal Configuration set to Increased Cooling

General Notes

Binaries compiled on RHEL5.5

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -ansi-alias

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

470.lbm: -DSPEC_CPU_LP64

481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-ansi-alias -opt-prefetch -auto-ilp32

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll12

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-B /usr/share/libhugetlbfss/ -Wl,-hugetlbfss-link=BDT

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll14 -ansi-alias
-B /usr/share/libhugetlbfss/ -Wl,-melf_x86_64 -Wl,-hugetlbfss-link=BDT

Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll12
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfss/ -Wl,-melf_x86_64 -Wl,-hugetlbfss-link=BDT

459.GemsFDTD: basepeak = yes

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Peak Optimization Flags (Continued)

```
465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -unroll14 -auto
           -inline-calloc -opt-malloc-options=3
           -B /usr/share/libhugetlbfss/ -Wl,-melf_x86_64 -Wl,-hugetlbfss-link=BDT
```

Benchmarks using both Fortran and C:

```
435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
              -static -auto-ilp32
```

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>
<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20110316.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>
<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20110316.xml>

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