



SPEC® CFP2006 Result

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Hewlett-Packard Company

SPECfp®_rate2006 = 252

ProLiant DL380 G7
(3.06 GHz, Intel Xeon X5675)

SPECfp_rate_base2006 = 247

CPU2006 license: 3

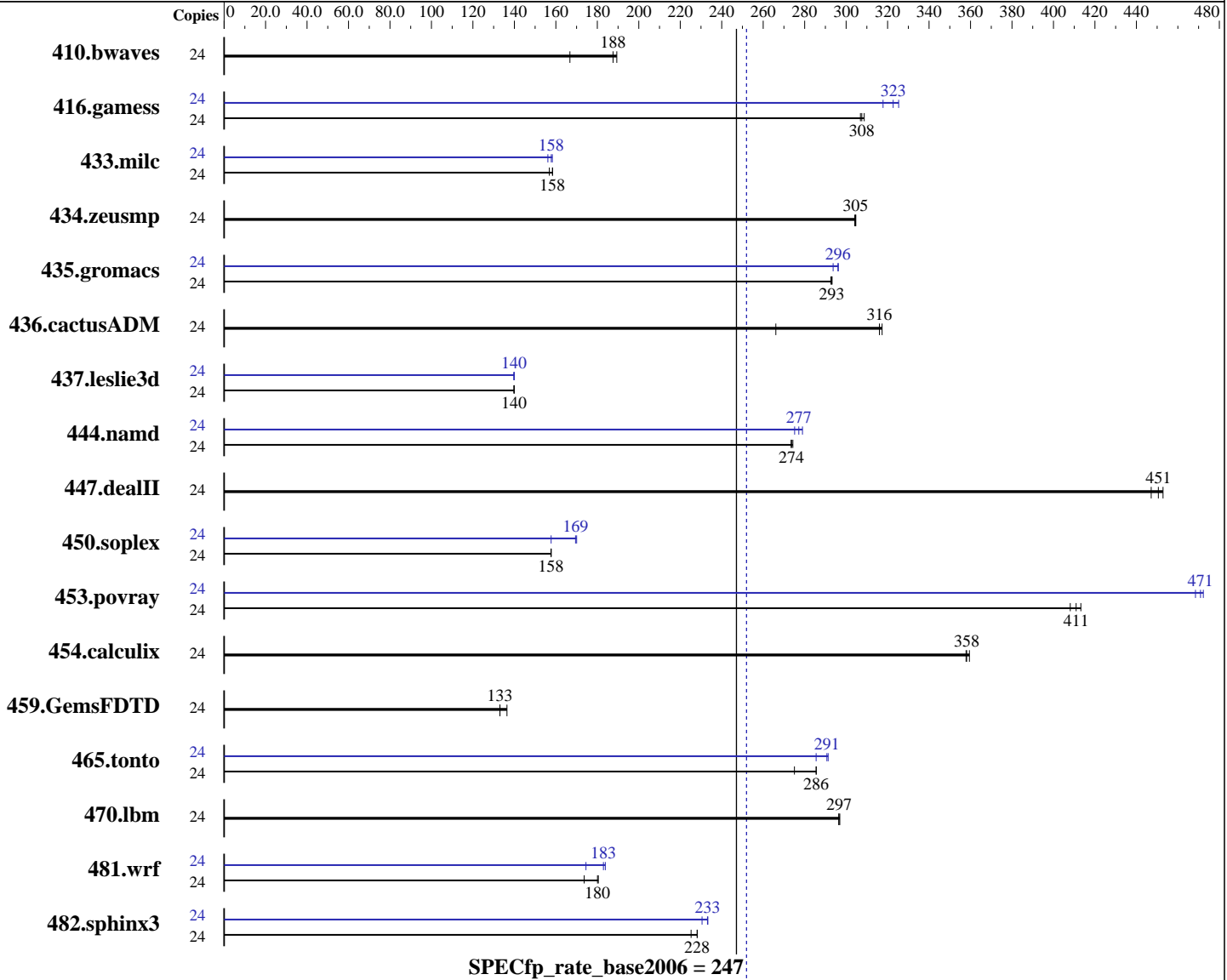
Test date: Oct-2011

Test sponsor: Hewlett-Packard Company

Hardware Availability: Feb-2011

Tested by: Hewlett-Packard Company

Software Availability: Sep-2011



Hardware

CPU Name: Intel Xeon X5675
 CPU Characteristics: Intel Turbo Boost Technology up to 3.46 GHz
 CPU MHz: 3067
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.1, Kernel 2.6.32-131.0.15.el6.x86_64
 Compiler: C/C++/Fortran: Version 12.1.0.225 of Intel Compiler XE Build 20110803
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit

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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem: 1 x 146 GB 15 K SAS
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	1722	189	<u>1739</u>	<u>188</u>	1956	167	24	1722	189	<u>1739</u>	<u>188</u>	1956	167
416.gamess	24	1522	309	<u>1528</u>	<u>308</u>	1531	307	24	1444	325	<u>1456</u>	<u>323</u>	1479	318
433.milc	24	1405	157	1391	158	<u>1391</u>	<u>158</u>	24	1411	156	<u>1396</u>	<u>158</u>	1392	158
434.zeusmp	24	717	305	<u>717</u>	<u>305</u>	718	304	24	717	305	<u>717</u>	<u>305</u>	718	304
435.gromacs	24	584	293	<u>585</u>	<u>293</u>	586	293	24	578	296	<u>579</u>	<u>296</u>	583	294
436.cactusADM	24	1078	266	904	317	<u>907</u>	<u>316</u>	24	1078	266	904	317	<u>907</u>	<u>316</u>
437.leslie3d	24	1612	140	1614	140	<u>1613</u>	<u>140</u>	24	1612	140	<u>1613</u>	<u>140</u>	1615	140
444.namd	24	704	273	<u>703</u>	<u>274</u>	702	274	24	700	275	<u>694</u>	<u>277</u>	690	279
447.dealII	24	<u>609</u>	<u>451</u>	614	447	606	453	24	<u>609</u>	<u>451</u>	614	447	606	453
450.soplex	24	1269	158	1270	158	<u>1270</u>	<u>158</u>	24	1269	158	1177	170	<u>1181</u>	<u>169</u>
453.povray	24	<u>311</u>	<u>411</u>	309	413	313	408	24	273	468	270	472	<u>271</u>	<u>471</u>
454.calculix	24	551	359	553	358	<u>553</u>	<u>358</u>	24	551	359	553	358	<u>553</u>	<u>358</u>
459.GemsFDTD	24	1867	136	<u>1914</u>	<u>133</u>	1914	133	24	1867	136	<u>1914</u>	<u>133</u>	1914	133
465.tonto	24	859	275	<u>827</u>	<u>286</u>	827	286	24	827	286	<u>812</u>	<u>291</u>	811	291
470.lbm	24	1110	297	<u>1112</u>	<u>297</u>	1112	296	24	1110	297	<u>1112</u>	<u>297</u>	1112	296
481.wrf	24	1544	174	1485	180	<u>1489</u>	<u>180</u>	24	1536	175	<u>1465</u>	<u>183</u>	1458	184
482.sphinx3	24	2077	225	2050	228	<u>2051</u>	<u>228</u>	24	2029	231	<u>2006</u>	<u>233</u>	2005	233

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages disabled with:
echo never > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>



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Platform Notes

BIOS configuration:
HP Power Profile set to Maximum Performance
Thermal Configuration set to Increased Cooling
Data Reuse set to Disabled

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006/smartheap:/cpu2006/ic12.1-libs/ia32:/cpu2006/ic12.1-libs/intel64"

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

434.zeusmp: -DSPEC_CPU_LP64

435.gromacs: -DSPEC_CPU_LP64 -nofor_main

436.cactusADM: -DSPEC_CPU_LP64 -nofor_main

437.leslie3d: -DSPEC_CPU_LP64

444.namd: -DSPEC_CPU_LP64

447.deallI: -DSPEC_CPU_LP64

453.povray: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
 -prof-use(pass 2) -static -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
 -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
 -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealIII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
 -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
 -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

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Peak Optimization Flags (Continued)

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -static -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20111122.html>
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20111122.xml>
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.xml>

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