



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

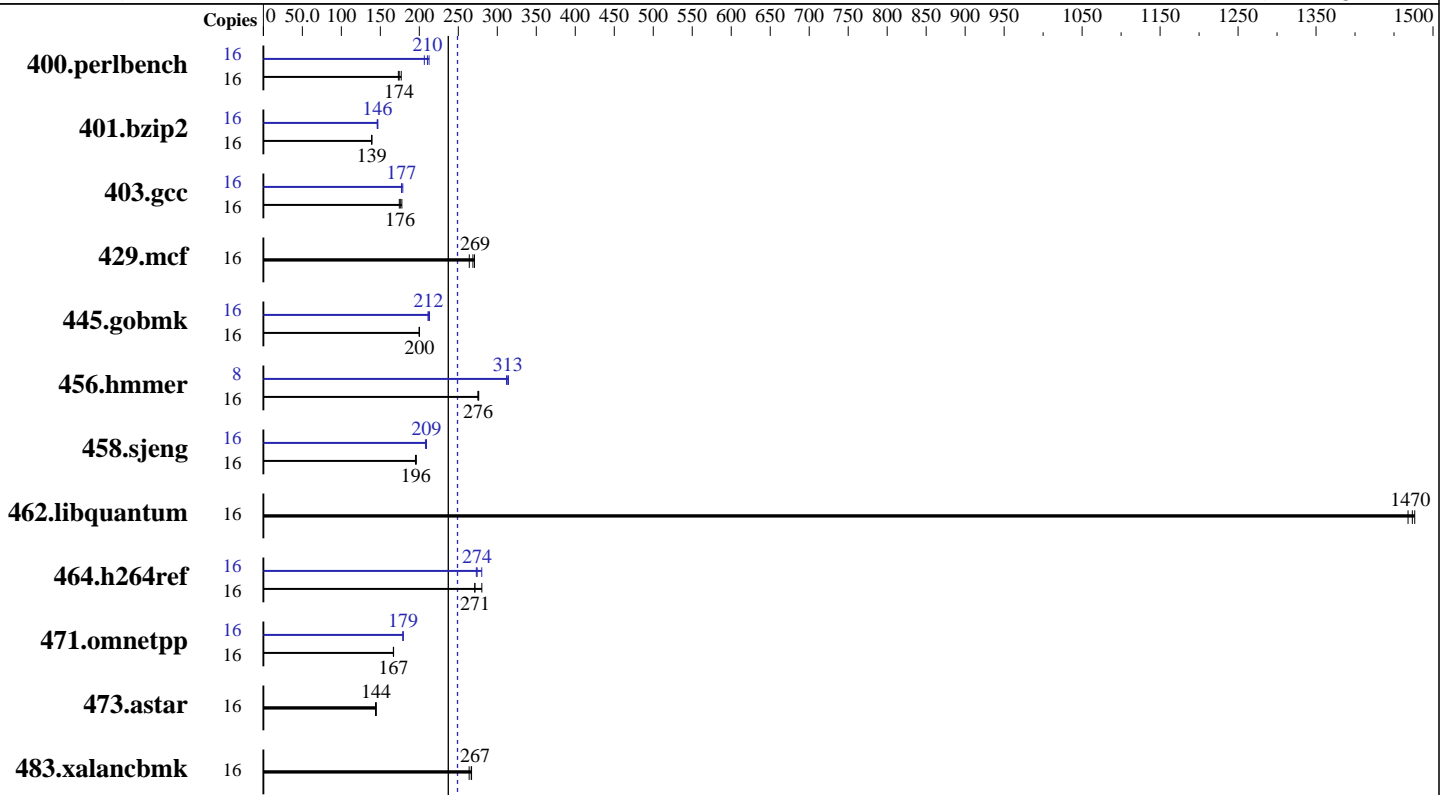
SPECint®\_rate2006 = 249

Servidor Itautec MX223+ (Intel Xeon E5630)

SPECint\_rate\_base2006 = 237

CPU2006 license: 9001  
Test sponsor: Itautec  
Tested by: Itautec

Test date: Nov-2011  
Hardware Availability: Jul-2011  
Software Availability: Aug-2011



SPECint\_rate2006 = 249

SPECint\_rate\_base2006 = 237

## Hardware

CPU Name: Intel Xeon E5630  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
 CPU MHz: 2533  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)  
 Disk Subsystem: 1 x 500 GB SAS, 15000 RPM  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86\_64), Kernel 2.6.32.12-0.7-default  
 Compiler: C/C++: Version 12.1.0 of Intel Compiler XE Build 20110811  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint\_rate2006 = 249

Servidor Itautec MX223+ (Intel Xeon E5630)

SPECint\_rate\_base2006 = 237

CPU2006 license: 9001  
Test sponsor: Itautec  
Tested by: Itautec

Test date: Nov-2011  
Hardware Availability: Jul-2011  
Software Availability: Aug-2011

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	903	173	884	177	<b>897</b>	<b>174</b>	16	<b>743</b>	<b>210</b>	736	212	757	206
401.bzip2	16	1112	139	<b>1111</b>	<b>139</b>	1110	139	16	1052	147	<b>1054</b>	<b>146</b>	1058	146
403.gcc	16	739	174	<b>733</b>	<b>176</b>	725	178	16	<b>727</b>	<b>177</b>	720	179	727	177
429.mcf	16	553	264	539	271	<b>543</b>	<b>269</b>	16	553	264	539	271	<b>543</b>	<b>269</b>
445.gobmk	16	838	200	<b>839</b>	<b>200</b>	840	200	16	788	213	<b>793</b>	<b>212</b>	794	211
456.hammer	16	<b>542</b>	<b>276</b>	541	276	542	275	8	<b>238</b>	<b>313</b>	239	312	237	314
458.sjeng	16	987	196	993	195	<b>990</b>	<b>196</b>	16	<b>926</b>	<b>209</b>	931	208	926	209
462.libquantum	16	225	1480	<b>225</b>	<b>1470</b>	226	1470	16	225	1480	<b>225</b>	<b>1470</b>	226	1470
464.h264ref	16	1264	280	<b>1305</b>	<b>271</b>	1306	271	16	<b>1291</b>	<b>274</b>	1264	280	1297	273
471.omnetpp	16	599	167	<b>600</b>	<b>167</b>	600	167	16	558	179	<b>558</b>	<b>179</b>	559	179
473.astar	16	776	145	781	144	<b>778</b>	<b>144</b>	16	776	145	781	144	<b>778</b>	<b>144</b>
483.xalancbmk	16	414	267	418	264	<b>414</b>	<b>267</b>	16	414	267	418	264	<b>414</b>	<b>267</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.  
Large pages were not enabled for this run

## Platform Notes

Data Reuse disabled in BIOS.

## General Notes

This result was measured on the Servidor Itautec MX224.  
The Servidor Itautec MX203+, Servidor Itautec MX223+ and the Servidor Itautec MX224 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc -m32

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint\_rate2006 = 249

Servidor Itaotec MX223+ (Intel Xeon E5630)

SPECint\_rate\_base2006 = 237

CPU2006 license: 9001  
Test sponsor: Itaotec  
Tested by: Itaotec

Test date: Nov-2011  
Hardware Availability: Jul-2011  
Software Availability: Aug-2011

## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/rcaneca/sh/SmartHeap\_8.1/lib -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:  
icpc -m32



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint\_rate2006 = 249

Servidor Itaotec MX223+ (Intel Xeon E5630)

SPECint\_rate\_base2006 = 237

CPU2006 license: 9001  
Test sponsor: Itaotec  
Tested by: Itaotec

Test date: Nov-2011  
Hardware Availability: Jul-2011  
Software Availability: Aug-2011

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/rcaneca/sh/SmartHeap\_8.1/lib -lsmartheap

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint\_rate2006 = 249

Servidor Itautec MX223+ (Intel Xeon E5630)

SPECint\_rate\_base2006 = 237

CPU2006 license: 9001  
Test sponsor: Itautec  
Tested by: Itautec

Test date: Nov-2011  
Hardware Availability: Jul-2011  
Software Availability: Aug-2011

## Peak Optimization Flags (Continued)

473.astar: basepeak = yes  
483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-Intel-Linux64-Platform.html>  
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Itautec-Intel-Linux64-Platform.xml>  
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.  
Report generated on Thu Jul 24 00:43:24 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 20 December 2011.