



SPEC® CFP2006 Result

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NEC Corporation

SPECfp®2006 = 78.9

Express5800/T120d (Intel Xeon E5-2470)

SPECfp_base2006 = 75.6

CPU2006 license: 9006

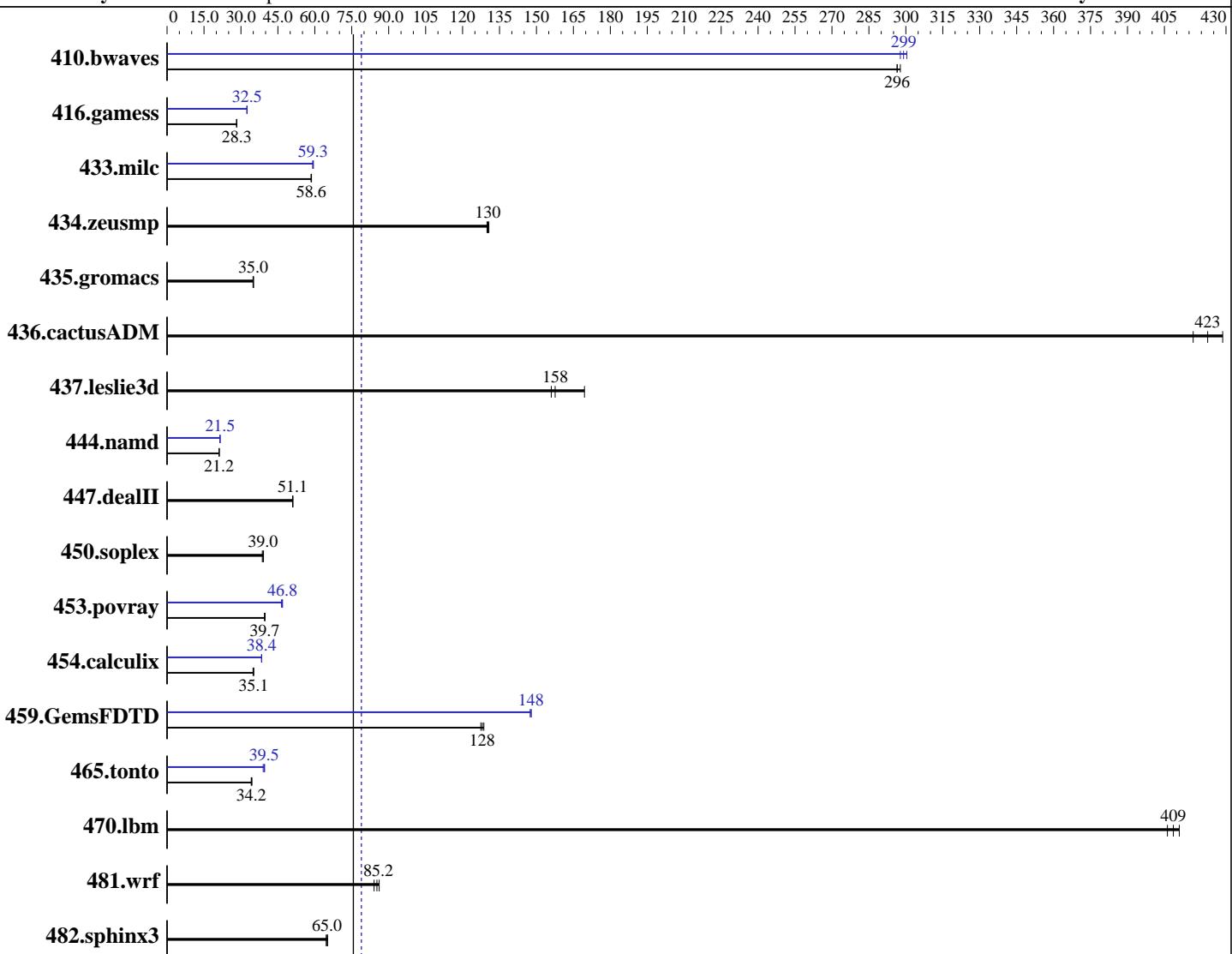
Test date: May-2012

Test sponsor: NEC Corporation

Hardware Availability: May-2012

Tested by: NEC Corporation

Software Availability: Dec-2011



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Hardware

CPU Name: Intel Xeon E5-2470
CPU Characteristics: Intel Turbo Boost Technology up to 3.10 GHz
CPU MHz: 2300
FPU: Integrated
CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)
Compiler: Kernel 2.6.32-220.el6.x86_64
C/C++: Version 12.1.2.273 of Intel C++ Studio XE for Linux;
Fortran: Version 12.1.2.273 of Intel Fortran Studio XE for Linux
Auto Parallel: Yes
File System: ext4

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L3 Cache: 20 MB I+D on chip per chip
 Other Cache: None
 Memory: 96 GB (12 x 8 GB 2Rx4 PC3L-12800R-11, ECC)
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	45.8	296	45.8	296	45.6	298	45.4	299	45.6	298	45.2	300
416.gamess	691	28.3	694	28.2	692	28.3	603	32.5	604	32.4	603	32.5
433.milc	157	58.5	157	58.6	157	58.7	154	59.4	155	59.1	155	59.3
434.zeusmp	69.8	130	70.0	130	69.6	131	69.8	130	70.0	130	69.6	131
435.gromacs	205	34.9	203	35.1	204	35.0	205	34.9	203	35.1	204	35.0
436.cactusADM	28.7	417	28.3	423	27.9	429	28.7	417	28.3	423	27.9	429
437.leslie3d	59.6	158	60.3	156	55.5	170	59.6	158	60.3	156	55.5	170
444.namd	379	21.2	379	21.2	379	21.1	373	21.5	373	21.5	373	21.5
447.dealII	224	51.1	224	51.1	224	51.0	224	51.1	224	51.1	224	51.0
450.soplex	213	39.2	214	39.0	216	38.7	213	39.2	214	39.0	216	38.7
453.povray	134	39.8	134	39.6	134	39.7	114	46.8	114	46.9	115	46.4
454.calculix	236	34.9	235	35.2	235	35.1	215	38.4	215	38.4	215	38.4
459.GemsFDTD	82.9	128	83.3	127	82.5	129	71.8	148	72.0	147	71.8	148
465.tonto	288	34.2	285	34.5	288	34.2	248	39.6	252	39.0	249	39.5
470.lbm	33.4	411	33.8	406	33.6	409	33.4	411	33.8	406	33.6	409
481.wrf	131	85.2	133	84.1	130	86.1	131	85.2	133	84.1	130	86.1
482.sphinx3	299	65.2	302	64.6	300	65.0	299	65.2	302	64.6	300	65.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:

Energy Performance: Performance

Memory Voltage: 1.5 V

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64"

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General Notes (Continued)

OMP_NUM_THREADS = "16"

Added glibc-static-2.12-1.47.el6.x86_64.rpm
to enable static linking

Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

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Base Optimization Flags (Continued)

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

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Peak Optimization Flags (Continued)

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll14 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll12
-inline-level=0 -scalar-rep -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll12
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll14

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>
<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120d-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>
<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120d-RevA.xml>



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For other inquiries, please contact webmaster@spec.org.

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