



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®_rate2006 = 527

ProLiant BL465c Gen8
(2.30 GHz AMD Opteron 6376)

SPECint_rate_base2006 = 459

CPU2006 license: 3

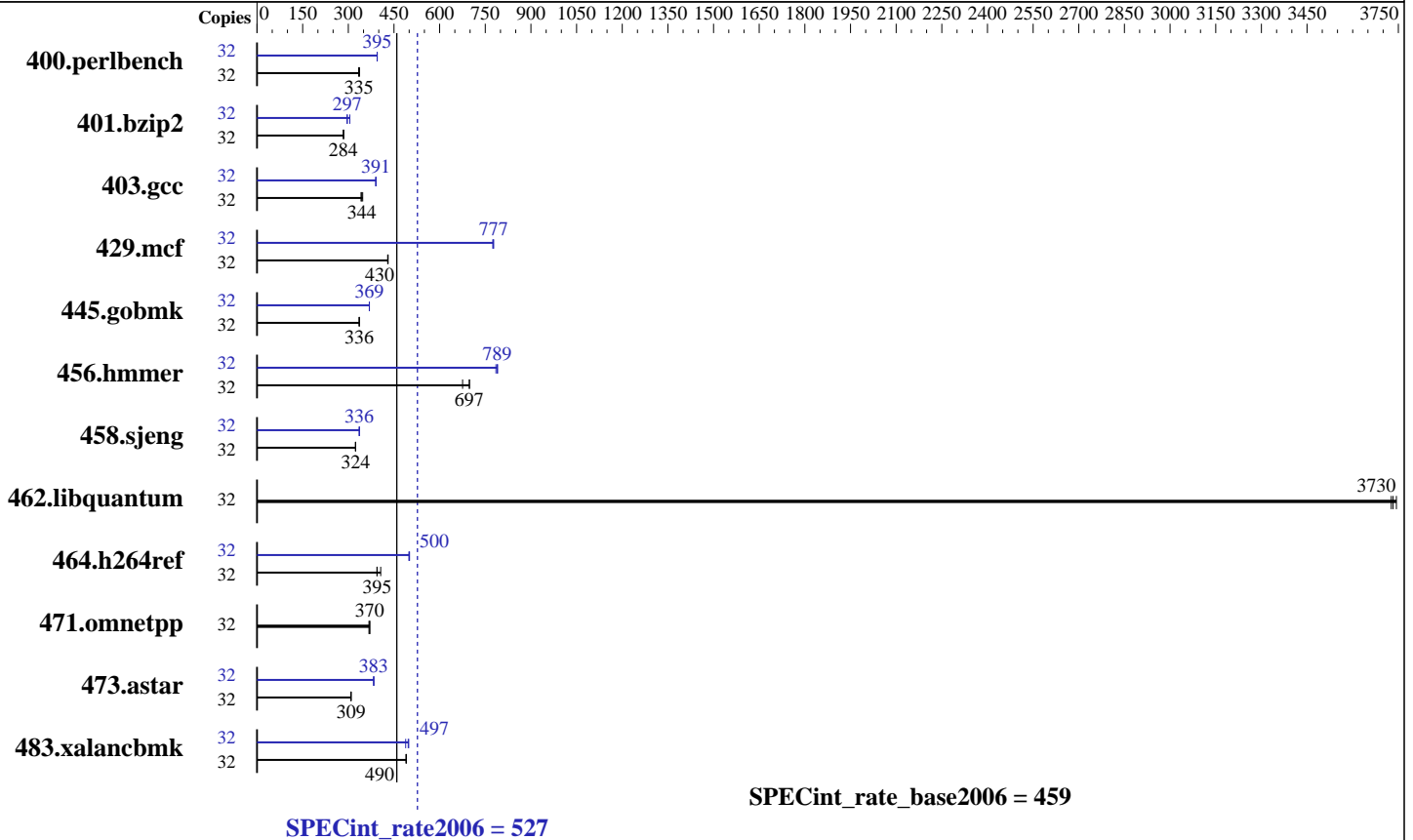
Test date: Nov-2012

Test sponsor: Hewlett-Packard Company

Hardware Availability: Nov-2012

Tested by: Hewlett-Packard Company

Software Availability: Aug-2012



Hardware

CPU Name: AMD Opteron 6376
 CPU Characteristics: AMD Turbo CORE technology up to 3.20 GHz
 CPU MHz: 2300
 FPU: Integrated
 CPU(s) enabled: 32 cores, 2 chips, 16 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 512 KB I on chip per chip,
64 KB I shared / 2 cores;
16 KB D on chip per core
 Secondary Cache: 16 MB I+D on chip per chip, 2 MB shared / 2 cores
 L3 Cache: 16 MB I+D on chip per chip, 8 MB shared / 8 cores
 Other Cache: None
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)
 Disk Subsystem: 2 x 200 GB SSD SAS, RAID 1
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.3,
Kernel 2.6.32-279.el6.x86_64
 Compiler: C/C++: Version 4.5.2 of
x86 Open64 Compiler Suite (from AMD)
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap 10.0 32-bit Library for Linux



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	<u>932</u>	<u>335</u>	936	334	928	337	32	791	395	792	395	<u>792</u>	<u>395</u>
401.bzip2	32	<u>1087</u>	<u>284</u>	1088	284	1086	284	32	1013	305	1044	296	<u>1040</u>	<u>297</u>
403.gcc	32	<u>749</u>	<u>344</u>	742	347	754	342	32	660	390	<u>659</u>	<u>391</u>	659	391
429.mcf	32	677	431	<u>679</u>	<u>430</u>	679	430	32	<u>376</u>	<u>777</u>	377	775	376	777
445.gobmk	32	<u>999</u>	<u>336</u>	1000	336	999	336	32	910	369	<u>909</u>	<u>369</u>	909	369
456.hammer	32	442	675	428	698	<u>428</u>	<u>697</u>	32	380	785	<u>378</u>	<u>789</u>	378	791
458.sjeng	32	<u>1196</u>	<u>324</u>	1197	324	1195	324	32	1153	336	1151	336	<u>1151</u>	<u>336</u>
462.libquantum	32	178	3730	177	3740	<u>178</u>	<u>3730</u>	32	178	3730	177	3740	<u>178</u>	<u>3730</u>
464.h264ref	32	1742	406	<u>1795</u>	<u>395</u>	1797	394	32	1419	499	1413	501	<u>1415</u>	<u>500</u>
471.omnetpp	32	540	370	544	368	<u>540</u>	<u>370</u>	32	540	370	544	368	<u>540</u>	<u>370</u>
473.astar	32	726	309	<u>727</u>	<u>309</u>	729	308	32	586	383	<u>586</u>	<u>383</u>	586	384
483.xalancbmk	32	<u>451</u>	<u>490</u>	450	491	451	490	32	452	488	443	499	<u>444</u>	<u>497</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit

Set transparent_hugepage=never as a boot parameter in /boot/grub/menu.lst

Set vm/nr_hugepages=28672 in /etc/sysctl.conf
mount -t hugetlbfs nodev /mnt/hugepages

Platform Notes

BIOS configuration:
HP Power Profile set to Maximum Performance
Thermal Configuration set to Maximum Cooling
Collaborative Power Control set to Disable
Minimum Processor Idle Power C1e State to Enabled
Processor Power and Utilization Monitoring set to Disable



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General Notes

Environment variables set by runspec before the start of the run:

HUGETLB_LIMIT = "896"

LD_LIBRARY_PATH = "/cpu2006/amd1206-rate-libs-revA/32:/cpu2006/amd1206-rate-libs-revA/64"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at <http://developer.amd.com/cpu/open64>

Binaries were compiled on a system with 2x AMD Opteron 6386SE chips + 128GB Memory using RHEL 6.3

Base Compiler Invocation

C benchmarks:

openc

C++ benchmarks:

openCC

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-Ofast -CG:local_sched_alg=1 -INLINE:aggressive=ON -IPA:plimit=8000
-IPA:small_pu=100 -HP:bd=2m:heap=2m -mso -LNO:prefetch=2
-march=bdver1

C++ benchmarks:

-Ofast -m32 -INLINE:aggressive=on -CG:cmp_peep=on -D__OPEN64_FAST_SET
-march=bdver1 -L/root/work/libraries/SmartHeap-10/lib -lsmartheap



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Peak Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-LNO:prefetch=2 -LNO:opt=0 -IPA:plimit=20000
-OPT:unroll_times_max=8 -OPT:unroll_size=256
-OPT:unroll_level=2 -OPT:keep_ext=on -WOPT:if_conv=0
-WOPT:sib=on -CG:local_sched_alg=1 -CG:unroll_fb_req=on
-CG:movext_icmp=off -HP:bd=2m:heap=2m -march=bdver1
-GRA:aggr_loop_splitting=off -GRA:loop_splitting=off

401.bzip2: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -O3
-LNO:prefetch=2 -LNO:pf2=0 -OPT:alias=disjoint
-OPT:goto=off -CG:local_sched_alg=1 -HP:bd=2m:heap=2m
-march=bdver2

403.gcc: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-LNO:trip_count=256 -CG:cmp_peep=on -CG:pre_minreg_level=2
-m32 -HP:bd=2m:heap=2m -GRA:unspill=on -IPA:small_pu=200
-WOPT:sib=on -march=bdver2 -mno-fma4

429.mcf: -O3 -OPT:unroll_times_max=5 -ipa -INLINE:aggressive=on
-CG:gcm=off -CG:dsched=on -GRA:prioritize_by_density=on
-m32 -HP:bd=2m:heap=2m -mso -march=bdver1

445.gobmk: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-OPT:unroll_size=256 -OPT:unroll_times_max=8
-OPT:keep_ext=on -IPA:plimit=750 -IPA:min_hotness=300
```

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Peak Optimization Flags (Continued)

445.gobmk (continued):

-IPA:pu_reorder=1 -LNO:ignore_feedback=off -WOPT:if_conv=2
-HP:bd=2m:heap=2m -march=bdver1

456.hmmr: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast

-LNO:prefetch=2 -OPT:alias=disjoint
-OPT:unroll_times_max=16 -OPT:unroll_size=512
-OPT:unroll_level=2 -OPT:keep_ext=on -CG:cflow=0
-CG:cmp_peep=on -CG:pre_local_sched=off -HP:bd=2m:heap=2m
-CG:p2align=0 -CG:load_exe=3 -CG:dsched=on -march=bdver1

458.sjeng: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast

-CG:ptr_load_use=0 -CG:divrem_opt=on -CG:movext_icmp=off
-CG:locs_best=on -LNO:full_unroll=10 -IPA:pu_reorder=2
-HP:heap=2m:bd=2m -WOPT:sib=on -march=bdver1

462.libquantum: basepeak = yes

464.h264ref: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -O3

-OPT:unroll_size=256 -OPT:unroll_times_max=2
-IPA:plimit=20000 -OPT:alias=disjoint -CG:ptr_load_use=0
-CG:local_sched_alg=1 -HP:bd=2m:heap=2m -march=bdver1

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast

-WOPT:if_conv=0 -WOPT:sib=on -CG:divrem_opt=on
-CG:p2align=1 -CG:dsched=on -GRA:optimize_boundary=on
-OPT:alias=disjoint -INLINE:aggressive=on
-IPA:small_pu=3000 -IPA:plimit=3000 -HP:bd=2m:heap=2m
-march=bdver1

483.xalancbmk: -Ofast -LNO:prefetch=2 -OPT:unroll_size=512

-OPT:unroll_times_max=8 -D_OPEN64_FAST_SET
-INLINE:aggressive=on -m32 -CG:cmp_peep=on
-CG:local_sched=off -CG:p2align=1 -GRA:unspill=on
-TENV:frame_pointer=off -fno-emit-exceptions -march=bdver2
-mno-fma4
-L/root/work/libraries/SmartHeap-10/lib -lsmarheap

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-AMD-V1.2-A.20121205.html>

<http://www.spec.org/cpu2006/flags/x86-open64-452-flags-rate-revA-II.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-AMD-V1.2-A.20121205.xml>

<http://www.spec.org/cpu2006/flags/x86-open64-452-flags-rate-revA-II.xml>



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