



SPEC® CFP2006 Result

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IBM Corporation

IBM Flex System x222
(Intel Xeon E5-2430L, 2.00 GHz)

SPECfp®2006 = 61.8

SPECfp_base2006 = 58.8

CPU2006 license: 11

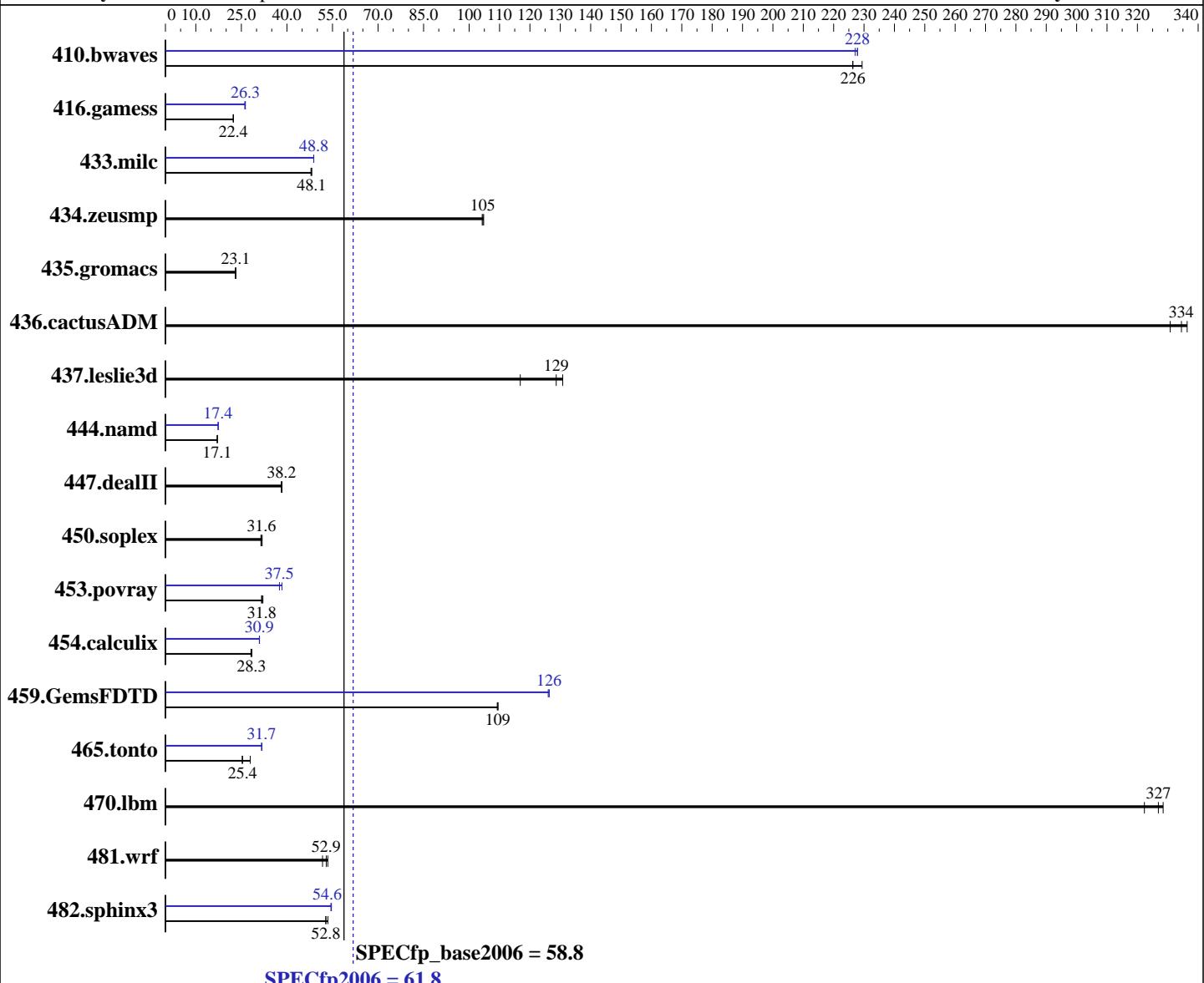
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jun-2013

Hardware Availability: Sep-2013

Software Availability: Dec-2011



Hardware

CPU Name: Intel Xeon E5-2430L
CPU Characteristics: Intel Turbo Boost Technology up to 2.50 GHz
CPU MHz: 2000
FPU: Integrated
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)
Compiler: 2.6.32-220.el6.x86_64
C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;
Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
Auto Parallel: Yes
File System: ext4

Continued on next page

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L3 Cache: 15 MB I+D on chip per chip
Other Cache: None
Memory: 96 GB (12 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz)
Disk Subsystem: 1 x 100 GB SATA, SSD
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---------------|-------------|-------------|-------------|-------------|------------|-------------|-------------|-------------|-------------|-------------|------------|-------------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 60.1 | 226 | 60.1 | 226 | 59.3 | 229 | 59.9 | 227 | 59.7 | 228 | 59.7 | 228 |
| 416.gamess | 876 | 22.4 | 876 | 22.4 | 876 | 22.3 | 744 | 26.3 | 745 | 26.3 | 750 | 26.1 |
| 433.milc | 191 | 48.1 | 191 | 48.1 | 192 | 47.9 | 188 | 48.8 | 188 | 48.8 | 188 | 48.8 |
| 434.zeusmp | 86.9 | 105 | 87.1 | 105 | 87.3 | 104 | 86.9 | 105 | 87.1 | 105 | 87.3 | 104 |
| 435.gromacs | 310 | 23.0 | 309 | 23.1 | 308 | 23.2 | 310 | 23.0 | 309 | 23.1 | 308 | 23.2 |
| 436.cactusADM | 35.5 | 336 | 35.7 | 334 | 36.1 | 331 | 35.5 | 336 | 35.7 | 334 | 36.1 | 331 |
| 437.leslie3d | 73.1 | 129 | 71.9 | 131 | 80.5 | 117 | 73.1 | 129 | 71.9 | 131 | 80.5 | 117 |
| 444.namd | 470 | 17.1 | 469 | 17.1 | 470 | 17.1 | 462 | 17.4 | 461 | 17.4 | 462 | 17.4 |
| 447.dealII | 298 | 38.4 | 299 | 38.2 | 299 | 38.2 | 298 | 38.4 | 299 | 38.2 | 299 | 38.2 |
| 450.soplex | 262 | 31.8 | 265 | 31.4 | 264 | 31.6 | 262 | 31.8 | 265 | 31.4 | 264 | 31.6 |
| 453.povray | 166 | 32.1 | 167 | 31.8 | 168 | 31.6 | 142 | 37.5 | 142 | 37.5 | 139 | 38.4 |
| 454.calculix | 290 | 28.5 | 291 | 28.3 | 293 | 28.2 | 266 | 31.0 | 267 | 30.9 | 267 | 30.9 |
| 459.GemsFDTD | 96.9 | 109 | 96.9 | 109 | 97.1 | 109 | 84.2 | 126 | 84.0 | 126 | 84.0 | 126 |
| 465.tonto | 352 | 27.9 | 390 | 25.3 | 387 | 25.4 | 311 | 31.7 | 310 | 31.8 | 311 | 31.7 |
| 470.lbm | 41.8 | 328 | 42.0 | 327 | 42.6 | 322 | 41.8 | 328 | 42.0 | 327 | 42.6 | 322 |
| 481.wrf | 209 | 53.5 | 216 | 51.7 | 211 | 52.9 | 209 | 53.5 | 216 | 51.7 | 211 | 52.9 |
| 482.sphinx3 | 364 | 53.5 | 369 | 52.8 | 369 | 52.8 | 357 | 54.7 | 358 | 54.5 | 357 | 54.6 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

Operating Mode set to Maximum Performance in BIOS
Sysinfo program /cpu2006.1.2/config/sysinfo.rev6800
\$Rev: 6800 \$ \$Date:: 2011-10-11 ## 6f2ebdff5032aaa42e583f96b07f99d3
running on Caraspeccpu Sat Jun 29 15:25:03 2013

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

Continued on next page



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Platform Notes (Continued)

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2430L 0 @ 2.00GHz
        2 "physical id"s (chips)
        24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 6
    siblings : 12
    physical 0: cores 0 1 2 3 4 5
    physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB
```

```
From /proc/meminfo
MemTotal:      99037652 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.2 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux Caraspeccpu 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jun 27 10:58
```

```
SPEC is set to: /cpu2006.1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/vg_caraspeccpu-lv_root
                ext4   82G   11G   68G  13%  /
```

Additional information from dmidecode:

```
Memory:
12x Micron 36JSF1G72PZ-1G6M1 8 GB 1600 MHz 2 rank
```

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/cpu2006.1.2/lib32:/cpu2006.1.2/lib64"

OMP_NUM_THREADS = "12"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB

Continued on next page



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General Notes (Continued)

memory using RHEL5.5

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

Continued on next page



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Base Optimization Flags (Continued)

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
```

Fortran benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias
```

```
470.lbm: basepeak = yes
```

```
482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel
```

C++ benchmarks:

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Peak Optimization Flags (Continued)

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll14 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll12
-inline-level=0 -scalar-rep -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll12
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll14

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.xml>



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Tested with SPEC CPU2006 v1.2.

Report generated on Thu Jul 24 17:43:13 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 19 November 2013.