



SPEC[®] CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

SPECint[®]_rate2006 = 69.4

Express5800/T110f-E (Intel Celeron G1820)

SPECint_rate_base2006 = 67.0

CPU2006 license: 9006

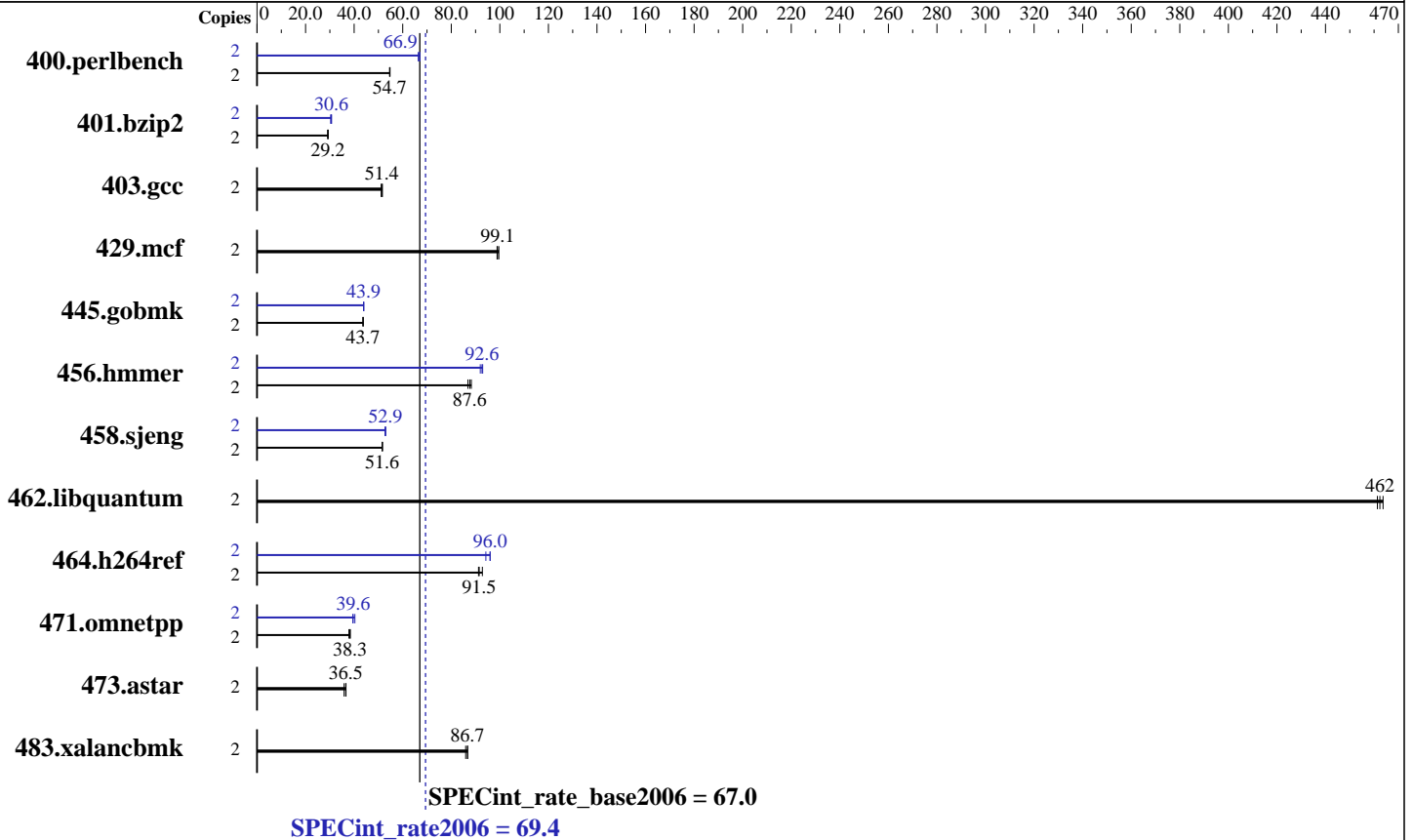
Test date: Jan-2014

Test sponsor: NEC Corporation

Hardware Availability: Jan-2014

Tested by: NEC Corporation

Software Availability: Sep-2013



Hardware

CPU Name: Intel Celeron G1820
 CPU Characteristics: 2700
 CPU MHz: Integrated
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 2 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (2 x 8 GB 2Rx8 PC3L-12800E-11, ECC, running at 1333 MHz and CL9)
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)
 Kernel 2.6.32-358.18.1.el6.x86_64
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

SPECint_rate2006 = 69.4

Express5800/T110f-E (Intel Celeron G1820)

SPECint_rate_base2006 = 67.0

CPU2006 license: 9006

Test date: Jan-2014

Test sponsor: NEC Corporation

Hardware Availability: Jan-2014

Tested by: NEC Corporation

Software Availability: Sep-2013

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	358	54.7	358	54.6	357	54.8	2	292	66.9	291	67.0	294	66.4
401.bzip2	2	663	29.1	660	29.2	659	29.3	2	629	30.7	637	30.3	630	30.6
403.gcc	2	312	51.6	313	51.4	315	51.1	2	312	51.6	313	51.4	315	51.1
429.mcf	2	185	98.9	183	99.6	184	99.1	2	185	98.9	183	99.6	184	99.1
445.gobmk	2	480	43.7	481	43.6	480	43.7	2	478	43.9	478	43.9	477	44.0
456.hammer	2	213	87.6	215	86.9	211	88.3	2	201	92.9	201	92.6	203	91.9
458.sjeng	2	469	51.6	469	51.6	469	51.6	2	457	52.9	457	53.0	459	52.7
462.libquantum	2	89.6	462	89.8	461	89.4	464	2	89.6	462	89.8	461	89.4	464
464.h264ref	2	484	91.5	477	92.8	485	91.2	2	461	96.0	470	94.2	461	96.0
471.omnetpp	2	330	37.8	325	38.4	327	38.3	2	310	40.3	316	39.6	316	39.5
473.astar	2	384	36.5	383	36.6	392	35.9	2	384	36.5	383	36.6	392	35.9
483.xalancbmk	2	159	86.7	161	85.9	159	86.7	2	159	86.7	161	85.9	159	86.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:
Energy Performance: Performance

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

SPECint_rate2006 = 69.4

Express5800/T110f-E (Intel Celeron G1820)

SPECint_rate_base2006 = 67.0

CPU2006 license: 9006

Test date: Jan-2014

Test sponsor: NEC Corporation

Hardware Availability: Jan-2014

Tested by: NEC Corporation

Software Availability: Sep-2013

Base Compiler Invocation

C benchmarks:

icc -m32

C++ benchmarks:

icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

SPECint_rate2006 = 69.4

Express5800/T110f-E (Intel Celeron G1820)

SPECint_rate_base2006 = 67.0

CPU2006 license: 9006

Test date: Jan-2014

Test sponsor: NEC Corporation

Hardware Availability: Jan-2014

Tested by: NEC Corporation

Software Availability: Sep-2013

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
 401.bzip2: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LINUX
 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
 -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
 -L/sh -lsmartheap

473.astar: basepeak = yes

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

SPECint_rate2006 = 69.4

Express5800/T110f-E (Intel Celeron G1820)

SPECint_rate_base2006 = 67.0

CPU2006 license: 9006

Test date: Jan-2014

Test sponsor: NEC Corporation

Hardware Availability: Jan-2014

Tested by: NEC Corporation

Software Availability: Sep-2013

Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120-RevB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120-RevB.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 19:59:26 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 25 February 2014.