



SPEC® CFP2006 Result

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NEC Corporation

SPECfp®2006 = 75.5

Express5800/R120e-2E (Intel Xeon E5-2430L v2)

SPECfp_base2006 = 72.9

CPU2006 license: 9006

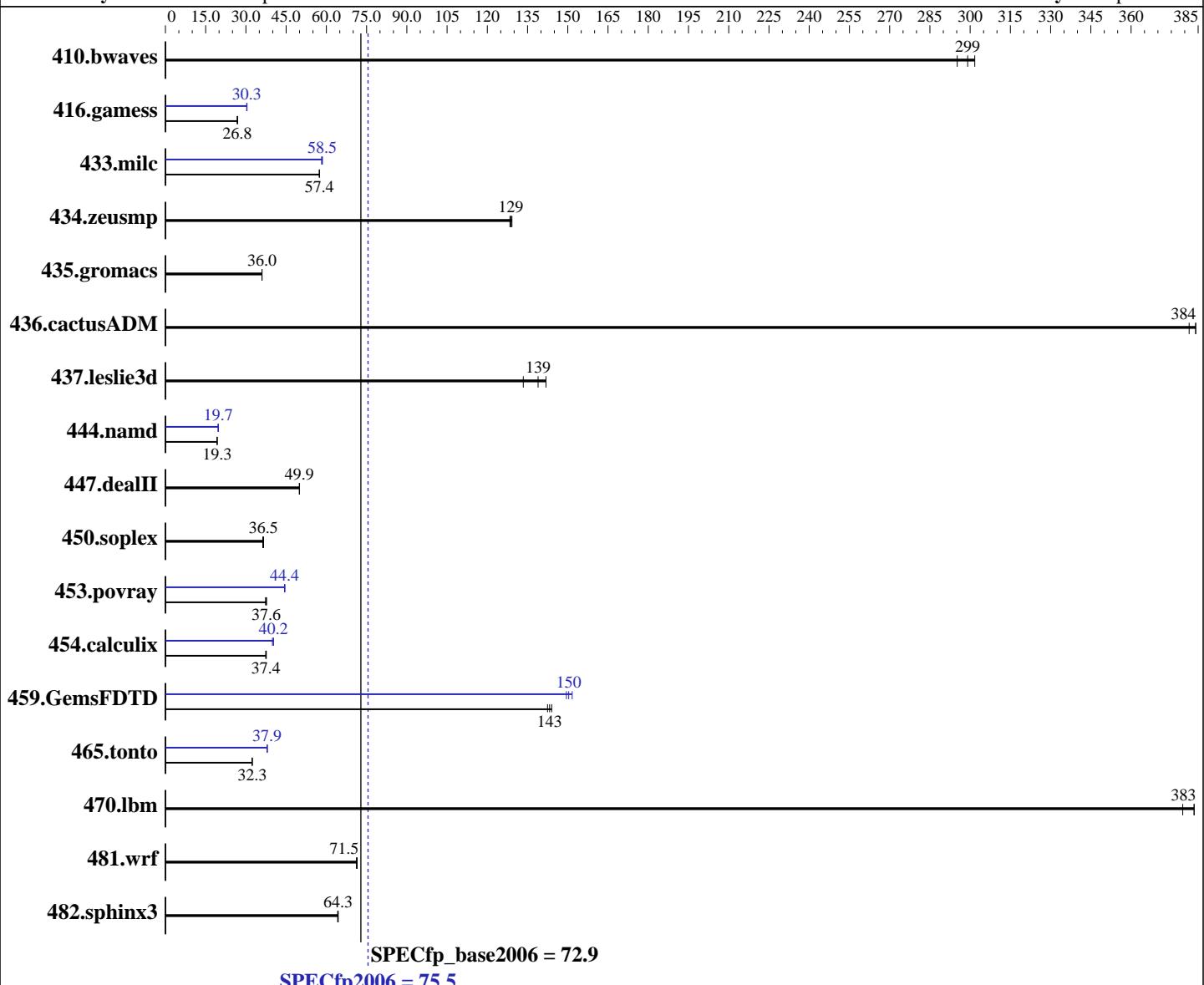
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Feb-2014

Hardware Availability: Jan-2014

Software Availability: Sep-2013



Hardware

CPU Name: Intel Xeon E5-2430L v2
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)
 Compiler: Kernel 2.6.32-358.18.1.el6.x86_64
 C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4

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L3 Cache: 15 MB I+D on chip per chip
 Other Cache: None
 Memory: 192 GB (12 x 16 GB 2Rx4 PC3L-12800R-11, ECC)
 Disk Subsystem: 1 x 250 GB SATA, 7200 RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|
| | Seconds | Ratio |
| 410.bwaves | 45.4 | 299 | 45.0 | 302 | 46.0 | 295 | 45.4 | 299 | 45.0 | 302 | 46.0 | 295 |
| 416.gamess | 727 | 26.9 | 732 | 26.8 | 731 | 26.8 | 645 | 30.3 | 644 | 30.4 | 646 | 30.3 |
| 433.milc | 160 | 57.4 | 160 | 57.4 | 160 | 57.4 | 157 | 58.5 | 157 | 58.5 | 158 | 58.2 |
| 434.zeusmp | 70.4 | 129 | 70.9 | 128 | 70.7 | 129 | 70.4 | 129 | 70.9 | 128 | 70.7 | 129 |
| 435.gromacs | 198 | 36.0 | 198 | 36.0 | 198 | 36.0 | 198 | 36.0 | 198 | 36.0 | 198 | 36.0 |
| 436.cactusADM | 31.3 | 382 | 31.1 | 384 | 31.1 | 384 | 31.3 | 382 | 31.1 | 384 | 31.1 | 384 |
| 437.leslie3d | 66.3 | 142 | 67.7 | 139 | 70.5 | 133 | 66.3 | 142 | 67.7 | 139 | 70.5 | 133 |
| 444.namd | 416 | 19.3 | 416 | 19.3 | 416 | 19.3 | 407 | 19.7 | 407 | 19.7 | 408 | 19.7 |
| 447.dealII | 229 | 50.0 | 229 | 49.9 | 229 | 49.9 | 229 | 50.0 | 229 | 49.9 | 229 | 49.9 |
| 450.soplex | 228 | 36.6 | 228 | 36.5 | 230 | 36.3 | 228 | 36.6 | 228 | 36.5 | 230 | 36.3 |
| 453.povray | 143 | 37.3 | 141 | 37.6 | 141 | 37.7 | 120 | 44.4 | 119 | 44.6 | 120 | 44.4 |
| 454.calculix | 220 | 37.4 | 220 | 37.4 | 219 | 37.6 | 205 | 40.3 | 205 | 40.2 | 207 | 39.9 |
| 459.GemsFDTD | 74.5 | 142 | 74.1 | 143 | 73.7 | 144 | 70.0 | 152 | 71.0 | 149 | 70.6 | 150 |
| 465.tonto | 305 | 32.3 | 304 | 32.3 | 304 | 32.4 | 259 | 38.0 | 260 | 37.9 | 259 | 37.9 |
| 470.lbm | 35.8 | 383 | 35.8 | 383 | 36.2 | 379 | 35.8 | 383 | 35.8 | 383 | 36.2 | 379 |
| 481.wrf | 156 | 71.5 | 157 | 71.2 | 156 | 71.5 | 156 | 71.5 | 157 | 71.2 | 156 | 71.5 |
| 482.sphinx3 | 303 | 64.4 | 303 | 64.3 | 304 | 64.2 | 303 | 64.4 | 303 | 64.3 | 304 | 64.2 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:

Energy Performance: Performance

Memory Voltage: 1.5 V

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

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General Notes (Continued)

OMP_NUM_THREADS = "12"

The Express5800/R120e-1E and
the Express5800/R120e-2E models are electronically equivalent.
The results have been measured on the Express5800/R120e-2E model.

Added glibc-static-2.12-1.107.el6.x86_64.rpm
to enable static linking
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias
```

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
```

Fortran benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias
```

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

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Peak Optimization Flags (Continued)

C++ benchmarks:

```
444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
           -auto-ilp32
```

```
447.dealII: basepeak = yes
```

```
450.soplex: basepeak = yes
```

```
453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: basepeak = yes
```

```
416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
            -inline-level=0 -scalar-rep- -static
```

```
434.zeusmp: basepeak = yes
```

```
437.leslie3d: basepeak = yes
```

```
459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
               -inline-level=0 -opt-prefetch -parallel
```

```
465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
            -opt-malloc-options=3 -auto -unroll4
```

Benchmarks using both Fortran and C:

```
435.gromacs: basepeak = yes
```

```
436.cactusADM: basepeak = yes
```

```
454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias
```

```
481.wrf: basepeak = yes
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>
<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120-RevB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>
<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120-RevB.xml>



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