SPEC® CFP2006 Result
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HITACHI

BladeSymphony BS520H (Intel Xeon E5-2670 v2)

$SPECfp®\_rate2006 = 598$

$SPECfp\_rate\_base2006 = 585$

CPU2006 license: 35　　　　　　　　　　　　　　　　　　　　　**Test date:** Mar-2014
Test sponsor: HITACHI　　　　　　　　　　　　　　　　　　**Hardware Availability:** Feb-2014
Tested by: HITACHI　　　　　　　　　　　　　　　　　　　　**Software Availability:** Sep-2013

Benchmark	Copies	Result
410.bwaves	40	487
416.gamess	40 / 40	655 / 644
433.milc	40 / 40	458 / 458
434.zeusmp	40	659
435.gromacs	40 / 40	806 / 794
436.cactusADM	40	702
437.leslie3d	20 / 40	351 / 321
444.namd	40 / 40	506 / 501
447.dealII	40	1100
450.soplex	20 / 40	385 / 338
453.povray	40 / 40	1020 / 880
454.calculix	40	962
459.GemsFDTD	40	299
465.tonto	40 / 40	690 / 667
470.lbm	40	600
481.wrf	40 / 40	571 / 571
482.sphinx3	40 / 40	569 / 599

SPECfp_rate_base2006 = 585

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Hardware

CPU Name:	Intel Xeon E5-2670 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 3.30 GHz
CPU MHz:	2500
FPU:	Integrated
CPU(s) enabled:	20 cores, 2 chips, 10 cores/chip, 2 threads/core
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 6.4 (Santiago) 2.6.32-358.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux; Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
Auto Parallel:	No
File System:	ext4

Continued on next page

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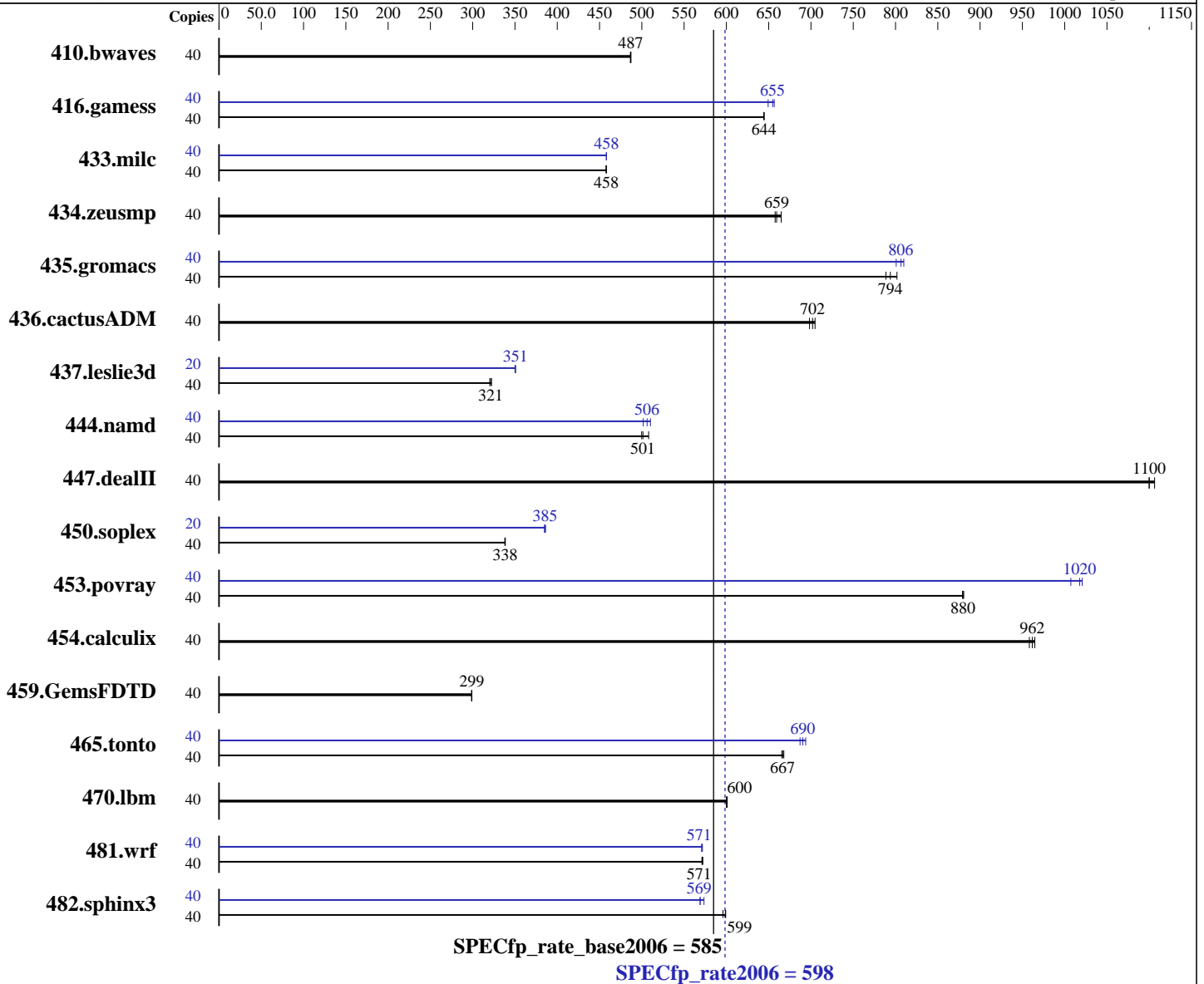|	
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L3 Cache:	25 MB I+D on chip per chip	System State:	Run level 3 (multu-user)
Other Cache:	None	Base Pointers:	32/64-bit
Memory:	128 GB (8 x 16 GB 2Rx4 PC3-14900R-13, ECC)	Peak Pointers:	32/64-bit
		Other Software:	None
Disk Subsystem:	1 x 146 GB SAS, 15000 RPM		
Other Hardware:	None		

Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	40	1116	487	**1117**	**487**	1117	486	40	1116	487	**1117**	**487**	1117	486
416.gamess	40	1216	644	1215	645	**1215**	**644**	40	1206	649	1193	657	**1196**	**655**
433.milc	40	802	458	802	458	**802**	**458**	40	**801**	**458**	801	458	802	458
434.zeusmp	40	554	658	548	665	**552**	**659**	40	554	658	548	665	**552**	**659**
435.gromacs	40	356	802	362	789	**360**	**794**	40	357	801	**354**	**806**	353	810
436.cactusADM	40	678	705	**681**	**702**	685	698	40	678	705	**681**	**702**	685	698
437.leslie3d	40	1174	320	1167	322	**1170**	**321**	20	537	350	536	351	**536**	**351**
444.namd	40	631	508	**640**	**501**	642	500	40	640	502	**634**	**506**	629	510
447.dealII	40	416	1100	**416**	**1100**	414	1110	40	416	1100	**416**	**1100**	414	1110
450.soplex	40	985	339	987	338	**986**	**338**	20	**433**	**385**	433	385	432	386
453.povray	40	242	879	**242**	**880**	242	881	40	**209**	**1020**	208	1020	211	1010
454.calculix	40	344	958	**343**	**962**	342	965	40	344	958	**343**	**962**	342	965
459.GemsFDTD	40	1419	299	1421	299	**1419**	**299**	40	1419	299	1421	299	**1419**	**299**
465.tonto	40	591	666	589	668	**591**	**667**	40	573	687	**570**	**690**	567	694
470.lbm	40	**916**	**600**	916	600	915	601	40	**916**	**600**	916	600	915	601
481.wrf	40	781	572	**782**	**571**	782	571	40	783	571	**783**	**571**	782	572
482.sphinx3	40	1308	596	1301	599	**1302**	**599**	40	1370	569	1359	574	**1370**	**569**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

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CPU2006 license: 35		**Test date:**	Mar-2014
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Platform Notes

```
Sysinfo program /home/cpu2006/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on localhost.localdomain Wed Mar  5 10:22:45 2014

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2670 v2 @ 2.50GHz
      2 "physical id"s (chips)
      40 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 10
      siblings  : 20
      physical 0: cores 0 1 2 3 4 8 9 10 11 12
      physical 1: cores 0 1 2 3 4 8 9 10 11 12
   cache size : 25600 KB

From /proc/meminfo
   MemTotal:       132334256 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

/usr/bin/lsb_release -d
   Red Hat Enterprise Linux Server release 6.4 (Santiago)

From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
   Linux localhost.localdomain 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41
   EST 2013 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Mar 5 10:11

SPEC is set to: /home/cpu2006
   Filesystem     Type     Size  Used Avail Use% Mounted on
   /dev/mapper/VolGroup-lv_root
                  ext4     133G   16G  110G  13% /

Additional information from dmidecode:
  BIOS HITACHI EP1043 02/04/2014
  Memory:
   16x Not Specified Not Specified
   8x Samsung M393B2G70QH0-CMA 16 GB 1867 MHz 2 rank

(End of data from sysinfo program)
```

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General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RedHat EL 6.4
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>       /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
 BladeSymphony BS520H and Hitachi Compute Blade 520H are electronically equivalent.
 The results have been measured on a BladeSymphony BS520H
```

Base Compiler Invocation

C benchmarks:
        icc   -m64

C++ benchmarks:
        icpc   -m64

Fortran benchmarks:
        ifort -m64

Benchmarks using both Fortran and C:
        icc   -m64 ifort -m64

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
   437.leslie3d: -DSPEC_CPU_LP64
     444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
   454.calculix: -DSPEC_CPU_LP64 -nofor_main
  459.GemsFDTD: -DSPEC_CPU_LP64
     465.tonto: -DSPEC_CPU_LP64
      470.lbm: -DSPEC_CPU_LP64
       481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
    482.sphinx3: -DSPEC_CPU_LP64
```

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Base Optimization Flags

C benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3
```

Fortran benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:
```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc    -m64
```

   482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
```
icpc   -m64
```

   450.soplex: `icpc -m32`

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc    -m64 ifort -m64
```

Peak Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
   437.leslie3d: -DSPEC_CPU_LP64
      444.namd: -DSPEC_CPU_LP64
     447.dealII: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
```

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Peak Portability Flags (Continued)

```
   454.calculix: -DSPEC_CPU_LP64 -nofor_main
  459.GemsFDTD: -DSPEC_CPU_LP64
      465.tonto: -DSPEC_CPU_LP64
       470.lbm: -DSPEC_CPU_LP64
       481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
    433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
              -prof-use(pass 2) -auto-ilp32

     470.lbm: basepeak = yes

  482.sphinx3: -xAVX -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
              -unroll2
```

C++ benchmarks:

```
   444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
             -prof-use(pass 2) -fno-alias -auto-ilp32

  447.dealII: basepeak = yes

   450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
             -prof-use(pass 2) -opt-malloc-options=3

   453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
             -prof-use(pass 2) -unroll4 -ansi-alias
```

Fortran benchmarks:

```
   410.bwaves: basepeak = yes

   416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
             -inline-level=0 -scalar-rep-

   434.zeusmp: basepeak = yes

   437.leslie3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch
```

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Peak Optimization Flags (Continued)

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.2.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.2.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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