



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

**SPECint®2006 = 63.4**

PowerEdge R220 (Intel Xeon E3-1270 v3, 3.50 GHz)

**SPECint\_base2006 = 60.9**

CPU2006 license: 55

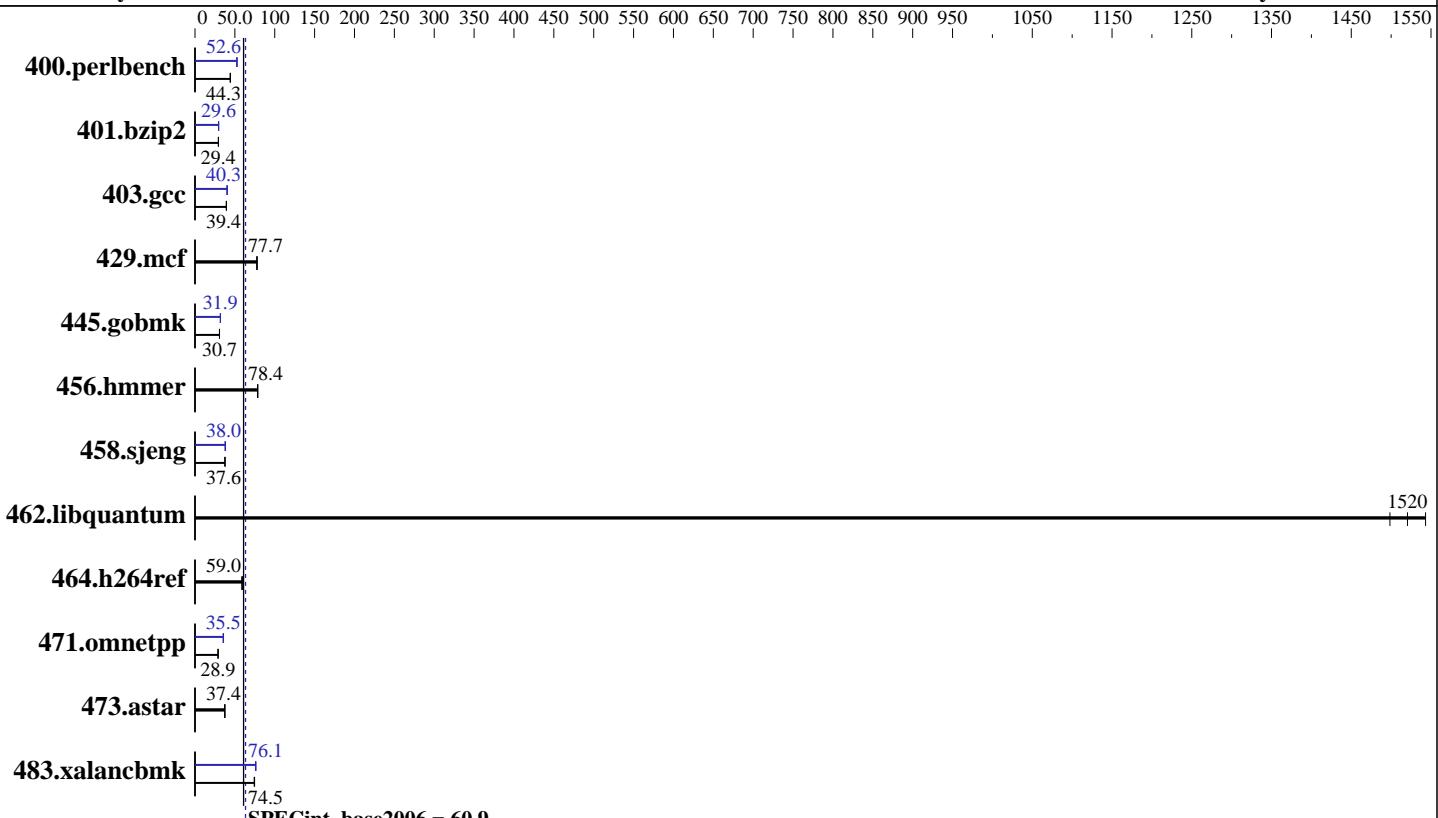
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Feb-2014

Hardware Availability: Mar-2014

Software Availability: Feb-2014



## Hardware

CPU Name: Intel Xeon E3-1270 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.90 GHz  
 CPU MHz: 3500  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 32 GB (4 x 8 GB 2Rx8 PC3L-12800E-11, ECC)  
 Disk Subsystem: 1 x 300 GB 10000 RPM SAS  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) 3.0.76-0.11-default  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext2  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b>221</b>	<b>44.3</b>	221	44.3	220	44.3	<b>185</b>	<b>52.7</b>	<b>186</b>	<b>52.6</b>	186	52.5
401.bzip2	329	29.3	<b>328</b>	<b>29.4</b>	328	29.4	<b>326</b>	<b>29.6</b>	<b>326</b>	<b>29.6</b>	326	29.6
403.gcc	204	39.4	<b>205</b>	<b>39.4</b>	205	39.3	<b>200</b>	<b>40.2</b>	<b>200</b>	<b>40.3</b>	200	40.3
429.mcf	118	77.5	117	78.0	<b>117</b>	<b>77.7</b>	118	77.5	117	78.0	<b>117</b>	<b>77.7</b>
445.gobmk	341	30.7	343	30.6	<b>341</b>	<b>30.7</b>	<b>329</b>	<b>31.9</b>	329	31.9	330	31.8
456.hmmer	118	78.9	119	78.4	<b>119</b>	<b>78.4</b>	118	78.9	119	78.4	<b>119</b>	<b>78.4</b>
458.sjeng	322	37.6	322	37.6	<b>322</b>	<b>37.6</b>	319	37.9	319	38.0	<b>319</b>	<b>38.0</b>
462.libquantum	<b>13.6</b>	<b>1520</b>	13.4	1540	13.8	1500	<b>13.6</b>	<b>1520</b>	13.4	1540	13.8	1500
464.h264ref	372	59.4	<b>375</b>	<b>59.0</b>	376	58.8	<b>372</b>	<b>59.4</b>	<b>375</b>	<b>59.0</b>	376	58.8
471.omnetpp	216	29.0	<b>216</b>	<b>28.9</b>	216	28.9	<b>176</b>	<b>35.5</b>	177	35.3	175	35.8
473.astar	<b>188</b>	<b>37.4</b>	188	37.3	187	37.4	<b>188</b>	<b>37.4</b>	188	37.3	187	37.4
483.xalancbmk	<b>92.6</b>	<b>74.5</b>	92.9	74.3	92.3	74.7	<b>90.7</b>	<b>76.1</b>	90.3	76.4	90.8	76.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Virtualization Technology disabled

Execute Disable disabled

System Profile set to Performance

Logical Processor disabled

Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6818

\$Rev: 6818 \$ \$Date::: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191

running on R220 Mon Feb 24 16:04:13 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E3-1270 v3 @ 3.50GHz  
1 "physical id"s (chips)  
4 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

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## Platform Notes (Continued)

```
cpu cores : 4
siblings : 4
physical 0: cores 0 1 2 3
cache size : 8192 KB

From /proc/meminfo
MemTotal:      32809896 kB
HugePages_Total:      0
Hugepagesize:     2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)

From /etc/*release* /etc/*version*
SuSE-release:
      SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 3

uname -a:
Linux R220 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013 (ccab990)
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Feb 24 15:46 last=S

SPEC is set to: /root/cpu2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        ext2  221G   41G  180G  19%  /

Additional information from dmidecode:
BIOS Dell Inc. 1.0.1 02/17/2014
Memory:
 4x Hynix/Hyundai HMT41GU7AFR8A-PB 8 GB 1600 MHz

(End of data from sysinfo program)
```

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/root/cpu2006-1.2/lib32:/root/cpu2006-1.2/lib64:/root/cpu2006-1.2/sh"  
OMP\_NUM\_THREADS = "4"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB  
memory using RedHat EL 6.4  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>



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## Base Compiler Invocation

C benchmarks:

  icc -m64

C++ benchmarks:

  icpc -m64

## Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hammer: -DSPEC_CPU_LP64
458sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

## Base Optimization Flags

C benchmarks:

  -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

  -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
  -Wl,-z,muldefs -L/sh -lsmartheap64

## Base Other Flags

C benchmarks:

  403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

  icc -m64

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## Peak Compiler Invocation (Continued)

400.perlbench: icc -m32

445.gobmk: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

456.hammer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

464.h264ref: -DSPEC\_CPU\_LP64

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32  
-opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias

456.hammer: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll14

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## Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>  
<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>  
<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revC.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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