



# SPEC® CINT2006 Result

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## IBM Corporation

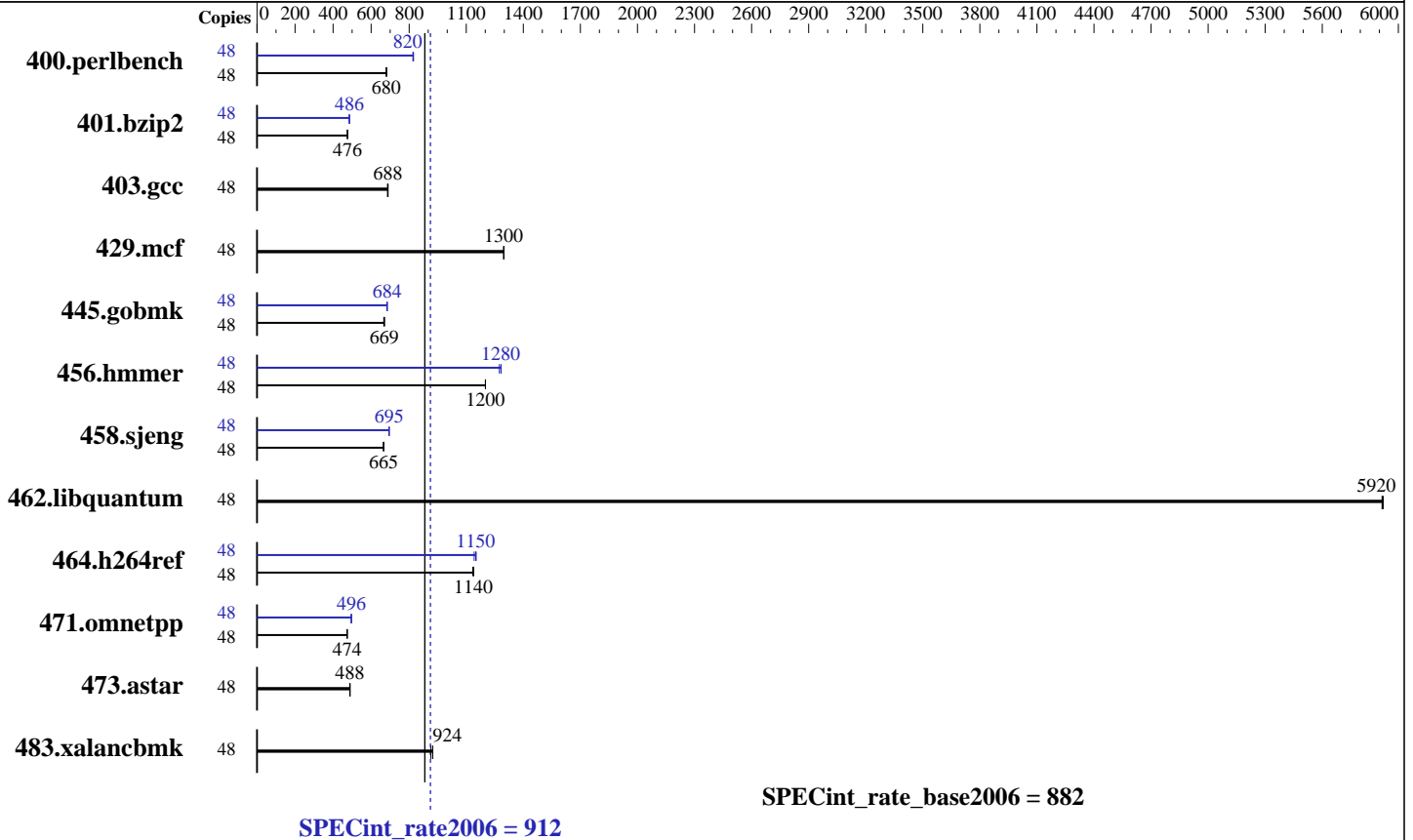
IBM Flex System x240  
(Intel Xeon E5-2695 v2, 3.20 GHz)

SPECint®\_rate2006 = 912

SPECint\_rate\_base2006 = 882

CPU2006 license: 11  
Test sponsor: IBM Corporation  
Tested by: IBM Corporation

Test date: Jul-2014  
Hardware Availability: Dec-2013  
Software Availability: Sep-2013



### Hardware

CPU Name: Intel Xeon E5-2695 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 30 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)  
 Disk Subsystem: 1 x 300 GB SAS, 10000 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)  
 2.6.32-358.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	48	690	679	<b>690</b>	<b>680</b>	688	681	48	572	820	<b>572</b>	<b>820</b>	570	823
401.bzip2	48	974	476	<b>974</b>	<b>476</b>	975	475	48	953	486	956	485	<b>954</b>	<b>486</b>
403.gcc	48	563	686	561	689	<b>562</b>	<b>688</b>	48	563	686	561	689	<b>562</b>	<b>688</b>
429.mcf	48	337	1300	338	1300	<b>337</b>	<b>1300</b>	48	337	1300	338	1300	<b>337</b>	<b>1300</b>
445.gobmk	48	756	666	<b>753</b>	<b>669</b>	751	670	48	<b>736</b>	<b>684</b>	735	685	738	682
456.hammer	48	373	1200	<b>373</b>	<b>1200</b>	373	1200	48	349	1280	352	1270	<b>349</b>	<b>1280</b>
458.sjeng	48	<b>873</b>	<b>665</b>	874	665	873	666	48	836	695	837	694	<b>836</b>	<b>695</b>
462.libquantum	48	168	5910	168	5920	<b>168</b>	<b>5920</b>	48	168	5910	168	5920	<b>168</b>	<b>5920</b>
464.h264ref	48	936	1130	933	1140	<b>933</b>	<b>1140</b>	48	<b>924</b>	<b>1150</b>	922	1150	931	1140
471.omnetpp	48	<b>633</b>	<b>474</b>	633	474	633	474	48	<b>605</b>	<b>496</b>	606	495	604	496
473.astar	48	688	490	<b>691</b>	<b>488</b>	691	487	48	688	490	<b>691</b>	<b>488</b>	691	487
483.xalancbmk	48	362	914	359	924	<b>359</b>	<b>924</b>	48	362	914	359	924	<b>359</b>	<b>924</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Operating Mode set to Maximum Performance in BIOS  
Sysinfo program /cpu2006.1.2\_14.0\_updates/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
running on BT2-IVB Tue Jul 29 02:06:20 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-2695 v2 @ 2.40GHz  
2 "physical id"s (chips)  
48 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 12

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### Platform Notes (Continued)

```
siblings      : 24
physical 0:   cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 1:   cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size   : 30720 KB
```

```
From /proc/meminfo
MemTotal:      264501256 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

```
/usr/bin/lsc_release -d
Red Hat Enterprise Linux Server release 6.4 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux BT2-IVB 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jul 28 17:53
```

```
SPEC is set to: /cpu2006.1.2_14.0_updates
Filesystem      Type      Size Used Avail Use% Mounted on
/dev/mapper/vg_bt2ivb-lv_root
ext4            265G 100G 153G 40% /
```

```
Additional information from dmidecode:
BIOS IBM -[B2E141AUS-1.50]- 05/08/2014
Memory:
16x Micron 36JSF2G72PZ-1G9E1 16 GB 1867 MHz 2 rank
8x Not Specified Not Specified
```

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/cpu2006.1.2\_14.0\_updates/libs/32:/cpu2006.1.2\_14.0\_updates/libs/64:/cpu2006.1.2\_14.0\_updates/sh"

```
Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```



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## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:  
icpc -m32



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32  
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias  
403.gcc: basepeak = yes  
429.mcf: basepeak = yes  
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3  
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32  
462.libquantum: basepeak = yes  
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap  
473.astar: basepeak = yes

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## Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>  
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>  
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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