



SPEC® CFP2006 Result

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Hewlett-Packard Company

ProLiant DL580 Gen9
(2.00 GHz, Intel Xeon E7-8880L v3)

SPECfp®2006 = 109

SPECfp_base2006 = 102

CPU2006 license: 3

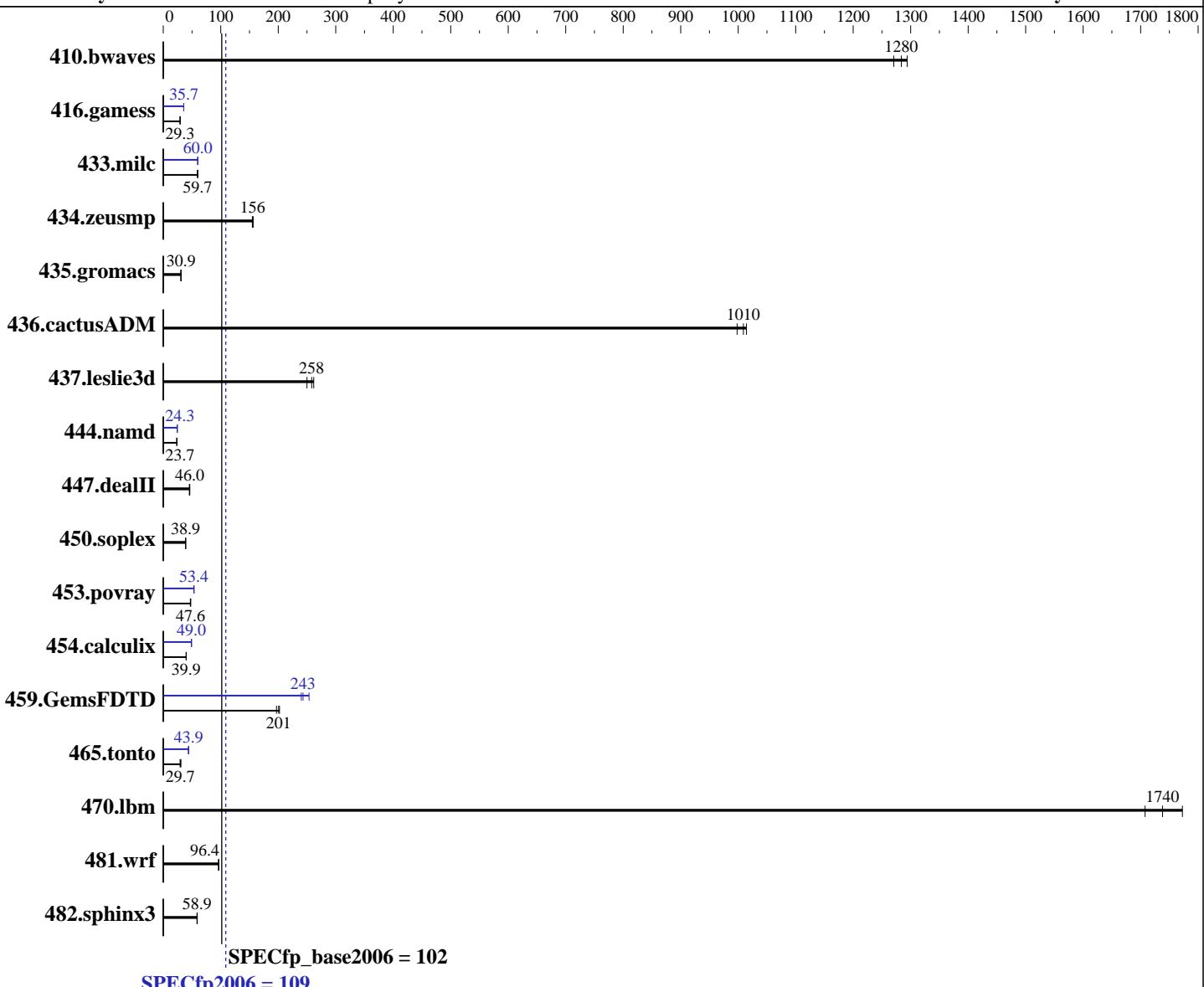
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Jun-2015

Hardware Availability: Jun-2015

Software Availability: Mar-2015



Hardware

CPU Name: Intel Xeon E7-8880L v3
CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz: 2000
FPU: Integrated
CPU(s) enabled: 72 cores, 4 chips, 18 cores/chip
CPU(s) orderable: 2,4 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: Red Hat Enterprise Linux Server release 7.1 (Maipo)
Compiler: Kernel 3.10.0-229.el7.x86_64
Auto Parallel: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
File System: Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
xfs

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L3 Cache: 45 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (32 x 16 GB 2Rx4 PC4-2133P-R,
running at 1600 MHz)
Disk Subsystem: 2 x 400 GB SAS SSD, RAID 1
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	10.7	1270	10.5	1290	<u>10.6</u>	<u>1280</u>	10.7	1270	10.5	1290	<u>10.6</u>	<u>1280</u>
416.gamess	667	29.3	668	29.3	<u>668</u>	<u>29.3</u>	550	35.6	548	35.7	<u>549</u>	<u>35.7</u>
433.milc	<u>154</u>	<u>59.7</u>	153	60.2	154	59.4	<u>153</u>	<u>60.0</u>	153	60.0	153	60.1
434.zeusmp	<u>58.4</u>	<u>156</u>	58.2	156	58.7	155	<u>58.4</u>	<u>156</u>	58.2	156	58.7	155
435.gromacs	231	30.9	234	30.6	<u>231</u>	<u>30.9</u>	231	30.9	234	30.6	<u>231</u>	<u>30.9</u>
436.cactusADM	<u>11.8</u>	<u>1010</u>	12.0	998	11.8	1010	<u>11.8</u>	<u>1010</u>	12.0	998	11.8	1010
437.leslie3d	37.6	250	35.9	262	<u>36.4</u>	<u>258</u>	37.6	250	35.9	262	<u>36.4</u>	<u>258</u>
444.namd	339	23.6	<u>339</u>	<u>23.7</u>	339	23.7	330	24.3	<u>330</u>	<u>24.3</u>	330	24.3
447.dealII	248	46.0	252	45.3	<u>249</u>	<u>46.0</u>	248	46.0	252	45.3	<u>249</u>	<u>46.0</u>
450.soplex	214	38.9	<u>214</u>	<u>38.9</u>	214	38.9	<u>214</u>	<u>38.9</u>	<u>214</u>	<u>38.9</u>	214	38.9
453.povray	113	47.1	<u>112</u>	<u>47.6</u>	112	47.7	<u>99.6</u>	<u>53.4</u>	100	53.2	99.5	53.5
454.calculix	<u>207</u>	<u>39.9</u>	207	39.9	207	39.8	168	49.1	168	49.0	<u>168</u>	<u>49.0</u>
459.GemsFDTD	53.9	197	<u>52.9</u>	<u>201</u>	52.5	202	41.8	254	<u>43.6</u>	<u>243</u>	44.2	240
465.tonto	320	30.7	334	29.4	<u>332</u>	<u>29.7</u>	<u>224</u>	<u>43.9</u>	223	44.1	224	43.9
470.lbm	<u>7.91</u>	<u>1740</u>	8.05	1710	7.75	1770	<u>7.91</u>	<u>1740</u>	8.05	1710	7.75	1770
481.wrf	116	96.4	<u>116</u>	<u>96.4</u>	116	96.2	<u>116</u>	<u>96.4</u>	<u>116</u>	<u>96.4</u>	116	96.2
482.sphinx3	332	58.7	<u>331</u>	<u>58.9</u>	329	59.2	<u>332</u>	<u>58.7</u>	<u>331</u>	<u>58.9</u>	329	59.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Platform Notes

BIOS Configuration

Power Profile set to Custom

Power Regulator set to Static High Performance Mode

Minimum Processor Idle Power Core C-State set to C6 State

Energy/Performance Bias set to Maximum Performance

Collaborative Power Control set to Disabled

Thermal Configuration set to Maximum Cooling

Continued on next page



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Platform Notes (Continued)

Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x Refresh
Intel Hyperthreading Options set to Disabled
Sysinfo program /home/cpu2006/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date::: 2014-06-25 #\\$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Mon Jun 1 12:17:05 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-8880L v3 @ 2.00GHz
        4 "physical id"s (chips)
        72 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 18
siblings : 18
physical 0: cores 0 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 1: cores 0 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 2: cores 0 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 3: cores 0 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
cache size : 46080 KB
```

```
From /proc/meminfo
MemTotal:      528072756 kB
HugePages_Total:      0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.1 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="7.1"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.1 (Maipo)"
    ANSI_COLOR="0;31"
    CPE_NAME="cpe:/o:redhat:enterprise_linux:7.1:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.1:ga:server

uname -a:
Linux localhost.localdomain 3.10.0-229.el7.x86_64 #1 SMP Thu Jan 29 18:37:38
EST 2015 x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Jun 1 12:14

SPEC is set to: /home/cpu2006

Continued on next page



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Platform Notes (Continued)

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/mapper/rhel-home	xfs	318G	6.0G	312G	2%	/home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP U17 03/13/2015

Memory:

32x HP 752369-081 16 GB 2 rank 2133 MHz, configured at 1600 MHz
64x UNKNOWN NOT AVAILABLE

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have one line reading as:

32x HP 752369-081 16 GB 2 rank 2133 MHz, configured at 1600 MHz

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

OMP_NUM_THREADS = "72"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

Continued on next page



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Base Portability Flags (Continued)

```

416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



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Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -auto-ilp32 -ansi-alias
```

```
470.lbm: basepeak = yes
```

```
482.sphinx3: basepeak = yes
```

C++ benchmarks:

```
444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -fno-alias -auto-ilp32
```

```
447.dealII: basepeak = yes
```

```
450.soplex: basepeak = yes
```

```
453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
             -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14
             -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: basepeak = yes
```

```
416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
              -inline-level=0 -scalar-rep-
```

```
434.zeusmp: basepeak = yes
```

```
437.leslie3d: basepeak = yes
```

```
459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
                -inline-level=0 -opt-prefetch -parallel
```

```
465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
             -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
             -inline-calloc -opt-malloc-options=3 -auto -unroll14
```

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Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

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