



SPEC® CINT2006 Result

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Lenovo Group Limited

Lenovo ThinkServer TS150
(3.70 GHz, Intel Xeon E3-1280 v5)

SPECint®2006 = 75.8

SPECint_base2006 = 73.5

CPU2006 license: 9017

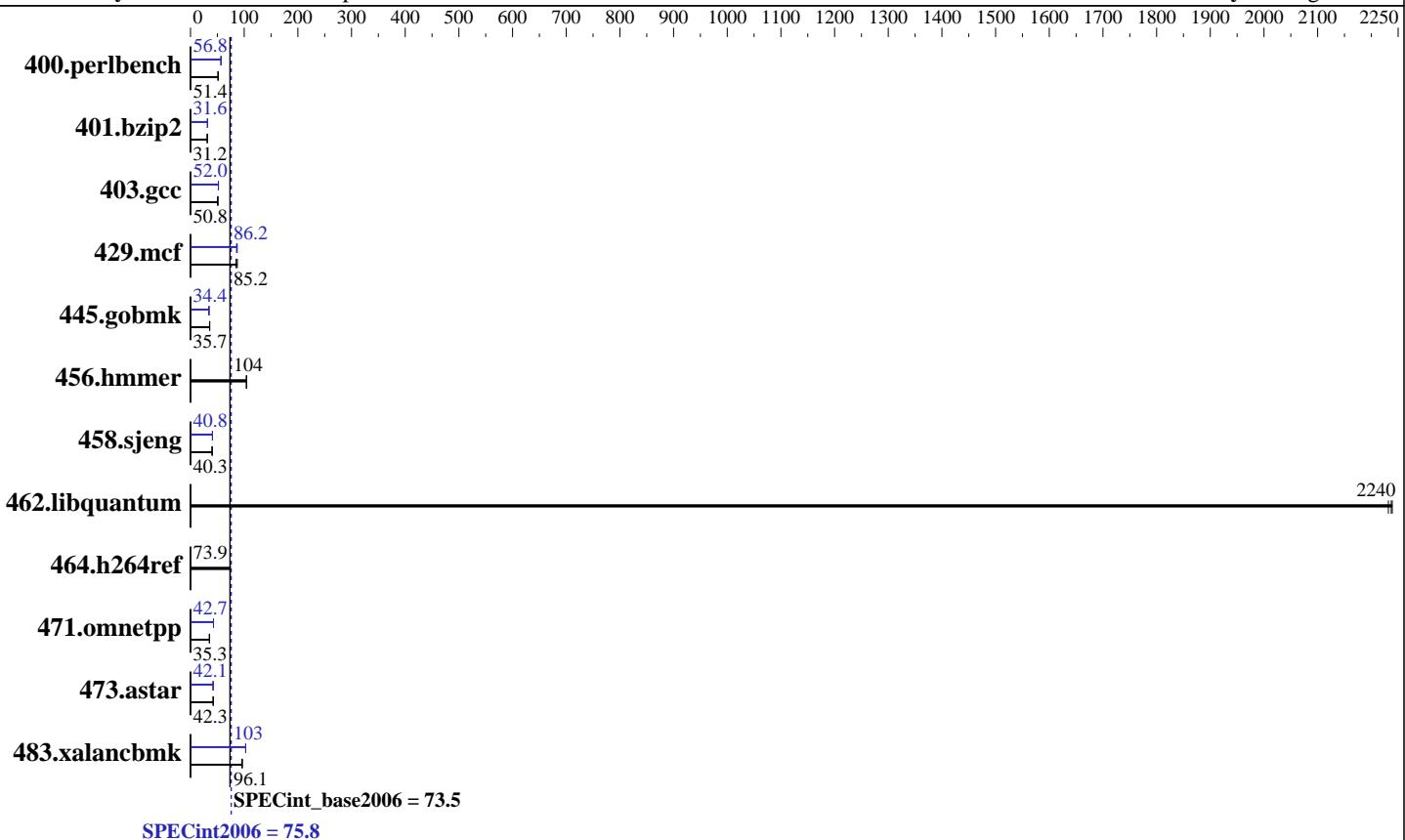
Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Dec-2015

Hardware Availability: Dec-2015

Software Availability: Aug-2015



Hardware

CPU Name:	Intel Xeon E3-1280 v5
CPU Characteristics:	Intel Turbo Boost Technology up to 4.00 GHz
CPU MHz:	3700
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	32 GB (4 x 8 GB 2Rx8 PC4-2133P-U)
Disk Subsystem:	1 x 800 GB SATA SSD
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 12 (x86_64)
Compiler:	Kernel 3.12.28-4-default
Auto Parallel:	C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.2



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	190	51.5	191	51.2	<u>190</u>	51.4	<u>172</u>	56.8	172	56.8	172	56.8
401.bzip2	309	31.2	311	31.1	<u>309</u>	31.2	<u>305</u>	31.6	305	31.6	307	31.5
403.gcc	158	50.9	<u>158</u>	50.8	159	50.8	<u>155</u>	52.0	155	51.9	155	52.1
429.mcf	107	84.8	104	87.3	<u>107</u>	85.2	<u>106</u>	86.2	105	87.1	106	85.9
445.gobmk	<u>294</u>	35.7	294	35.6	294	35.7	305	34.4	<u>305</u>	34.4	305	34.4
456.hmmer	89.4	104	<u>89.4</u>	104	89.7	104	89.4	104	<u>89.4</u>	104	89.7	104
458.sjeng	<u>300</u>	40.3	300	40.3	300	40.3	<u>296</u>	40.8	296	40.9	296	40.8
462.libquantum	<u>9.26</u>	2240	9.25	2240	9.28	2230	<u>9.26</u>	2240	9.25	2240	9.28	2230
464.h264ref	<u>299</u>	73.9	299	74.1	299	73.9	<u>299</u>	73.9	299	74.1	299	73.9
471.omnetpp	176	35.5	179	35.0	<u>177</u>	35.3	<u>146</u>	42.7	146	42.7	146	42.8
473.astar	165	42.4	167	42.1	<u>166</u>	42.3	166	42.3	167	42.0	<u>167</u>	42.1
483.xalancbmk	<u>71.8</u>	96.1	71.5	96.5	71.9	96.0	<u>67.1</u>	103	<u>67.1</u>	103	67.2	103

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:

EIST Support set to Enabled

Intel (R) Hyper-Threading set to Disabled

C1E Support set to Enabled

C State Support set to Enabled

Turbo Mode set to Enable

```
Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914
$Rev: 6914 $ $Date::: 2014-06-25 #$
e3fbb8667b5a285932ceab81e28219e1
running on TS150 Fri Dec 11 07:16:33 2015
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) CPU E3-1280 v5 @ 3.70GHz
```

```
1 "physical id"s (chips)
```

```
4 "processors"
```

```
cores, siblings (Caution: counting these is hw and system dependent. The
Continued on next page
```



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Platform Notes (Continued)

```
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 4
siblings   : 4
physical 0: cores 0 1 2 3
cache size : 8192 KB

From /proc/meminfo
MemTotal:      32933292 kB
HugePages_Total:       0
Hugepagesize:     2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 0
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
    NAME="SLES"
    VERSION="12"
    VERSION_ID="12"
    PRETTY_NAME="SUSE Linux Enterprise Server 12"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12"

uname -a:
Linux TS150 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014 (9879bd4)
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Dec 11 07:12

SPEC is set to: /home/cpu2006-1.2-ic16.0
Filesystem      Type  Size  Used  Avail Use% Mounted on
/dev/sda3        xfs   693G   27G   667G   4% /home
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS LENOVO FWKT28A  11/17/2015
Memory:
 4x Samsung M378A1G43DB0-CPB 8 GB 2 rank 2133 MHz

(End of data from sysinfo program)
```



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General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"

OMP_NUM_THREADS = "4"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmr: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64



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Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

445.gobmk: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

403.gcc: -DSPEC_CPU_LP64

429.mcf: -DSPEC_CPU_LP64

445.gobmk: -D_FILE_OFFSET_BITS=64

456.hmmr: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

464.h264ref: -DSPEC_CPU_LP64

471.omnetpp: -D_FILE_OFFSET_BITS=64

473.astar: -DSPEC_CPU_LP64

483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)

-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)

-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch

-ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)

-ipo(pass 2) -O3(pass 2) -no-prec-div

-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

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Peak Optimization Flags (Continued)

401.bzip2 (continued):
 -opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
 -opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
 -opt-prefetch -auto-p32

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
 -prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias

456.hmmr: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
 -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
 -par-num-threads=1(pass 1) -prof-use(pass 2)
 -opt-ra-region-strategy=block -ansi-alias
 -Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
 -auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
 -ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Default-Platform-Flags.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Default-Platform-Flags.xml>

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