



SPEC[®] CFP2006 Result

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Cisco Systems

Cisco UCS B200 M4 (Intel Xeon E5-2640 v4, 2.40 GHz)

SPECfp[®]_rate2006 = 712

SPECfp_rate_base2006 = 696

CPU2006 license: 9019

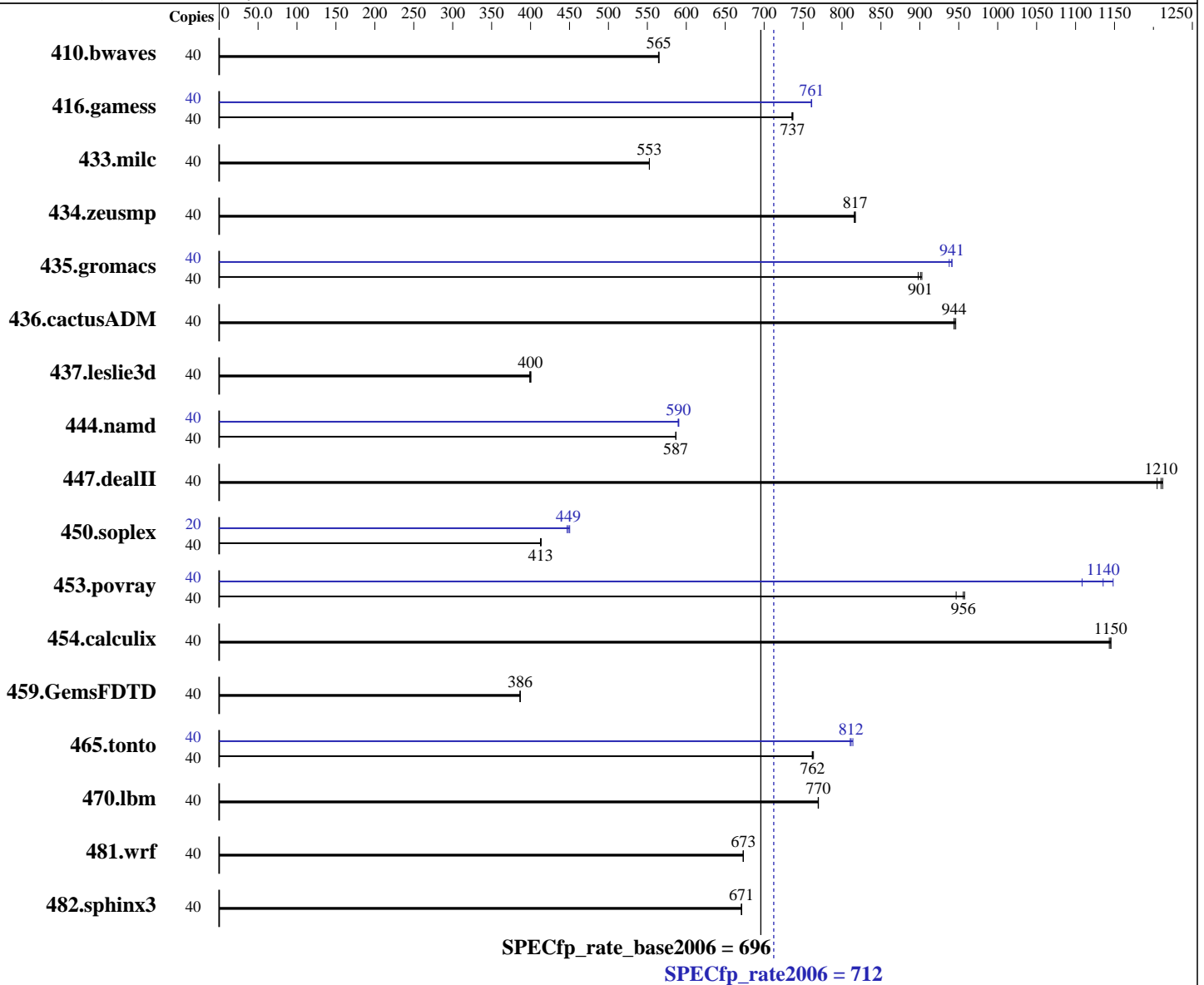
Test sponsor: Cisco Systems

Tested by: Cisco Systems

Test date: May-2016

Hardware Availability: Apr-2016

Software Availability: Dec-2015



Hardware

CPU Name: Intel Xeon E5-2640 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 3.40 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86_64) 3.12.49-11-default
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: xfs
 System State: Run level 3 (multi-user)

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L3 Cache: 25 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)
Disk Subsystem: 1 x 400 GB SSD SAS
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---------------|--------|-------------|------------|-------------|-------------|-------------|------------|--------|-------------|------------|-------------|-------------|-------------|-------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 40 | <u>962</u> | <u>565</u> | 962 | 565 | 963 | 564 | 40 | <u>962</u> | <u>565</u> | 962 | 565 | 963 | 564 |
| 416.gamess | 40 | <u>1063</u> | <u>737</u> | 1063 | 737 | 1065 | 736 | 40 | <u>1029</u> | <u>761</u> | 1029 | 761 | 1030 | 761 |
| 433.milc | 40 | 665 | 552 | <u>664</u> | <u>553</u> | 664 | 553 | 40 | 665 | 552 | <u>664</u> | <u>553</u> | 664 | 553 |
| 434.zeusmp | 40 | 446 | 816 | <u>446</u> | <u>817</u> | 445 | 817 | 40 | 446 | 816 | <u>446</u> | <u>817</u> | 445 | 817 |
| 435.gromacs | 40 | 318 | 898 | <u>317</u> | <u>901</u> | 316 | 903 | 40 | <u>303</u> | <u>941</u> | 303 | 941 | 305 | 938 |
| 436.cactusADM | 40 | 506 | 944 | 505 | 946 | <u>506</u> | <u>944</u> | 40 | 506 | 944 | 505 | 946 | <u>506</u> | <u>944</u> |
| 437.leslie3d | 40 | 942 | 399 | 939 | 400 | <u>940</u> | <u>400</u> | 40 | 942 | 399 | 939 | 400 | <u>940</u> | <u>400</u> |
| 444.namd | 40 | <u>547</u> | <u>587</u> | 547 | 586 | 547 | 587 | 40 | 543 | 590 | 544 | 590 | <u>544</u> | <u>590</u> |
| 447.dealII | 40 | 380 | 1200 | <u>378</u> | <u>1210</u> | 378 | 1210 | 40 | 380 | 1200 | <u>378</u> | <u>1210</u> | 378 | 1210 |
| 450.soplex | 40 | 806 | 414 | <u>808</u> | <u>413</u> | 808 | 413 | 20 | <u>372</u> | <u>449</u> | 370 | 450 | 373 | 447 |
| 453.povray | 40 | 222 | 957 | 225 | 947 | <u>223</u> | <u>956</u> | 40 | 185 | 1150 | 192 | 1110 | <u>187</u> | <u>1140</u> |
| 454.calculix | 40 | 289 | 1140 | <u>288</u> | <u>1150</u> | 288 | 1150 | 40 | 289 | 1140 | <u>288</u> | <u>1150</u> | 288 | 1150 |
| 459.GemsFDTD | 40 | 1098 | 386 | 1097 | 387 | <u>1098</u> | <u>386</u> | 40 | 1098 | 386 | 1097 | 387 | <u>1098</u> | <u>386</u> |
| 465.tonto | 40 | 516 | 763 | 517 | 762 | <u>516</u> | <u>762</u> | 40 | 486 | 811 | 484 | 814 | <u>485</u> | <u>812</u> |
| 470.lbm | 40 | <u>714</u> | <u>770</u> | 714 | 770 | 714 | 769 | 40 | <u>714</u> | <u>770</u> | 714 | 770 | 714 | 769 |
| 481.wrf | 40 | 664 | 673 | 663 | 673 | <u>664</u> | <u>673</u> | 40 | 664 | 673 | 663 | 673 | <u>664</u> | <u>673</u> |
| 482.sphinx3 | 40 | 1162 | 671 | <u>1162</u> | <u>671</u> | 1162 | 671 | 40 | 1162 | 671 | <u>1162</u> | <u>671</u> | 1162 | 671 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"



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Platform Notes

BIOS Settings:

CPU performance set to Enterprise
 Power Technology set to Energy Efficient
 Energy Performance BIAS setting set to Balanced Performance
 Memory RAS configuration set to Maximum Performance
 Memory Power Saving Mode set to Disabled
 QPI Snoop Mode set to Home Directory Snoop with OSB
 Sysinfo program /opt/CISCO_Benchmarks/cpu2006/config/sysinfo.rev6914
 \$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
 running on linux-nsg9 Wed May 25 09:59:15 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```

model name      : Intel(R) Xeon(R) CPU E5-2640 v4 @ 2.40GHz
 2 "physical id"s (chips)
 40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores    : 10
  siblings     : 20
  physical 0   : cores 0 1 2 3 4 8 9 10 11 12
  physical 1   : cores 0 1 2 3 4 8 9 10 11 12
cache size    : 25600 KB

```

From /proc/meminfo

```

MemTotal:      264367400 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

```

/usr/bin/lsb_release -d

SUSE Linux Enterprise Server 12 SP1

From /etc/*release* /etc/*version*

```

SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

```

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Platform Notes (Continued)

```
uname -a:
Linux linux-nsg9 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 May 24 22:41
```

```
SPEC is set to: /opt/CISCO_Benchmarks/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda1        xfs   325G  13G  313G   4% /
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Cisco Systems, Inc. B200M4.3.1.2.4.041120161047 04/11/2016

Memory:

16x 0xCE00 M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz, configured at 2133 MHz
8x NO DIMM NO DIMM

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/opt/CISCO_Benchmarks/cpu2006/libs/32:/opt/CISCO_Benchmarks/cpu2006/libs/64:/opt/CISCO_Benchmarks/cpu2006/sh"
```

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

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Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks:

icc -m64

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Peak Compiler Invocation (Continued)

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.deallI: -DSPEC_CPU_LP64
 450.soplex: -D_FILE_OFFSET_BITS=64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
 -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
 -prof-use(pass 2) -fno-alias -auto-ilp32

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Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revE.xml>

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