



SPEC CPU®2017 Integer Speed Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380 Gen10

(1.70 GHz, Intel Xeon Bronze 3106)

SPECspeed®2017_int_base = 4.20

SPECspeed®2017_int_peak = 4.34

CPU2017 License: 3

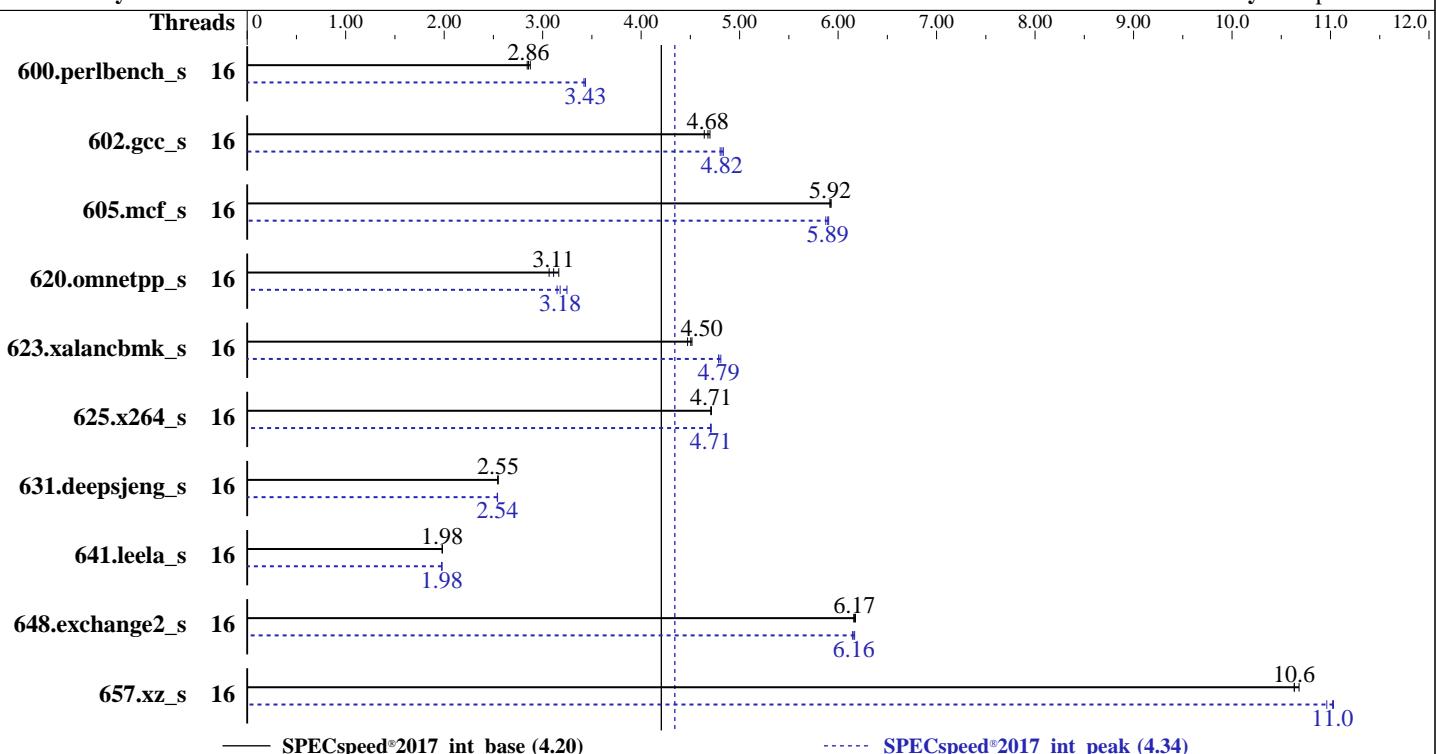
Test Date: Nov-2017

Test Sponsor: HPE

Hardware Availability: Oct-2017

Tested by: HPE

Software Availability: Sep-2017



Hardware		Software	
CPU Name:	Intel Xeon Bronze 3106	OS:	SUSE Linux Enterprise Server 12 (x86_64) SP3
Max MHz:	1700		Kernel 4.4.73-5-default
Nominal:	1700	Compiler:	C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux;
Enabled:	16 cores, 2 chips		Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux
Orderable:	1, 2 chip(s)	Parallel:	Yes
Cache L1:	32 KB I + 32 KB D on chip per core	Firmware:	HPE BIOS Version U30 released Oct-2017 (tested with U30 9/29/2017)
L2:	1 MB I+D on chip per core	File System:	xfs
L3:	11 MB I+D on chip per chip	System State:	Run level 3 (multi-user)
Other:	None	Base Pointers:	64-bit
Memory:	192 GB (24 x 8 GB 2Rx8 PC4-2666V-R, running at 2133)	Peak Pointers:	32/64-bit
Storage:	1 x 960 GB SATA SSD, RAID 0	Other:	jemalloc: jemalloc memory allocator library V5.0.1; jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets; jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5; jemalloc: sources available from jemalloc.net or releases
Other:	None	Power Management:	--



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Results Table

Benchmark	Base								Peak							
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	16	624	2.84	622	2.86	617	2.87	16	517	3.43	519	3.42	517	3.44		
602.gcc_s	16	851	4.68	847	4.70	858	4.64	16	829	4.80	826	4.82	824	4.83		
605.mcf_s	16	796	5.93	798	5.92	798	5.92	16	800	5.90	804	5.88	801	5.89		
620.omnetpp_s	16	515	3.16	532	3.07	524	3.11	16	518	3.15	513	3.18	502	3.25		
623.xalancbmk_s	16	315	4.50	314	4.52	317	4.47	16	295	4.81	296	4.79	296	4.79		
625.x264_s	16	374	4.71	374	4.71	375	4.71	16	375	4.71	375	4.71	374	4.71		
631.deepsjeng_s	16	563	2.55	563	2.54	563	2.55	16	564	2.54	564	2.54	564	2.54		
641.leela_s	16	861	1.98	861	1.98	861	1.98	16	864	1.98	863	1.98	864	1.98		
648.exchange2_s	16	476	6.18	477	6.16	477	6.17	16	477	6.16	478	6.15	477	6.17		
657.xz_s	16	581	10.6	581	10.6	579	10.7	16	564	11.0	560	11.0	561	11.0		
SPECspeed®2017_int_base = 4.20				SPECspeed®2017_int_peak = 4.34												

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled by default

Filesystem page cache cleared with:

```
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
irqbalance disabled with "systemctl stop irqbalance"
tuned profile set with "tuned-adm profile throughput-performance"
```

General Notes

Environment variables set by runcpu before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790K CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.4

Platform Notes

BIOS Configuration:

Thermal Configuration set to Maximum Cooling

LLC Prefetch set to Enabled

LLC Dead Line Allocation set to Disabled

Memory Patrol Scrubbing set to Disabled

Workload Profile set to General Peak Frequency Compute

Energy/Performance Bias set to Maximum Performance

Workload Profile set to Custom

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Platform Notes (Continued)

NUMA Group Size Optimization set to Flat
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on linux-b7s1 Thu Nov 16 10:47:16 2017

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Bronze 3106 CPU @ 1.70GHz
  2 "physical id"s (chips)
  16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 8
  siblings   : 8
  physical 0: cores 0 1 2 3 4 5 6 7
  physical 1: cores 0 1 2 3 4 5 6 7
```

From lscpu:

```
Architecture:           x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:            Little Endian
CPU(s):                16
On-line CPU(s) list:  0-15
Thread(s) per core:   1
Core(s) per socket:   8
Socket(s):             2
NUMA node(s):          2
Vendor ID:             GenuineIntel
CPU family:            6
Model:                 85
Model name:            Intel(R) Xeon(R) Bronze 3106 CPU @ 1.70GHz
Stepping:               4
CPU MHz:                1696.027
BogoMIPS:              3392.05
Virtualization:        VT-x
L1d cache:              32K
L1i cache:              32K
L2 cache:                1024K
L3 cache:                11264K
NUMA node0 CPU(s):    0-3,8-11
NUMA node1 CPU(s):    4-7,12-15
Flags:      fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
           pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
           lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
```

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Platform Notes (Continued)

```
aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch arat epb pln pts dtherm intel_pt
tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2
erms invpcid rtm cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd
avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc pku ospke
```

```
/proc/cpuinfo cache data
cache size : 11264 KB
```

```
From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
physical chip.
```

```
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 8 9 10 11
node 0 size: 96349 MB
node 0 free: 94153 MB
node 1 cpus: 4 5 6 7 12 13 14 15
node 1 size: 96766 MB
node 1 free: 96392 MB
node distances:
node 0 1
 0: 10 21
 1: 21 10
```

```
From /proc/meminfo
MemTotal:      197750952 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP3
```

```
From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 3
  # This file is deprecated and will be removed in a future service pack or release.
  # Please check /etc/os-release for details about this release.
os-release:
  NAME="SLES"
  VERSION="12-SP3"
  VERSION_ID="12.3"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp3"
```

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Platform Notes (Continued)

```
uname -a:  
Linux linux-b7s1 4.4.73-5-default #1 SMP Tue Jul 4 15:33:39 UTC 2017 (b7ce4e4) x86_64  
x86_64 x86_64 GNU/Linux  
  
run-level 3 Nov 16 10:43  
  
SPEC is set to: /home/cpu2017  
Filesystem      Type  Size  Used Avail Use% Mounted on  
/dev/sda4        xfs   852G   43G  809G   6% /home  
  
Additional information from dmidecode follows.  WARNING: Use caution when you interpret  
this section. The 'dmidecode' program reads system data which is "intended to allow  
hardware to be accurately determined", but the intent may not be met, as there are  
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.  
BIOS HPE U30 10/11/2017  
Memory:  
24x UNKNOWN NOT AVAILABLE 8 GB 2 rank 2666, configured at 2133  
  
(End of data from sysinfo program)
```

Compiler Version Notes

```
=====  
C      | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base,  
| peak) 625.x264_s(base, peak) 657.xz_s(base, peak)  
-----  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----  
=====  
C++    | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak)  
| 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)  
-----  
icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----  
=====  
Fortran | 648.exchange2_s(base, peak)  
-----  
ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----
```



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Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64

602.gcc_s: -DSPEC_LP64

605.mcf_s: -DSPEC_LP64

620.omnetpp_s: -DSPEC_LP64

623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX

625.x264_s: -DSPEC_LP64

631.deepsjeng_s: -DSPEC_LP64

641.leela_s: -DSPEC_LP64

648.exchange2_s: -DSPEC_LP64

657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div

-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP

-L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:

-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div

-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:

-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div

-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

-L/usr/local/je5.0.1-64/lib -ljemalloc



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Base Other Flags

C benchmarks:

-m64 -std=c11

C++ benchmarks:

-m64

Fortran benchmarks:

-m64

Peak Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Peak Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX2 -qopt-mem-layout-trans=3 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp

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Peak Optimization Flags (Continued)

600.perlbench_s (continued):

```
-DSPEC_OPENMP -fno-strict-overflow  
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

```
602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2  
-xCORE-AVX2 -qopt-mem-layout-trans=3 -ipo -O3  
-no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp  
-DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc
```

```
605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3  
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP  
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

```
625.x264_s: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP  
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

657.xz_s: Same as 602.gcc_s

C++ benchmarks:

```
620.omnetpp_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3  
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP  
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

```
623.xalancbmk_s: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32  
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3  
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP  
-L/usr/local/je5.0.1-32/lib -ljemalloc
```

631.deepsjeng_s: Same as 620.omnetpp_s

641.leela_s: Same as 620.omnetpp_s

Fortran benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte  
-L/usr/local/je5.0.1-64/lib -ljemalloc
```



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Peak Other Flags

C benchmarks:

-m64 -std=c11

C++ benchmarks (except as noted below):

-m64

623.xalancbmk_s: -m32

Fortran benchmarks:

-m64

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.html>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.xml>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revG.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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