SPEC® CPU2017 Floating Point Speed Result

Huawei

$SPECspeed2017\_fp\_base =$ 79.1

Huawei 5885H V5 (Intel Xeon Gold 5122)

$SPECspeed2017\_fp\_peak =$ 80.2
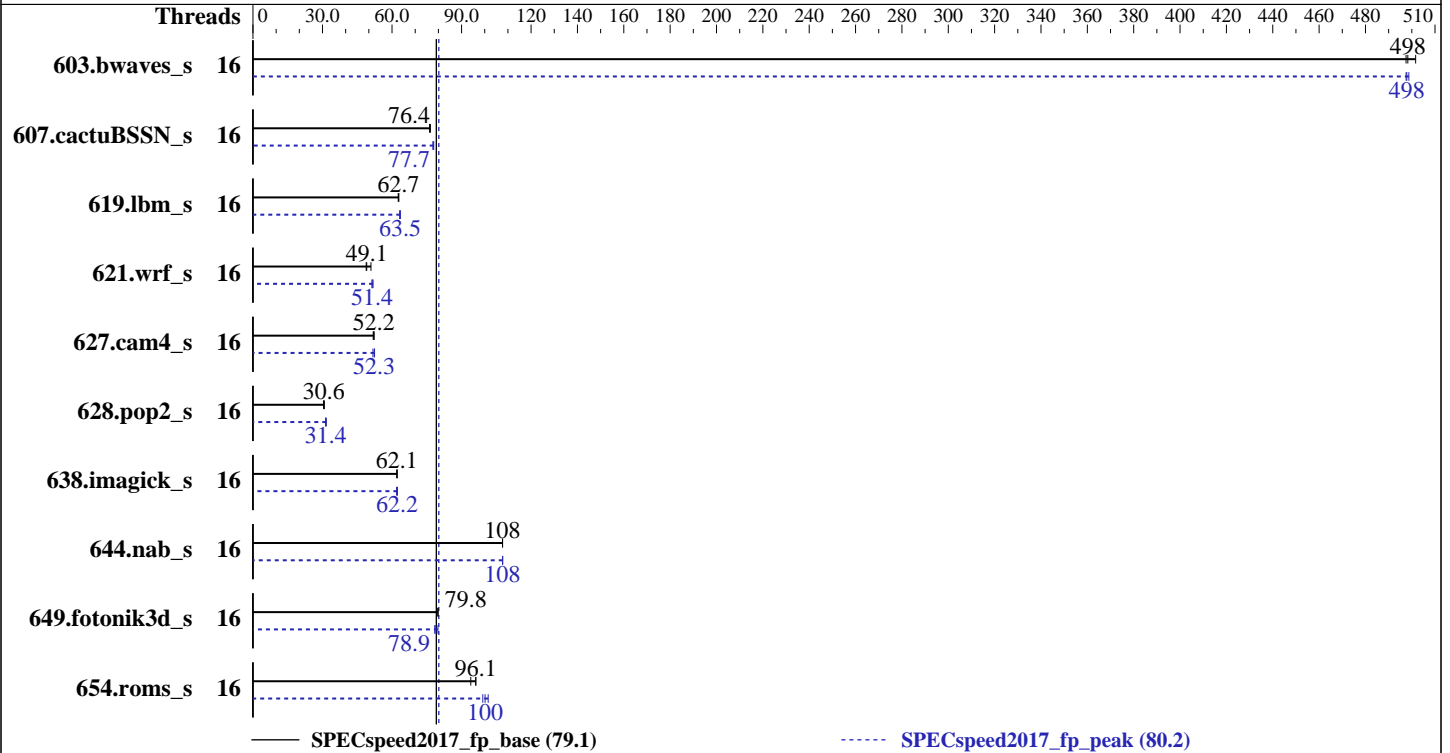
CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Test Date: May-2018
Hardware Availability: May-2018
Software Availability: Mar-2018

Threads

Benchmark	Threads	base	peak
603.bwaves_s	16	498	498
607.cactuBSSN_s	16	76.4	77.7
619.lbm_s	16	62.7	63.5
621.wrf_s	16	49.1	51.4
627.cam4_s	16	52.2	52.3
628.pop2_s	16	30.6	31.4
638.imagick_s	16	62.1	62.2
644.nab_s	16	108	108
649.fotonik3d_s	16	79.8	78.9
654.roms_s	16	96.1	100

SPECspeed2017_fp_base (79.1)   SPECspeed2017_fp_peak (80.2)

Hardware

CPU Name: Intel Xeon Gold 5122
 Max MHz.: 3700
 Nominal: 3600
Enabled: 16 cores, 4 chips
Orderable: 2,4 chips
Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 16.5 MB I+D on chip per chip
 Other: None
Memory: 1536 GB (48 x 32 GB 2Rx4 PC4-2666V-R)
Storage: 1 x 900 GB SAS HDD 10K RPM, RAID 0
Other: None

Software

OS: SUSE Linux Enterprise Server 12 SP2
 4.4.120-92.70-default
Compiler: C/C++: Version 18.0.0.128 of Intel C/C++
 Compiler for Linux;
 Fortran: Version 18.0.0.128 of Intel Fortran
 Compiler for Linux
Parallel: Yes
Firmware: Version 0.84 released Mar-2018
File System: ext4
System State: Run level 5 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: None

SPEC CPU2017 Floating Point Speed Result

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Results Table

Benchmark	Base Threads	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Peak Threads	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
603.bwaves_s	16	**118**	**498**	118	502	119	498	16	119	498	**119**	**498**	118	499
607.cactuBSSN_s	16	219	76.2	218	76.5	**218**	**76.4**	16	215	77.7	**214**	**77.7**	214	78.0
619.lbm_s	16	83.5	62.7	83.2	63.0	**83.5**	**62.7**	16	82.4	63.6	**82.5**	**63.5**	82.7	63.3
621.wrf_s	16	271	48.9	**269**	**49.1**	260	50.9	16	255	51.8	**257**	**51.4**	257	51.4
627.cam4_s	16	170	52.3	**170**	**52.2**	171	51.9	16	**169**	**52.3**	171	51.8	169	52.5
628.pop2_s	16	386	30.8	**387**	**30.6**	390	30.5	16	375	31.6	**378**	**31.4**	379	31.3
638.imagick_s	16	**232**	**62.1**	232	62.1	232	62.2	16	**232**	**62.2**	231	62.3	233	62.0
644.nab_s	16	162	108	**162**	**108**	162	108	16	162	108	**162**	**108**	162	108
649.fotonik3d_s	16	114	79.8	115	79.4	**114**	**79.8**	16	**116**	**78.9**	116	78.4	114	79.8
654.roms_s	16	164	96.2	168	94.0	**164**	**96.1**	16	155	101	159	99.2	**157**	**100**

SPECspeed2017_fp_base =	**79.1**
SPECspeed2017_fp_peak =	**80.2**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Numa balancing was disabled using "echo 0 > /proc/sys/kernel/numa_balancing"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>   /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

(Continued on next page)

SPEC CPU2017 Floating Point Speed Result

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SPECspeed2017_fp_base = 79.1

SPECspeed2017_fp_peak = 80.2

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Test Sponsor: Huawei
Tested by: Huawei

Test Date: May-2018
Hardware Availability: May-2018
Software Availability: Mar-2018

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

```
BIOS configuration:
Sub NUMA Cluster (SNC) set to enabled
IMC (Integrated memory controller) Interleaving set to 1 way interleave
Xtended Prediction Table (XPT) Prefetch set to Enable
Memory Patrol Scrub set to Disable
Last Level Cache (LLC) Prefetch set to Disable
Hyper-Threading set to Disable
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on linux-scjg Mon May 21 15:22:52 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
   https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) Gold 5122 CPU @ 3.60GHz
      4  "physical id"s (chips)
      16 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following
   excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
      cpu cores : 4
      siblings  : 4
      physical 0: cores 1 5 9 13
      physical 1: cores 1 5 9 13
      physical 2: cores 1 5 9 13
      physical 3: cores 1 5 9 13

From lscpu:
      Architecture:          x86_64
      CPU op-mode(s):        32-bit, 64-bit
      Byte Order:            Little Endian
      CPU(s):                16
      On-line CPU(s) list:   0-15
      Thread(s) per core:    1
      Core(s) per socket:    4
      Socket(s):             4
      NUMA node(s):          8
      Vendor ID:             GenuineIntel
      CPU family:            6
      Model:                 85
```

(Continued on next page)

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Huawei

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SPECspeed2017_fp_base = 79.1

SPECspeed2017_fp_peak = 80.2

CPU2017 License: 3175　　　　　　　　　　　　　　　　**Test Date:**　　　　　May-2018
Test Sponsor:　　Huawei　　　　　　　　　　　　　　**Hardware Availability:** May-2018
Tested by:　　　　Huawei　　　　　　　　　　　　　　**Software Availability:**　Mar-2018

Platform Notes (Continued)

```
        Model name:             Intel(R) Xeon(R) Gold 5122 CPU @ 3.60GHz
        Stepping:               4
        CPU MHz:                3601.000
        CPU max MHz:            3601.0000
        CPU min MHz:            1200.0000
        BogoMIPS:               7200.08
        Virtualization:         VT-x
        L1d cache:              32K
        L1i cache:              32K
        L2 cache:               1024K
        L3 cache:               16896K
        NUMA node0 CPU(s):      0,2
        NUMA node1 CPU(s):      1,3
        NUMA node2 CPU(s):      4,6
        NUMA node3 CPU(s):      5,7
        NUMA node4 CPU(s):      8,10
        NUMA node5 CPU(s):      9,11
        NUMA node6 CPU(s):      12,14
        NUMA node7 CPU(s):      13,15
        Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
    pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
    lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
    aperfmperf eagerfpu pni pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
    xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
    avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pln pts dtherm
    intel_pt rsb_ctxsw spec_ctrl stibp retpoline kaiser tpr_shadow vnmi flexpriority ept
    vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx avx512f
    avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec
    xgetbv1 cqm_llc cqm_occup_llc

 /proc/cpuinfo cache data
    cache size : 16896 KB

 From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
 physical chip.
    available: 8 nodes (0-7)
    node 0 cpus: 0 2
    node 0 size: 191756 MB
    node 0 free: 189068 MB
    node 1 cpus: 1 3
    node 1 size: 193524 MB
    node 1 free: 193137 MB
    node 2 cpus: 4 6
    node 2 size: 193524 MB
    node 2 free: 191198 MB
    node 3 cpus: 5 7
    node 3 size: 193524 MB
```

(Continued on next page)

Huawei

Huawei 5885H V5 (Intel Xeon Gold 5122)

$SPECspeed2017_fp_base = $ 79.1

$SPECspeed2017_fp_peak = $ 80.2

CPU2017 License: 3175　　　　　　　　　　　　　　　　　　**Test Date:** May-2018
Test Sponsor: Huawei　　　　　　　　　　　　　　　　　**Hardware Availability:** May-2018
Tested by: Huawei　　　　　　　　　　　　　　　　　　**Software Availability:** Mar-2018

Platform Notes (Continued)

```
   node 3 free: 192916 MB
   node 4 cpus: 8 10
   node 4 size: 193524 MB
   node 4 free: 192630 MB
   node 5 cpus: 9 11
   node 5 size: 193524 MB
   node 5 free: 193062 MB
   node 6 cpus: 12 14
   node 6 size: 193524 MB
   node 6 free: 192717 MB
   node 7 cpus: 13 15
   node 7 size: 193365 MB
   node 7 free: 192764 MB
   node distances:
   node   0   1   2   3   4   5   6   7
     0:  10  20  20  20  20  20  20  20
     1:  20  10  20  20  20  20  20  20
     2:  20  20  10  20  20  20  20  20
     3:  20  20  20  10  20  20  20  20
     4:  20  20  20  20  10  20  20  20
     5:  20  20  20  20  20  10  20  20
     6:  20  20  20  20  20  20  10  20
     7:  20  20  20  20  20  20  20  10

 From /proc/meminfo
    MemTotal:       1583378416 kB
    HugePages_Total:        0
    Hugepagesize:        2048 kB

 /usr/bin/lsb_release -d
    SUSE Linux Enterprise Server 12 SP2

 From /etc/*release* /etc/*version*
    SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 2
      # This file is deprecated and will be removed in a future service pack or release.
      # Please check /etc/os-release for details about this release.
    os-release:
      NAME="SLES"
      VERSION="12-SP2"
      VERSION_ID="12.2"
      PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
      ID="sles"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:12:sp2"
```

(Continued on next page)

Huawei

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Platform Notes (Continued)

```
  uname -a:
     Linux linux-scjg 4.4.120-92.70-default #1 SMP Wed Mar 14 15:59:43 UTC 2018 (52a83de)
     x86_64 x86_64 x86_64 GNU/Linux

  run-level 5 May 21 09:49

  SPEC is set to: /home/cpu2017
     Filesystem      Type   Size   Used Avail Use% Mounted on
     /dev/sda4       btrfs  717G   29G  687G   4% /home

  Additional information from dmidecode follows.  WARNING: Use caution when you interpret
  this section. The 'dmidecode' program reads system data which is "intended to allow
  hardware to be accurately determined", but the intent may not be met, as there are
  frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
    BIOS INSYDE Corp. 0.84 03/26/2018
    Memory:
     48x Hynix HMA84GR7AFR4N-VK 32 GB 2 rank 2666

  (End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================
  CC   619.lbm_s(base) 638.imagick_s(base, peak) 644.nab_s(base, peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
  CC   619.lbm_s(peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
  FC   607.cactuBSSN_s(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
```

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Test Sponsor: Huawei　　　　　　　　　　　　　　　　　　**Hardware Availability:** May-2018
Tested by: Huawei　　　　　　　　　　　　　　　　　　　**Software Availability:** Mar-2018

Compiler Version Notes (Continued)

```
--------------------------------------------------------------------------

==========================================================================
FC   607.cactuBSSN_s(peak)
--------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
--------------------------------------------------------------------------

==========================================================================
 FC   603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)
--------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
--------------------------------------------------------------------------

==========================================================================
FC   603.bwaves_s(peak) 649.fotonik3d_s(peak) 654.roms_s(peak)
--------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
--------------------------------------------------------------------------

==========================================================================
 CC   621.wrf_s(base) 627.cam4_s(base, peak) 628.pop2_s(base)
--------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
--------------------------------------------------------------------------

==========================================================================
CC   621.wrf_s(peak) 628.pop2_s(peak)
--------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
--------------------------------------------------------------------------
```

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Base Compiler Invocation

C benchmarks:
`icc`

Fortran benchmarks:
`ifort`

Benchmarks using both Fortran and C:
`ifort icc`

Benchmarks using Fortran, C, and C++:
`icpc icc ifort`

Base Portability Flags

603.bwaves_s: `-DSPEC_LP64`
607.cactuBSSN_s: `-DSPEC_LP64`
619.lbm_s: `-DSPEC_LP64`
621.wrf_s: `-DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian`
627.cam4_s: `-DSPEC_LP64 -DSPEC_CASE_FLAG`
628.pop2_s: `-DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian`
`-assume byterecl`
638.imagick_s: `-DSPEC_LP64`
644.nab_s: `-DSPEC_LP64`
649.fotonik3d_s: `-DSPEC_LP64`
654.roms_s: `-DSPEC_LP64`

Base Optimization Flags

C benchmarks:
`-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch`
`-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP`

Fortran benchmarks:
`-DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch`
`-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp`
`-nostandard-realloc-lhs -align array32byte`

Benchmarks using both Fortran and C:
`-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch`
`-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP`
`-nostandard-realloc-lhs -align array32byte`

(Continued on next page)

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Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:
```
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte
```

Base Other Flags

C benchmarks:
```
-m64 -std=c11
```

Fortran benchmarks:
```
-m64
```

Benchmarks using both Fortran and C:
```
-m64 -std=c11
```

Benchmarks using Fortran, C, and C++:
```
-m64 -std=c11
```

Peak Compiler Invocation

C benchmarks:
```
icc
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
ifort icc
```

Benchmarks using Fortran, C, and C++:
```
icpc icc ifort
```

Peak Portability Flags

```
Same as Base Portability Flags
```

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Peak Optimization Flags

C benchmarks:

619.lbm_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP

638.imagick_s: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP

644.nab_s: Same as 638.imagick_s

Fortran benchmarks:
-prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP
-DSPEC_OPENMP -O2 -xCORE-AVX512 -qopt-prefetch -ipo -O3
-ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:

621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

627.cam4_s: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

628.pop2_s: Same as 621.wrf_s

Benchmarks using Fortran, C, and C++:
-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512 -qopt-prefetch
-ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs
-align array32byte

Peak Other Flags

C benchmarks:
-m64 -std=c11

(Continued on next page)

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Test Sponsor: Huawei
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Test Date: May-2018
Hardware Availability: May-2018
Software Availability: Mar-2018

Peak Other Flags (Continued)

Fortran benchmarks:
`-m64`

Benchmarks using both Fortran and C:
`-m64 -std=c11`

Benchmarks using Fortran, C, and C++:
`-m64 -std=c11`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html`
`http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.7.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml`
`http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.7.xml`

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2018-05-21 03:22:51-0400.
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