



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017_fp_base = 527

SPECrate®2017_fp_peak = 528

CPU2017 License: 55

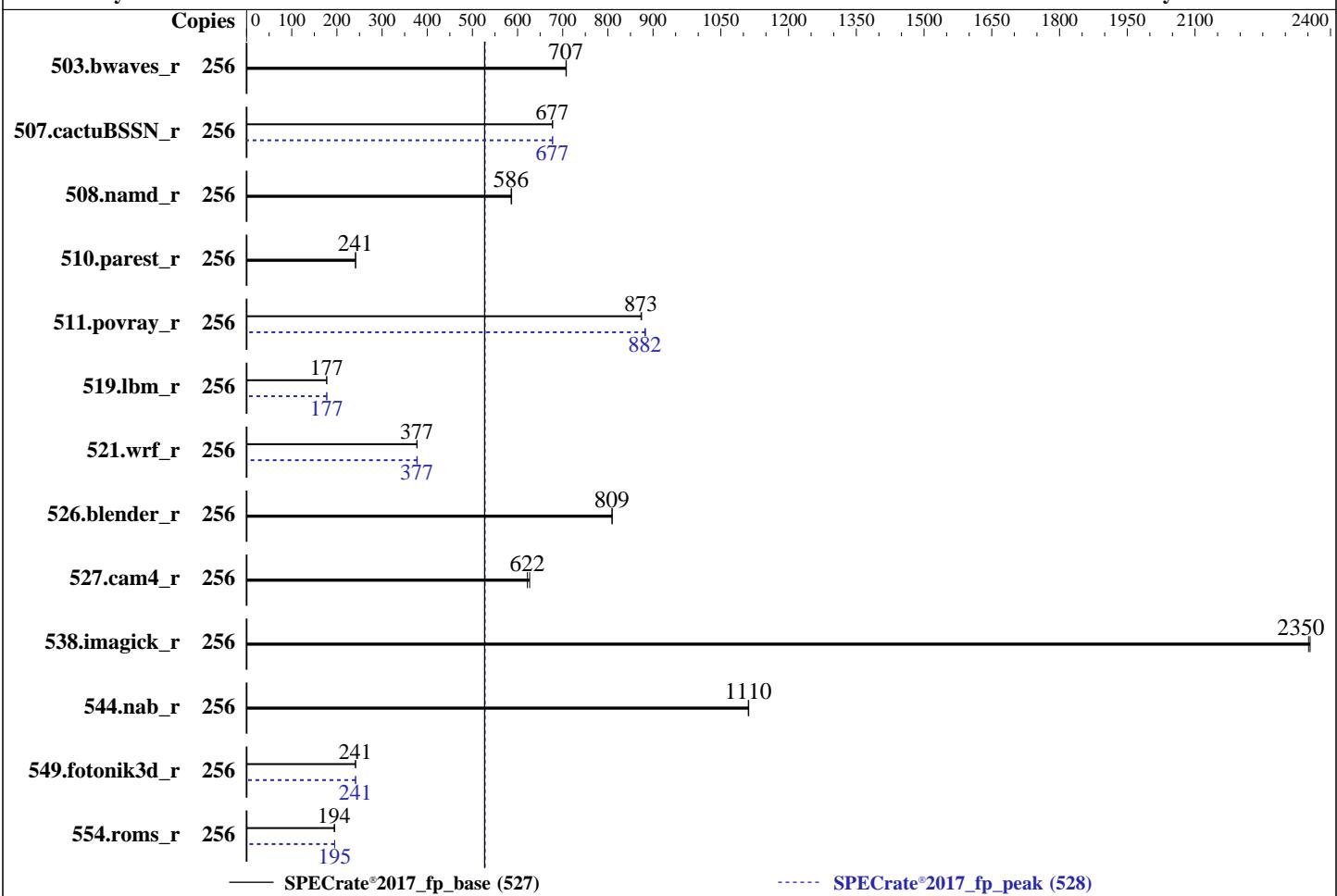
Test Date: Jul-2020

Test Sponsor: Dell Inc.

Hardware Availability: Feb-2020

Tested by: Dell Inc.

Software Availability: Jun-2020



Hardware

CPU Name: AMD EPYC 7662
 Max MHz: 3300
 Nominal: 2000
 Enabled: 128 cores, 2 chips, 2 threads/core
 Orderable: 1,2 chips
 Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 512 KB I+D on chip per core
 L3: 256 MB I+D on chip per chip, 16 MB shared / 4 cores
 Other: None
 Memory: 1 TB (16 x 64 GB 4Rx4 PC4-3200AA-R)
 Storage: 1 x 512GB SATA SSD
 Other: None

Software

OS: Ubuntu 20.04 LTS
 Compiler: kernel version
 Parallel: 5.4.0-40-generic
 Firmware: C/C++/Fortran: Version 2.0.0 of AOCC
 File System: No
 System State: Version 1.4.8 released May-2020
 Base Pointers: tmpfs
 Peak Pointers: System State: Run level 5 (multi-user)
 Other: 64-bit
 Power Management: 64-bit
 jemalloc: jemalloc memory allocator library v5.2.0
 BIOS set to prefer performance at the cost of additional power usage.



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PowerEdge R6525 (AMD EPYC 7662, 2.00 GHz)

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Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	256	3626	708	<u>3630</u>	<u>707</u>			256	3626	708	<u>3630</u>	<u>707</u>				
507.cactusBSSN_r	256	478	678	<u>479</u>	<u>677</u>			256	478	678	<u>479</u>	<u>677</u>				
508.namd_r	256	414	587	<u>415</u>	<u>586</u>			256	414	587	<u>415</u>	<u>586</u>				
510.parest_r	256	<u>2778</u>	<u>241</u>	2773	241			256	<u>2778</u>	<u>241</u>	2773	241				
511.povray_r	256	<u>684</u>	<u>873</u>	684	875			256	<u>677</u>	<u>883</u>	<u>678</u>	<u>882</u>				
519.lbm_r	256	1521	177	<u>1521</u>	<u>177</u>			256	<u>1520</u>	<u>177</u>	1519	178				
521.wrf_r	256	<u>1522</u>	<u>377</u>	1518	378			256	1518	378	<u>1521</u>	<u>377</u>				
526.blender_r	256	482	810	<u>482</u>	<u>809</u>			256	482	810	<u>482</u>	<u>809</u>				
527.cam4_r	256	714	627	<u>720</u>	<u>622</u>			256	714	627	<u>720</u>	<u>622</u>				
538.imagick_r	256	270	2350	<u>271</u>	<u>2350</u>			256	270	2350	<u>271</u>	<u>2350</u>				
544.nab_r	256	388	1110	<u>388</u>	<u>1110</u>			256	388	1110	<u>388</u>	<u>1110</u>				
549.fotonik3d_r	256	<u>4136</u>	<u>241</u>	4135	241			256	4134	241	<u>4136</u>	<u>241</u>				
554.roms_r	256	<u>2098</u>	<u>194</u>	2085	195			256	2086	195	<u>2086</u>	<u>195</u>				

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
 'numactl' was used to bind copies to the cores.
 See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
 'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
 numactl --interleave=all runcpu <etc>

Set dirty_ratio=8 to limit dirty cache to 8% of memory
 Set swappiness=1 to swap only if necessary
 Set zone_reclaim_mode=1 to free local node memory and avoid remote memory sync then drop_caches=3 to reset caches before invoking runcpu

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Operating System Notes (Continued)

dirty_ratio, swappiness, zone_reclaim_mode and drop_caches were all set using privileged echo (e.g. echo 1 > /proc/sys/vm/swappiness).

Transparent huge pages set to 'always' for this run (OS default)

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
    "/mnt/ramdisk/cpu2017-1.1.0/amd_rate_aocc200_rome_C_lib/64;/mnt/ramdisk/
     cpu2017-1.1.0/amd_rate_aocc200_rome_C_lib/32:"
MALLOC_CONF = "retain:true"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 7601 CPU + 512GB Memory using Fedora 26

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 225 GB ramdisk created with the cmd: "mount -t tmpfs -o size=225G tmpfs /mnt/ramdisk"
jemalloc: configured and built with GCC v9.1.0 in Ubuntu 19.04 with -O3 -znver2 -flio
jemalloc 5.2.0 is available here:
<https://github.com/jemalloc/jemalloc/releases/download/5.2.0/jemalloc-5.2.0.tar.bz2>

Platform Notes

C-States enabled

NUMA Nodes Per Socket set to 4

Virtualization Technology disabled

Memory Interleaving disabled

L3 cache as NUMA Domain enabled

L2 Stream HW prefetcher disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.0/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edb1e6e46a485a0011
running on ubuntu-2004 Sun Jul 19 05:47:29 2020

SUT (System Under Test) info as seen by some common utilities.

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Platform Notes (Continued)

For more information on this section, see
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : AMD EPYC 7662 64-Core Processor
2 "physical id"s (chips)
256 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 64
siblings : 128
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52
53 54 55 56 57 58 59 60 61 62 63
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52
53 54 55 56 57 58 59 60 61 62 63

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
Address sizes: 43 bits physical, 48 bits virtual
CPU(s): 256
On-line CPU(s) list: 0-255
Thread(s) per core: 2
Core(s) per socket: 64
Socket(s): 2
NUMA node(s): 32
Vendor ID: AuthenticAMD
CPU family: 23
Model: 49
Model name: AMD EPYC 7662 64-Core Processor
Stepping: 0
CPU MHz: 1786.784
BogoMIPS: 3992.68
Virtualization: AMD-V
L1d cache: 4 MiB
L1i cache: 4 MiB
L2 cache: 64 MiB
L3 cache: 512 MiB
NUMA node0 CPU(s): 0-3,128-131
NUMA node1 CPU(s): 4-7,132-135
NUMA node2 CPU(s): 8-11,136-139
NUMA node3 CPU(s): 12-15,140-143
NUMA node4 CPU(s): 16-19,144-147
NUMA node5 CPU(s): 20-23,148-151

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Platform Notes (Continued)

NUMA node6 CPU(s):	24-27,152-155
NUMA node7 CPU(s):	28-31,156-159
NUMA node8 CPU(s):	32-35,160-163
NUMA node9 CPU(s):	36-39,164-167
NUMA node10 CPU(s):	40-43,168-171
NUMA node11 CPU(s):	44-47,172-175
NUMA node12 CPU(s):	48-51,176-179
NUMA node13 CPU(s):	52-55,180-183
NUMA node14 CPU(s):	56-59,184-187
NUMA node15 CPU(s):	60-63,188-191
NUMA node16 CPU(s):	64-67,192-195
NUMA node17 CPU(s):	68-71,196-199
NUMA node18 CPU(s):	72-75,200-203
NUMA node19 CPU(s):	76-79,204-207
NUMA node20 CPU(s):	80-83,208-211
NUMA node21 CPU(s):	84-87,212-215
NUMA node22 CPU(s):	88-91,216-219
NUMA node23 CPU(s):	92-95,220-223
NUMA node24 CPU(s):	96-99,224-227
NUMA node25 CPU(s):	100-103,228-231
NUMA node26 CPU(s):	104-107,232-235
NUMA node27 CPU(s):	108-111,236-239
NUMA node28 CPU(s):	112-115,240-243
NUMA node29 CPU(s):	116-119,244-247
NUMA node30 CPU(s):	120-123,248-251
NUMA node31 CPU(s):	124-127,252-255
Vulnerability Itlb multihit:	Not affected
Vulnerability Llft:	Not affected
Vulnerability Mds:	Not affected
Vulnerability Meltdown:	Not affected
Vulnerability Spec store bypass:	Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1:	Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2:	Mitigation; Full AMD retpoline, IBPB conditional, IBRS_FW, STIBP conditional, RSB filling
Vulnerability Srbds:	Not affected
Vulnerability Tsx async abort:	Not affected
Flags:	fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperf_fmpf_pni pclmulqdq monitor ssse3 fma cx16 sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_13 cdp_13 hw_pstate sme ssbd mba sev ibrs ibpb stibp vmmcall fsqsbbase bmi1 avx2 smep bmi2 cqmq rdt_a rdseed adx smap clflushopt clwb sha_ni xsaveopt xsavec xgetbv1 xsaves cqmq_llc cqmq_occu_llc cqmq_mbm_total

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Platform Notes (Continued)

```
cqm_mbm_local clzero irperf xsaveerptr wbnoinvd arat npt lbrv svm_lock nrrip_save  
tsc_scale vmcb_clean flushbyasid decodeassists pausefilter pfthreshold avic  
v_vmsave_vmload vgif umip rdpid overflow_recov succor smca
```

```
/proc/cpuinfo cache data  
cache size : 512 KB
```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```
available: 32 nodes (0-31)  
node 0 cpus: 0 1 2 3 128 129 130 131  
node 0 size: 31867 MB  
node 0 free: 31470 MB  
node 1 cpus: 4 5 6 7 132 133 134 135  
node 1 size: 32254 MB  
node 1 free: 27392 MB  
node 2 cpus: 8 9 10 11 136 137 138 139  
node 2 size: 32254 MB  
node 2 free: 31898 MB  
node 3 cpus: 12 13 14 15 140 141 142 143  
node 3 size: 32253 MB  
node 3 free: 31872 MB  
node 4 cpus: 16 17 18 19 144 145 146 147  
node 4 size: 32254 MB  
node 4 free: 31907 MB  
node 5 cpus: 20 21 22 23 148 149 150 151  
node 5 size: 32254 MB  
node 5 free: 31906 MB  
node 6 cpus: 24 25 26 27 152 153 154 155  
node 6 size: 32254 MB  
node 6 free: 31903 MB  
node 7 cpus: 28 29 30 31 156 157 158 159  
node 7 size: 32253 MB  
node 7 free: 31902 MB  
node 8 cpus: 32 33 34 35 160 161 162 163  
node 8 size: 32254 MB  
node 8 free: 31901 MB  
node 9 cpus: 36 37 38 39 164 165 166 167  
node 9 size: 32254 MB  
node 9 free: 31908 MB  
node 10 cpus: 40 41 42 43 168 169 170 171  
node 10 size: 32254 MB  
node 10 free: 31903 MB  
node 11 cpus: 44 45 46 47 172 173 174 175  
node 11 size: 32253 MB  
node 11 free: 31903 MB  
node 12 cpus: 48 49 50 51 176 177 178 179
```

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Platform Notes (Continued)

```
node 12 size: 32254 MB
node 12 free: 31831 MB
node 13 cpus: 52 53 54 55 180 181 182 183
node 13 size: 32254 MB
node 13 free: 31891 MB
node 14 cpus: 56 57 58 59 184 185 186 187
node 14 size: 32254 MB
node 14 free: 31869 MB
node 15 cpus: 60 61 62 63 188 189 190 191
node 15 size: 32216 MB
node 15 free: 31860 MB
node 16 cpus: 64 65 66 67 192 193 194 195
node 16 size: 32254 MB
node 16 free: 31876 MB
node 17 cpus: 68 69 70 71 196 197 198 199
node 17 size: 32254 MB
node 17 free: 31895 MB
node 18 cpus: 72 73 74 75 200 201 202 203
node 18 size: 32254 MB
node 18 free: 31897 MB
node 19 cpus: 76 77 78 79 204 205 206 207
node 19 size: 32253 MB
node 19 free: 31905 MB
node 20 cpus: 80 81 82 83 208 209 210 211
node 20 size: 32254 MB
node 20 free: 31869 MB
node 21 cpus: 84 85 86 87 212 213 214 215
node 21 size: 32254 MB
node 21 free: 31903 MB
node 22 cpus: 88 89 90 91 216 217 218 219
node 22 size: 32254 MB
node 22 free: 31892 MB
node 23 cpus: 92 93 94 95 220 221 222 223
node 23 size: 32253 MB
node 23 free: 31907 MB
node 24 cpus: 96 97 98 99 224 225 226 227
node 24 size: 32254 MB
node 24 free: 31906 MB
node 25 cpus: 100 101 102 103 228 229 230 231
node 25 size: 32254 MB
node 25 free: 31884 MB
node 26 cpus: 104 105 106 107 232 233 234 235
node 26 size: 32254 MB
node 26 free: 31907 MB
node 27 cpus: 108 109 110 111 236 237 238 239
node 27 size: 32253 MB
node 27 free: 31908 MB
```

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```
node 28 cpus: 112 113 114 115 240 241 242 243
node 28 size: 32254 MB
node 28 free: 31911 MB
node 29 cpus: 116 117 118 119 244 245 246 247
node 29 size: 32254 MB
node 29 free: 31908 MB
node 30 cpus: 120 121 122 123 248 249 250 251
node 30 size: 32254 MB
node 30 free: 31911 MB
node 31 cpus: 124 125 126 127 252 253 254 255
node 31 size: 32250 MB
node 31 free: 31905 MB
node distances:
node 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19
20 21 22 23 24 25 26 27 28 29 30 31
 0: 10 11 11 11 12 12 12 12 12 12 12 12 12 12 12 12 32 32 32
 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32
 1: 11 10 11 11 12 12 12 12 12 12 12 12 12 12 12 12 12 12 32 32
 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32
 2: 11 11 10 11 12 12 12 12 12 12 12 12 12 12 12 12 12 12 32 32
 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32
 3: 11 11 11 10 12 12 12 12 12 12 12 12 12 12 12 12 12 12 32 32
 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32
 4: 12 12 12 12 10 11 11 11 11 12 12 12 12 12 12 12 12 12 32 32
 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32
 5: 12 12 12 12 12 11 10 11 11 12 12 12 12 12 12 12 12 12 32 32
 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32
 6: 12 12 12 12 12 11 11 10 11 12 12 12 12 12 12 12 12 12 32 32
 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32
 7: 12 12 12 12 12 11 11 11 10 12 12 12 12 12 12 12 12 12 32 32
 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32
 8: 12 12 12 12 12 12 12 12 12 12 10 11 11 11 11 11 12 12 32 32
 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32
 9: 12 12 12 12 12 12 12 12 12 12 11 10 11 11 11 11 12 12 32 32
 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32
10: 12 12 12 12 12 12 12 12 12 12 11 11 10 11 11 10 11 12 32 32
 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32
11: 12 12 12 12 12 12 12 12 12 12 11 11 11 11 11 10 10 12 32 32
 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32
12: 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 32 32
 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32
13: 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 32 32
 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32
14: 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 32 32
 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32
15: 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 32 32
 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32
```

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```

16: 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 10 11 11 11
12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 11 10 11 11 11
17: 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 11 10 11 11
12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 11 10 11 11 11
18: 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 11 11 10 11
12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 11 11 10 11 11
19: 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 11 11 11 10
12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 11 11 11 10
20: 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 12 12 12 12
10 11 11 11 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 11 12 12 12 12
21: 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 12 12 12 12
11 10 11 11 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 11 12 12 12 12
22: 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 12 12 12 12
11 11 10 11 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 11 12 12 12 12
23: 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 12 12 12 12
11 11 11 10 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 12 11 12 12 12 12
24: 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 12 12 12 12
12 12 12 12 10 11 11 11 11 11 12 12 12 12 12 12 12 12 12 12 12 11 12 12 12 12
25: 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 12 12 12 12
12 12 12 12 11 10 11 11 11 11 12 12 12 12 12 12 12 12 12 12 12 11 12 12 12 12
26: 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 12 12 12 12
12 12 12 12 11 11 10 11 11 11 12 12 12 12 12 12 12 12 12 12 12 11 12 12 12 12
27: 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 12 12 12 12
12 12 12 12 11 11 11 10 12 12 12 12 12 12 12 12 12 12 12 12 12 11 12 12 12 12
28: 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 12 12 12 12
12 12 12 12 12 12 12 12 12 12 10 11 11 11 11 11 11 11 11 11 11 12 12 12 12 12
29: 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 12 12 12 12
12 12 12 12 12 12 12 12 12 12 12 11 10 11 11 11 11 11 11 11 11 12 12 12 12 12
30: 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 12 12 12 12
12 12 12 12 12 12 12 12 12 12 12 11 11 10 10 11 11 11 11 11 11 12 12 12 12 12
31: 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 32 12 12 12 12
12 12 12 12 12 12 12 12 12 12 12 11 11 11 10 10 11 11 11 11 10

```

From /proc/meminfo

```

MemTotal:      1056460204 kB
HugePages_Total:      0
Hugepagesize:     2048 kB

```

```
/usr/bin/lsb_release -d
Ubuntu 20.04 LTS
```

```
From /etc/*release* /etc/*version*
debian_version: bullseye/sid
os-release:
NAME="Ubuntu"
VERSION="20.04 LTS (Focal Fossa)"
ID=ubuntu
```

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```
ID_LIKE=debian
PRETTY_NAME="Ubuntu 20.04 LTS"
VERSION_ID="20.04"
HOME_URL="https://www.ubuntu.com/"
SUPPORT_URL="https://help.ubuntu.com/"
```

uname -a:

```
Linux ubuntu-2004 5.4.0-40-generic #44-Ubuntu SMP Tue Jun 23 00:01:04 UTC 2020 x86_64
x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

itlb_multihit:	Not affected
CVE-2018-3620 (L1 Terminal Fault):	Not affected
Microarchitectural Data Sampling:	Not affected
CVE-2017-5754 (Meltdown):	Not affected
CVE-2018-3639 (Speculative Store Bypass):	Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):	Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2):	Mitigation: Full AMD retpoline, IBPB: conditional, IBRS_FW, STIBP: conditional, RSB filling
srbds:	Not affected
tsx_async_abort:	Not affected

run-level 5 Jul 18 23:10

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.0

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
tmpfs	tmpfs	256G	4.3G	252G	2%	/mnt/ramdisk

From /sys/devices/virtual/dmi/id

BIOS:	Dell Inc.	1.4.8	05/06/2020
Vendor:	Dell Inc.		
Product:	PowerEdge	R6525	
Product Family:	PowerEdge		
Serial:	C3JVPX2		

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

5x	802C80B3802C	36ASF8G72PZ-3G2B2	64 GB	2 rank	3200
11x	802C8632802C	36ASF8G72PZ-3G2B2	64 GB	2 rank	3200
16x	Not Specified	Not Specified			

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SPEC CPU®2017 Floating Point Rate Result

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Dell Inc.

PowerEdge R6525 (AMD EPYC 7662, 2.00 GHz)

SPECrate®2017_fp_base = 527

SPECrate®2017_fp_peak = 528

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Jul-2020
Hardware Availability: Feb-2020
Software Availability: Jun-2020

Platform Notes (Continued)

(End of data from sysinfo program)

Compiler Version Notes

```
=====
C           | 519.lbm_r(base, peak) 538.imagick_r(base, peak)
           | 544.nab_r(base, peak)
-----
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
-----

=====
C++          | 508.namd_r(base, peak) 510.parest_r(base, peak)
-----
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
-----

=====
C++, C       | 511.povray_r(base, peak) 526.blender_r(base, peak)
-----
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
-----

=====
C++, C, Fortran | 507.cactusBSSN_r(base, peak)
-----
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
```

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Software Availability: Jun-2020

Compiler Version Notes (Continued)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

=====

Fortran	503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak)
	554.roms_r(base, peak)

AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

=====

Fortran, C	521.wrf_r(base, peak) 527.cam4_r(base, peak)
------------	----------------------------------------------

AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

Base Compiler Invocation

C benchmarks:

clang

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

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Dell Inc. PowerEdge R6525 (AMD EPYC 7662, 2.00 GHz)	SPECrate®2017_fp_base = 527 SPECrate®2017_fp_peak = 528
CPU2017 License: 55 Test Sponsor: Dell Inc. Tested by: Dell Inc.	Test Date: Jul-2020 Hardware Availability: Feb-2020 Software Availability: Jun-2020

Base Compiler Invocation (Continued)

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Base Portability Flags

```
503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_CASE_FLAG -Mbyteswapi -DSPEC_LP64
526.blender_r: -funsigned-char -D__BOOL_DEFINED -DSPEC_LP64
527.cam4_r: -DSPEC_CASE_FLAG -DSPEC_LP64
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64
```

Base Optimization Flags

C benchmarks:

```
-fsto -Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math  
-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50  
-fremap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist  
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp  
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000  
-flv-function-specialization -z muldefs -lmvec -lamdlibm -ljemalloc
```

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SPECrate®2017_fp_base = 527

SPECrate®2017_fp_peak = 528

Test Date: Jul-2020

Hardware Availability: Feb-2020

Software Availability: Jun-2020

Base Optimization Flags (Continued)

C benchmarks (continued):

-lflang

C++ benchmarks:

```
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2  
-mllvm -loop-unswitch-threshold=200000 -mllvm -vector-library=LIBMVEC  
-mllvm -unroll-threshold=100 -flv-function-specialization  
-mllvm -enable-partial-unswitch -z muldefs -lmvec -lamdlibm  
-ljemalloc -lflang
```

Fortran benchmarks:

```
-flto -Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver2  
-funroll-loops -Mrecursive -mllvm -vector-library=LIBMVEC -z muldefs  
-Kieee -fno-finite-math-only -lmvec -lamdlibm -ljemalloc -lflang
```

Benchmarks using both Fortran and C:

```
-flto -Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math  
-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50  
-fremap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist  
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp  
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000  
-flv-function-specialization -funroll-loops -Mrecursive -z muldefs  
-Kieee -fno-finite-math-only -lmvec -lamdlibm -ljemalloc -lflang
```

Benchmarks using both C and C++:

```
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2  
-fstruct-layout=3 -mllvm -unroll-threshold=50 -fremap-arrays  
-mllvm -function-specialize -mllvm -enable-gvn-hoist  
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp  
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000  
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000  
-mllvm -unroll-threshold=100 -mllvm -enable-partial-unswitch -z muldefs  
-lmvec -lamdlibm -ljemalloc -lflang
```

Benchmarks using Fortran, C, and C++:

```
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
```

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Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):

```
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2  
-fstruct-layout=3 -mllvm -unroll-threshold=50 -fremap-arrays  
-mllvm -function-specialize -mllvm -enable-gvn-hoist  
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp  
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000  
-fiv-function-specialization -mllvm -loop-unswitch-threshold=200000  
-mllvm -unroll-threshold=100 -mllvm -enable-partial-unswitch  
-funroll-loops -Mrecursive -z muldefs -Kieee -fno-finite-math-only  
-lmvec -lamdlibm -ljemalloc -lflang
```

Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags



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SPECrate®2017_fp_base = 527

SPECrate®2017_fp_peak = 528

Test Date: Jul-2020

Hardware Availability: Feb-2020

Software Availability: Jun-2020

Peak Optimization Flags

C benchmarks:

```
519.lbm_r: -flto -Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-region-vectorize  
-Wl,-mllvm -Wl,-vector-library=LIBMVEC  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast  
-march=znver2 -mno-sse4a -fstruct-layout=5  
-mllvm -vectorize-memory-aggressively  
-mllvm -function-specialize -mllvm -enable-gvn-hoist  
-mllvm -unroll-threshold=50 -fremap-arrays  
-mllvm -vector-library=LIBMVEC  
-mllvm -reduce-array-computations=3  
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000  
-flv-function-specialization -lmvec -lamdlibm -ljemalloc  
-lflang
```

```
538.imagick_r: basepeak = yes
```

```
544.nab_r: basepeak = yes
```

C++ benchmarks:

```
508.namd_r: basepeak = yes
```

```
510.parest_r: basepeak = yes
```

Fortran benchmarks:

```
503.bwaves_r: basepeak = yes
```

```
549.fotonik3d_r: -flto -Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-region-vectorize  
-Wl,-mllvm -Wl,-vector-library=LIBMVEC  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3  
-march=znver2 -funroll-loops -Mrecursive  
-mllvm -vector-library=LIBMVEC -Kieee  
-fno-finite-math-only -lmvec -lamdlibm -ljemalloc  
-lflang
```

```
554.roms_r: -flto -Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-region-vectorize  
-Wl,-mllvm -Wl,-vector-library=LIBMVEC  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver2  
-funroll-loops -Mrecursive -mllvm -vector-library=LIBMVEC  
-Kieee -fno-finite-math-only -lmvec -lamdlibm -ljemalloc
```

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Tested by: Dell Inc.

Software Availability: Jun-2020

Peak Optimization Flags (Continued)

554.roms_r (continued):

-lflang

Benchmarks using both Fortran and C:

```
521.wrf_r: -fsto -Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-region-vectorize  
-Wl,-mllvm -Wl,-vector-library=LIBMVEC  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast  
-march=znver2 -mno-sse4a -fstruct-layout=5  
-mllvm -vectorize-memory-aggressively  
-mllvm -function-specialize -mllvm -enable-gvn-hoist  
-mllvm -unroll-threshold=50 -fremap-arrays  
-mllvm -vector-library=LIBMVEC  
-mllvm -reduce-array-computations=3  
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000  
-flv-function-specialization -O3 -funroll-loops  
-Mrecursive -Kieee -fno-finite-math-only -lmvec  
-lamdlibm -ljemalloc -lflang
```

527.cam4_r: basepeak = yes

Benchmarks using both C and C++:

```
511.povray_r: -std=c++98 -fsto -Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-region-vectorize  
-Wl,-mllvm -Wl,-vector-library=LIBMVEC  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast  
-march=znver2 -mno-sse4a -fstruct-layout=5  
-mllvm -vectorize-memory-aggressively  
-mllvm -function-specialize -mllvm -enable-gvn-hoist  
-mllvm -unroll-threshold=50 -fremap-arrays  
-mllvm -vector-library=LIBMVEC  
-mllvm -reduce-array-computations=3  
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000  
-flv-function-specialization -mllvm -unroll-threshold=100  
-mllvm -enable-partial-unswitch  
-mllvm -loop-unswitch-threshold=200000 -lmvec -lamdlibm  
-ljemalloc -lflang
```

526.blender_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

```
-std=c++98 -fsto -Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
```

(Continued on next page)



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Peak Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):

```
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast -march=znver2  
-mno-sse4a -fstruct-layout=5 -mllvm -vectorize-memory-aggressively  
-mllvm -function-specialize -mllvm -enable-gvn-hoist  
-mllvm -unroll-threshold=50 -fremap-arrays  
-mllvm -vector-library=LIBMVEC -mllvm -reduce-array-computations=3  
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000  
-fvl-function-specialization -mllvm -unroll-threshold=100  
-mllvm -enable-partial-unswitch -mllvm -loop-unswitch-threshold=200000  
-O3 -funroll-loops -Mrecursive -Kieee -fno-finite-math-only -lmvec  
-lamdlibm -ljemalloc -lflang
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc200-flags-C4.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-revE12.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc200-flags-C4.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-revE12.xml>

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