



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant ML110 Gen11

(2.20 GHz, Intel Xeon Silver 4516Y+)

SPECspeed®2017_int_base = 13.5

SPECspeed®2017_int_peak = 13.8

CPU2017 License: 3

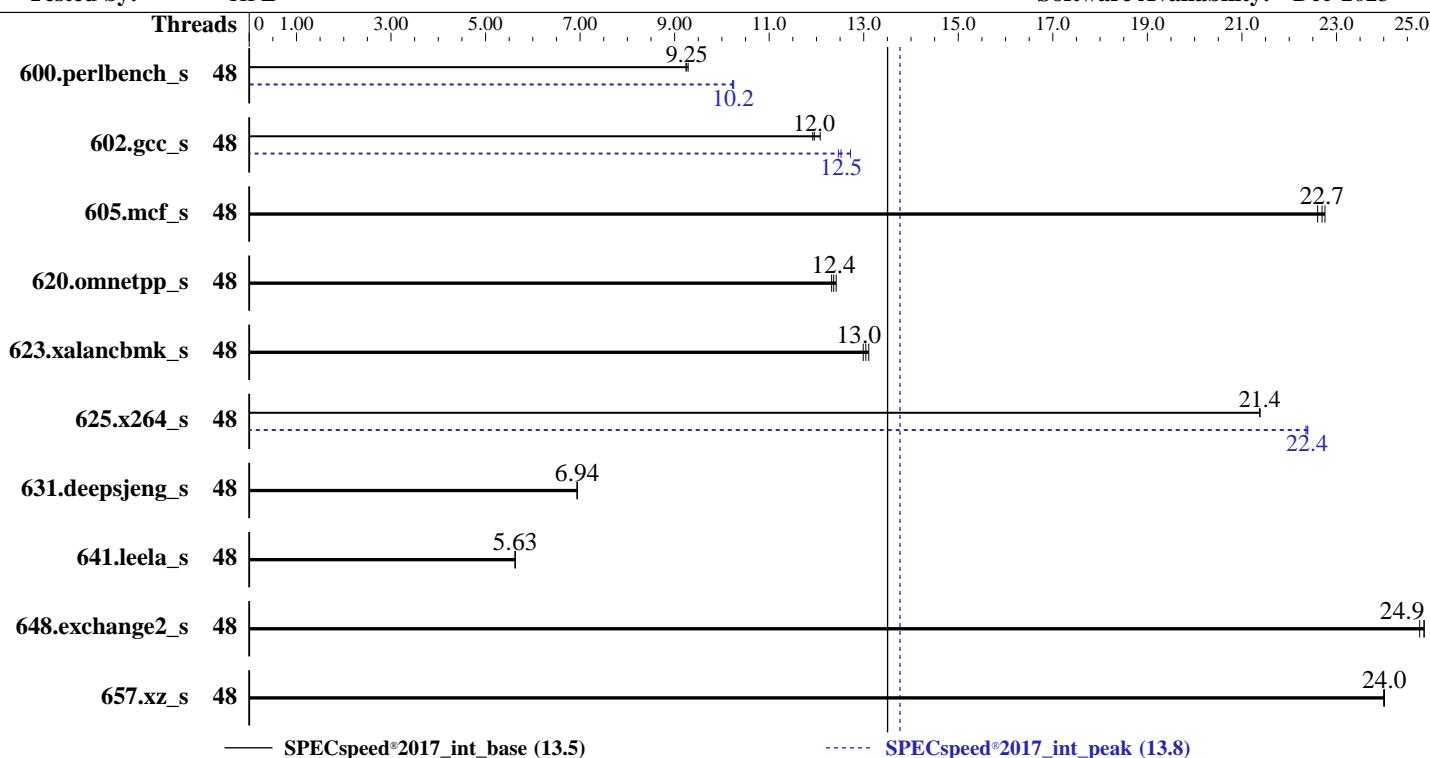
Test Date: Feb-2024

Test Sponsor: HPE

Hardware Availability: Feb-2024

Tested by: HPE

Software Availability: Dec-2023



Hardware

CPU Name: Intel Xeon Silver 4516Y+
 Max MHz: 3700
 Nominal: 2200
 Enabled: 24 cores, 1 chip, 2 threads/core
 Orderable: 1 chip
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 45 MB I+D on chip per chip
 Other: None
 Memory: 256 GB (8 x 32 GB 2Rx8 PC5-5600B-R, running at 4400)
 Storage: 1 x 240 GB SATA SSD
 Other: None

Software

OS: SUSE Linux Enterprise Server 15 SP5
 Compiler: Kernel 5.14.21-150500.53-default
 C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++ Compiler for Linux;
 Fortran: Version 2023.2.3 of Intel Fortran Compiler for Linux;
 Parallel: Yes
 Firmware: HPE BIOS Version v2.12 12/13/2023 released Dec-2023
 File System: btrfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: jemalloc memory allocator V5.0.1
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



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Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	48	191	9.29	192	9.25	192	9.24	48	174	10.2	173	10.2	173	10.2
602.gcc_s	48	330	12.1	334	11.9	333	12.0	48	319	12.5	318	12.5	313	12.7
605.mcf_s	48	208	22.7	207	22.8	209	22.6	48	208	22.7	207	22.8	209	22.6
620.omnetpp_s	48	132	12.3	131	12.4	132	12.4	48	132	12.3	131	12.4	132	12.4
623.xalancbmk_s	48	109	13.0	109	13.0	108	13.1	48	109	13.0	109	13.0	108	13.1
625.x264_s	48	82.5	21.4	82.5	21.4	82.5	21.4	48	78.9	22.4	78.8	22.4	78.8	22.4
631.deepsjeng_s	48	207	6.94	206	6.94	206	6.94	48	207	6.94	206	6.94	206	6.94
641.leela_s	48	303	5.62	303	5.63	303	5.63	48	303	5.62	303	5.63	303	5.63
648.exchange2_s	48	118	24.9	118	24.9	119	24.8	48	118	24.9	118	24.9	119	24.8
657.xz_s	48	258	24.0	257	24.0	257	24.0	48	258	24.0	257	24.0	257	24.0
SPECspeed®2017_int_base = 13.5														
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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
 Transparent Huge Pages enabled by default
 Prior to runcpu invocation
 Filesystem page cache synced and cleared with:
 sync; echo 3> /proc/sys/vm/drop_caches
 runcpu command invoked through numactl i.e.:
 numactl --interleave=all runcpu <etc>
 tuned service was stopped using "systemctl stop tuned"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
 KMP_AFFINITY = "granularity=fine,scatter"
 LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-64"
 MALLOC_CONF = "retain:true"
 OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

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General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

The system ROM used for this result contains Intel microcode version 0x21000200 for the Intel Xeon Silver 4516Y+ processor.

BIOS Configuration:

Workload Profile set to General Peak Frequency Compute

Enhanced Processor Performance Profile set to Aggressive

Thermal Configuration set to Maximum Cooling

Last Level Cache (LLC) Prefetch set to Enabled

Memory Patrol Scrubbing set to Disabled

```
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Mon Feb 12 02:56:35 2024
```

SUT (System Under Test) info as seen by some common utilities.

Table of contents

```
1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS
```

1. uname -a
Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043)
x86_64 x86_64 x86_64 GNU/Linux

2. w
02:56:35 up 2 min, 0 users, load average: 0.18, 0.11, 0.04

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Platform Notes (Continued)

USER	TTY	FROM	LOGIN@	IDLE	JCPU	PCPU	WHAT
------	-----	------	--------	------	------	------	------

3. Username
From environment variable \$USER: root

4. ulimit -a

core file size	(blocks, -c) unlimited
data seg size	(kbytes, -d) unlimited
scheduling priority	(-e) 0
file size	(blocks, -f) unlimited
pending signals	(-i) 1029877
max locked memory	(kbytes, -l) 64
max memory size	(kbytes, -m) unlimited
open files	(-n) 1024
pipe size	(512 bytes, -p) 8
POSIX message queues	(bytes, -q) 819200
real-time priority	(-r) 0
stack size	(kbytes, -s) unlimited
cpu time	(seconds, -t) unlimited
max user processes	(-u) 1029877
virtual memory	(kbytes, -v) unlimited
file locks	(-x) unlimited

5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize 29
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root@notty
bash -c cd $SPEC/ && $SPEC/intspeedGFS.sh
runcpu --nobuild --action validate --define default-platform-flags -c
  ic2023.2.3-lin-sapphirerapids-speed-20231121.cfg --define cores=48 --tune base,peak -o all --define
  intspeedaffinity --define drop_caches intspeed
runcpu --nobuild --action validate --define default-platform-flags --configfile
  ic2023.2.3-lin-sapphirerapids-speed-20231121.cfg --define cores=48 --tune base,peak --output_format all
  --define intspeedaffinity --define drop_caches --nopower --runmode speed --tune base:peak --size refspeed
  intspeed --nopreenv --note-preenv --logfile $SPEC/tmp/CPU2017.001/templogs/preenv.intspeed.001.0.log
  --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

6. /proc/cpuinfo

model name	: INTEL(R) XEON(R) SILVER 4516Y+
vendor_id	: GenuineIntel
cpu family	: 6
model	: 207
stepping	: 2
microcode	: 0x21000200
bugs	: spectre_v1 spectre_v2 spec_store_bypass swapgs eibrss_pbrss
cpu cores	: 24
siblings	: 48
1 physical ids (chips)	
48 processors (hardware threads)	
physical id 0: core ids 0-23	
physical id 0: apicids 0-47	

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

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Platform Notes (Continued)

7. lscpu

From lscpu from util-linux 2.37.4:

```

Architecture:           x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         46 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                48
On-line CPU(s) list:  0-47
Vendor ID:             GenuineIntel
Model name:            INTEL(R) XEON(R) SILVER 4516Y+
CPU family:            6
Model:                 207
Thread(s) per core:   2
Core(s) per socket:   24
Socket(s):             1
Stepping:              2
BogoMIPS:              4400.00
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                       clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                       lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                       nonstop_tsc cpuid aperf mperf tsc_known_freq pni pclmulqdq dtes64 monitor
                       ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1
                       sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
                       lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 cat_12 cdp_13
                       invpcid_single cdp_12 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow
                       vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep
                       bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
                       avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
                       xsaveopt xsavec xgetbv1 xsavec cqm_llc cqm_occup_llc cqm_mbm_total
                       cqm_mbm_local avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts hfi
                       avx512vbmi umip pkru ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq
                       avx512_vnni avx512_bitalg tme avx512_vpocndq la57 rdpid bus_lock_detect
                       cldemote movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig
                       arch_lbr avx512_fp16 amx_tile flush_l1d arch_capabilities
Virtualization:        VT-x
L1d cache:              1.1 MiB (24 instances)
L1i cache:              768 KiB (24 instances)
L2 cache:                48 MiB (24 instances)
L3 cache:                45 MiB (1 instance)
NUMA node(s):           1
NUMA node0 CPU(s):     0-47
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:      Not affected
Vulnerability Mds:       Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed:  Not affected
Vulnerability Spec store bypass: Mitigation: Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1:  Mitigation: usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:  Mitigation: Enhanced IBRS, IBPB conditional, RSB filling, PBRSB-eIBRS SW
                           sequence
Vulnerability Srbds:    Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	1.1M	12	Data	1	64	1	64
L1i	32K	768K	8	Instruction	1	64	1	64

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Platform Notes (Continued)

L2	2M	48M	16	Unified	2	2048	1	64
L3	45M	45M	15	Unified	3	49152	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.
available: 1 nodes (0)
node 0 cpus: 0-47
node 0 size: 257491 MB
node 0 free: 256740 MB
node distances:
node 0
0: 10

9. /proc/meminfo

MemTotal: 263671712 kB

10. who -r
run-level 3 Feb 12 02:54

11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)

Default Target Status
multi-user running

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	apparmor auditd cron getty@ irqbalance issue-generator kbdsettings lvm2-monitor postfix purge-kernels rollback sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime	systemd-remount-fs
disabled	blk-availability boot-sysctl ca-certificates chrony-wait chronyd console-getty debug-shell grub2-once haveged haveged-switch-root hwloc-dump-hwdata issue-add-ssh-keys kexec-load lunmask rpmconfigcheck serial-getty@ systemd-boot-check-no-failures systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd tuned
indirect	wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline

BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default
root=UUID=3bd5780a-e7c5-4370-8a0e-b4fd280ac947
splash=silent
mitigations=auto
quiet
security=apparmor

14. cpupower frequency-info

analyzing CPU 0:
 Unable to determine current policy
 boost state support:
 Supported: yes
 Active: yes

15. tuned-adm active

It seems that tuned daemon is not running, preset profile is not activated.

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Platform Notes (Continued)

Preset profile: throughput-performance

```
16. sysctl
kernel.numa_balancing          0
kernel.randomize_va_space       2
vm.compaction_proactiveness    20
vm.dirty_background_bytes       0
vm.dirty_background_ratio       10
vm.dirty_bytes                  0
vm.dirty_expire_centisecs      3000
vm.dirty_ratio                 20
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy       0
vm.nr_overcommit_hugepages     0
vm.swappiness                   60
vm.watermark_boost_factor      15000
vm.watermark_scale_factor       10
vm.zone_reclaim_mode           0
```

```
17. /sys/kernel/mm/transparent_hugepage
defrag           always defer defer+madvise [madvise] never
enabled          [always] madvise never
hpage_pmd_size  2097152
shmem_enabled    always within_size advise [never] deny force
```

```
18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                 1
max_ptes_none          511
max_ptes_shared         256
max_ptes_swap           64
pages_to_scan           4096
scan_sleep_millisecs   10000
```

```
19. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP5
```

```
20. Disk information
SPEC is set to: /home/cpu2017
Filesystem  Type  Size  Used Avail Use% Mounted on
/dev/sda3    btrfs  220G  89G  132G  41% /home
```

```
21. /sys/devices/virtual/dmi/id
Vendor:          HPE
Product:         ProLiant ML110 Gen11
Product Family:  ProLiant
Serial:          CNX2230TMQ
```

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Platform Notes (Continued)

22. dmidecode

Additional information from dmidecode 3.4 follows. **WARNING:** Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

8x Hynix HMCG88AGBRA193N 32 GB 2 rank 5600, configured at 4400

23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: HPE
BIOS Version: 2.12
BIOS Date: 12/13/2023
BIOS Revision: 2.12
Firmware Revision: 1.56

Compiler Version Notes

C | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak)
| 657.xz_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

C++ | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak)
| 641.leela_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Fortran | 648.exchange2_s(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx



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Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fopenmp  
-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs -align array32byte  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx



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Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
600.perlbench_s: -w -m64 -std=c11 -Wl,-z,muldefs  
-fprofile-generate(pass 1)  
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)  
-flto -Ofast(pass 1) -xCORE-AVX512 -O3 -ffast-math  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-fiopenmp -DSPEC_OPENMP -fno-strict-overflow  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

```
602.gcc_s: -w -m64 -std=c11 -Wl,-z,muldefs  
-fprofile-generate(pass 1)  
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)  
-flto -Ofast(pass 1) -xCORE-AVX512 -O3 -ffast-math  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-fiopenmp -DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib  
-ljemalloc
```

```
605.mcf_s: basepeak = yes
```

```
625.x264_s: -w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP  
-fno-alias -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

```
657.xz_s: basepeak = yes
```

C++ benchmarks:

```
620.omnetpp_s: basepeak = yes
```

```
623.xalancbmk_s: basepeak = yes
```

```
631.deepsjeng_s: basepeak = yes
```

```
641.leela_s: basepeak = yes
```

Fortran benchmarks:

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant ML110 Gen11

(2.20 GHz, Intel Xeon Silver 4516Y+)

SPECspeed®2017_int_base = 13.5

SPECspeed®2017_int_peak = 13.8

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Feb-2024

Hardware Availability: Feb-2024

Software Availability: Dec-2023

Peak Optimization Flags (Continued)

648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-EMR-rev1.0.html>
<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-EMR-rev1.0.xml>
<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml>

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