SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

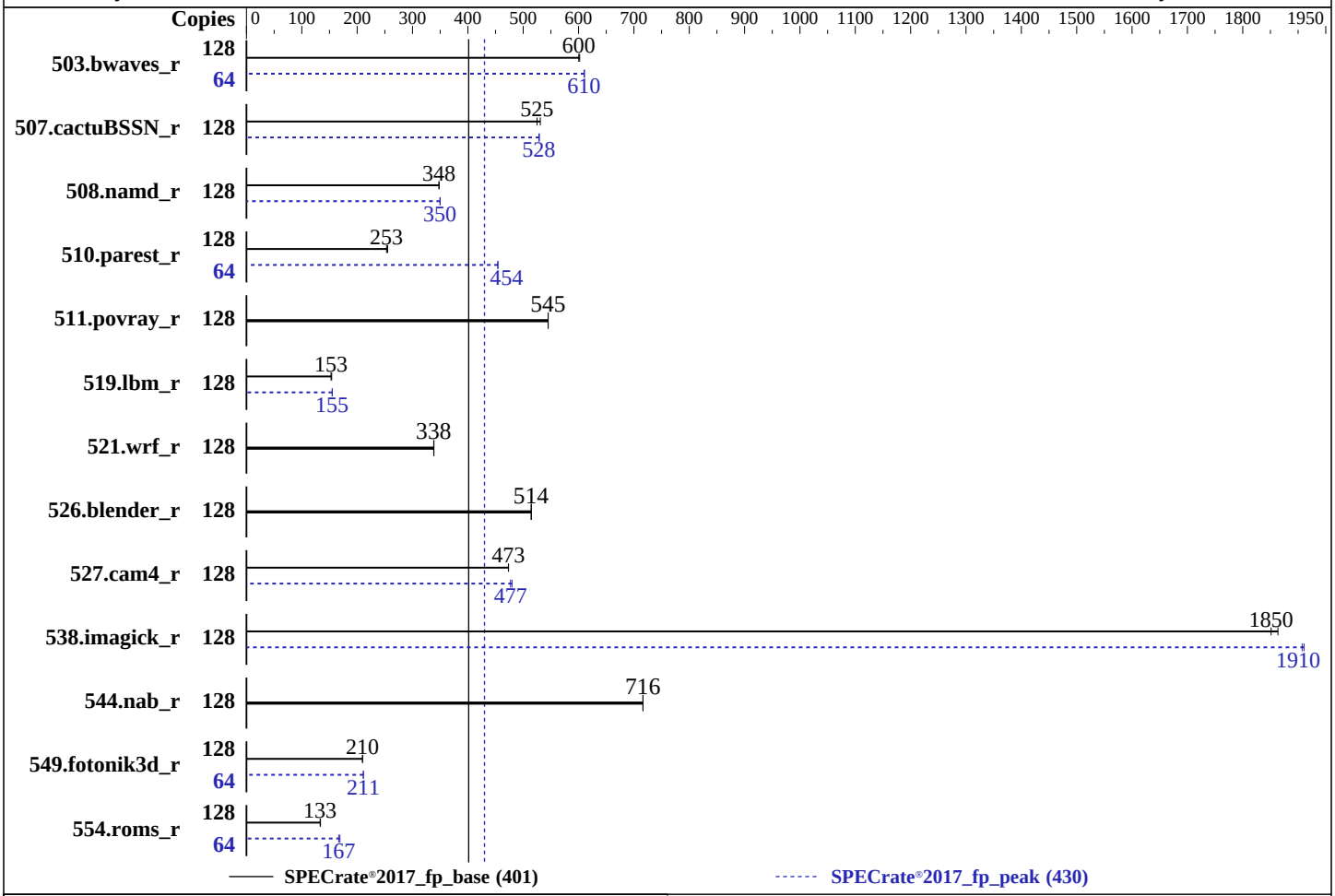
PowerEdge C6525 (AMD EPYC 7513 32-Core Processor)

SPECrate®2017_fp_base = 401

SPECrate®2017_fp_peak = 430

CPU2017 License: 6573
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Apr-2024
Hardware Availability: Jun-2021
Software Availability: Mar-2024



Hardware

CPU Name:	AMD EPYC 7513
Max MHz:	3650
Nominal:	2600
Enabled:	64 cores, 2 chips, 2 threads/core
Orderable:	1,2 chips
Cache L1:	32 KB I + 32 KB D on chip per core
L2:	512 KB I+D on chip per core
L3:	128 MB I+D on chip per chip, 32 MB shared / 8 cores
Other:	None
Memory:	512 GB (16 x 32 GB 2Rx8 PC4-3200AA-R)
Storage:	80 GB on tmpfs
Other:	CPU Cooling: Air

Software

OS:	Ubuntu 22.04.4 LTS 5.15.0-102-generic
Compiler:	C/C++/Fortran: Version 3.2.0 of AOCC
Parallel:	No
Firmware:	Version 2.5.6 released Oct-2021
File System:	tmpfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	64-bit
Other:	jemalloc: jemalloc memory allocator library v5.1.0
Power Management:	BIOS and OS set to prefer performance at the cost of additional power usage.

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	128	2132	602	**2138**	**600**			64	**1052**	**610**	1051	611		
507.cactuBSSN_r	128	305	531	**309**	**525**			128	306	529	**307**	**528**		
508.namd_r	128	**350**	**348**	349	348			128	347	350	**348**	**350**		
510.parest_r	128	1313	255	**1323**	**253**			64	**369**	**454**	368	454		
511.povray_r	128	**548**	**545**	548	545			128	**548**	**545**	548	545		
519.lbm_r	128	876	154	**883**	**153**			128	**871**	**155**	870	155		
521.wrf_r	128	**848**	**338**	846	339			128	**848**	**338**	846	339		
526.blender_r	128	**379**	**514**	379	515			128	**379**	**514**	379	515		
527.cam4_r	128	**473**	**473**	473	474			128	467	480	**469**	**477**		
538.imagick_r	128	**172**	**1850**	171	1860			128	167	1910	**167**	**1910**		
544.nab_r	128	**301**	**716**	301	716			128	**301**	**716**	301	716		
549.fotonik3d_r	128	2379	210	**2379**	**210**			64	1180	211	**1182**	**211**		
554.roms_r	128	**1528**	**133**	1521	134			64	602	169	**610**	**167**		

SPECrate®2017_fp_base = 401

SPECrate®2017_fp_peak = 430

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

```
The AMD64 AOCC Compiler Suite is available at
http://developer.amd.com/amd-aocc/
```

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
```

(Continued on next page)

Operating System Notes (Continued)

```
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
     "/mnt/ramdisk/cpu2017-1.1.9-aocc320-A1/amd_rate_aocc320_milanx_A_lib/lib;/mnt/ramdisk/cpu2017-1.1.9-ao
     cc320-A1/amd_rate_aocc320_milanx_A_lib/lib32:"
MALLOC_CONF = "retain:true"
```

General Notes

```
Binaries were compiled on a system with 2x AMD EPYC 7742 CPU + 1TiB Memory using OpenSUSE 15.2

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v4.8.2 in RHEL 7.4 (No options specified)
jemalloc 5.1.0 is available here:
https://github.com/jemalloc/jemalloc/releases/download/5.1.0/jemalloc-5.1.0.tar.bz2


 Benchmark run from a 80 GB ramdisk created with the cmd: "mount -t tmpfs -o size=80G tmpfs /mnt/ramdisk"
```

Platform Notes

```
 BIOS settings:
        DRAM Refresh Delay : Performance
       Memory Interleaving : Disabled
    DIMM Self Healing on
 Uncorrectable Memory Error : Disabled

  Virtualization Technology : Disabled
      NUMA Nodes per Socket : 4
    L3 cache as NUMA Domain : Enabled

             System Profile : Custom
       CPU Power Management : Maximum Performance
        Memory Patrol Scrub : Disabled
        PCI ASPM L1 Link
           Power Management : Disabled

 Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-aocc320-A1/bin/sysinfo
 Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
 running on amd-sut Thu Apr 11 01:47:47 2024

 SUT (System Under Test) info as seen by some common utilities.
```

(Continued on next page)

Platform Notes (Continued)

```
------------------------------------------------------------
Table of contents
------------------------------------------------------------
 1. uname -a
 2. w
 3. Username
 4. ulimit -a
 5. sysinfo process ancestry
 6. /proc/cpuinfo
 7. lscpu
 8. numactl --hardware
 9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.11-0ubuntu3.12)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. tuned-adm active
17. sysctl
18. /sys/kernel/mm/transparent_hugepage
19. /sys/kernel/mm/transparent_hugepage/khugepaged
20. OS release
21. Disk information
22. /sys/devices/virtual/dmi/id
23. dmidecode
24. BIOS
------------------------------------------------------------

------------------------------------------------------------
1. uname -a
  Linux amd-sut 5.15.0-102-generic #112-Ubuntu SMP Tue Mar 5 16:50:32 UTC 2024 x86_64 x86_64 x86_64 GNU/Linux

------------------------------------------------------------
2. w
   01:47:48 up  5:01,  1 user,  load average: 64.04, 110.20, 119.80
  USER     TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT
  root     tty1     -                20:48    4:59m  2.05s  0.46s /bin/bash ./amd_rate_aocc320_milanx_A1.sh

------------------------------------------------------------
3. Username
  From environment variable $USER:  root

------------------------------------------------------------
4. ulimit -a
  time(seconds)        unlimited
  file(blocks)         unlimited
  data(kbytes)         unlimited
  stack(kbytes)        unlimited
  coredump(blocks)     0
  memory(kbytes)       unlimited
  locked memory(kbytes) 2097152
  process              2061779
  nofiles              1024
  vmemory(kbytes)      unlimited
  locks                unlimited
  rtprio               0

------------------------------------------------------------
5. sysinfo process ancestry
```

(Continued on next page)

Platform Notes (Continued)

```
 /sbin/init
 /bin/login -p --
 -bash
 /bin/bash ./DELL_rate.sh
 /bin/bash /home/DellFiles/bin/dell-run-main.sh rate
 /bin/bash /home/DellFiles/bin/dell-run-main.sh rate
 /bin/bash /home/DellFiles/bin/AMD/dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_EPYC-3.inc --define
   DL-BIOS-LogProc=1 --define DL-VERS=5.2 --output_format html,pdf,txt
 python3 ./run_amd_rate_aocc320_milanx_A1.py
 /bin/bash ./amd_rate_aocc320_milanx_A1.sh
 runcpu --config amd_rate_aocc320_milanx_A1.cfg --tune all --reportable --iterations 2 --define
   DL-BIOS-L3NUMA=1 --define DL-BIOS-NPS=4 --define DL-BIOSinc=Dell-BIOS_EPYC-3.inc --define
   DL-BIOS-LogProc=1 --define DL-VERS=5.2 --output_format html,pdf,txt fprate
 runcpu --configfile amd_rate_aocc320_milanx_A1.cfg --tune all --reportable --iterations 2 --define
   DL-BIOS-L3NUMA=1 --define DL-BIOS-NPS=4 --define DL-BIOSinc=Dell-BIOS_EPYC-3.inc --define
   DL-BIOS-LogProc=1 --define DL-VERS=5.2 --output_format html,pdf,txt --nopower --runmode rate --tune
   base:peak --size test:train:refrate fprate --nopreenv --note-preenv --logfile
   $SPEC/tmp/CPU2017.002/templogs/preenv.fprate.002.0.log --lognum 002.0 --from_runcpu 2
 specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-aocc320-A1

------------------------------------------------------------
6. /proc/cpuinfo
    model name      : AMD EPYC 7513 32-Core Processor
    vendor_id       : AuthenticAMD
    cpu family      : 25
    model           : 1
    stepping        : 1
    microcode       : 0xa0011d1
    bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass srso
    TLB size        : 2560 4K pages
    cpu cores       : 32
    siblings        : 64
    2 physical ids (chips)
    128 processors (hardware threads)
    physical id 0: core ids 0-31
    physical id 1: core ids 0-31
    physical id 0: apicids 0-63
    physical id 1: apicids 64-127
  Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
  virtualized systems.  Use the above data carefully.

------------------------------------------------------------
7. lscpu

From lscpu from util-linux 2.37.2:
  Architecture:                    x86_64
  CPU op-mode(s):                  32-bit, 64-bit
  Address sizes:                   48 bits physical, 48 bits virtual
  Byte Order:                      Little Endian
  CPU(s):                          128
  On-line CPU(s) list:             0-127
  Vendor ID:                       AuthenticAMD
  Model name:                      AMD EPYC 7513 32-Core Processor
  CPU family:                      25
  Model:                           1
  Thread(s) per core:              2
  Core(s) per socket:              32
  Socket(s):                       2
  Stepping:                        1
```

(Continued on next page)

Platform Notes (Continued)

```
   BogoMIPS:                          5190.21
   Flags:                             fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                                      clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp
                                      lm constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmperf
                                      rapl pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic
                                      movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic
                                      cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce
                                      topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_l3
                                      cdp_l3 invpcid_single hw_pstate ssbd mba ibrs ibpb stibp vmmcall
                                      fsgsbase bmi1 avx2 smep bmi2 invpcid cqm rdt_a rdseed adx smap
                                      clflushopt clwb sha_ni xsaveopt xsavec xgetbv1 xsaves cqm_llc
                                      cqm_occup_llc cqm_mbm_total cqm_mbm_local clzero irperf xsaveerptr
                                      rdpru wbnoinvd amd_ppin arat npt lbrv svm_lock nrip_save tsc_scale
                                      vmcb_clean flushbyasid decodeassists pausefilter pfthreshold
                                      v_vmsave_vmload vgif v_spec_ctrl umip pku ospke vaes vpclmulqdq rdpid
                                      overflow_recov succor smca
   Virtualization:                    AMD-V
   L1d cache:                         2 MiB (64 instances)
   L1i cache:                         2 MiB (64 instances)
   L2 cache:                          32 MiB (64 instances)
   L3 cache:                          256 MiB (8 instances)
   NUMA node(s):                      8
   NUMA node0 CPU(s):                 0-7,64-71
   NUMA node1 CPU(s):                 8-15,72-79
   NUMA node2 CPU(s):                 16-23,80-87
   NUMA node3 CPU(s):                 24-31,88-95
   NUMA node4 CPU(s):                 32-39,96-103
   NUMA node5 CPU(s):                 40-47,104-111
   NUMA node6 CPU(s):                 48-55,112-119
   NUMA node7 CPU(s):                 56-63,120-127
   Vulnerability Gather data sampling: Not affected
   Vulnerability Itlb multihit:       Not affected
   Vulnerability L1tf:                Not affected
   Vulnerability Mds:                 Not affected
   Vulnerability Meltdown:            Not affected
   Vulnerability Mmio stale data:     Not affected
   Vulnerability Retbleed:            Not affected
   Vulnerability Spec rstack overflow: Mitigation; safe RET
   Vulnerability Spec store bypass:   Mitigation; Speculative Store Bypass disabled via prctl and seccomp
   Vulnerability Spectre v1:          Mitigation; usercopy/swapgs barriers and __user pointer sanitization
   Vulnerability Spectre v2:          Mitigation; Retpolines, IBPB conditional, IBRS_FW, STIBP always-on, RSB
                                      filling, PBRSB-eIBRS Not affected
   Vulnerability Srbds:               Not affected
   Vulnerability Tsx async abort:     Not affected

  From lscpu --cache:
       NAME ONE-SIZE ALL-SIZE WAYS TYPE        LEVEL  SETS PHY-LINE COHERENCY-SIZE
       L1d       32K       2M    8 Data            1    64        1             64
       L1i       32K       2M    8 Instruction     1    64        1             64
       L2       512K      32M    8 Unified         2  1024        1             64
       L3        32M     256M   16 Unified         3 32768        1             64

  ------------------------------------------------------------
  8. numactl --hardware
  NOTE: a numactl 'node' might or might not correspond to a physical chip.
    available: 8 nodes (0-7)
    node 0 cpus: 0-7,64-71
    node 0 size: 64072 MB
    node 0 free: 63451 MB
    node 1 cpus: 8-15,72-79
```

(Continued on next page)

Platform Notes (Continued)

```
  node 1 size: 64507 MB
  node 1 free: 63912 MB
  node 2 cpus: 16-23,80-87
  node 2 size: 64507 MB
  node 2 free: 63898 MB
  node 3 cpus: 24-31,88-95
  node 3 size: 64445 MB
  node 3 free: 63784 MB
  node 4 cpus: 32-39,96-103
  node 4 size: 64507 MB
  node 4 free: 63916 MB
  node 5 cpus: 40-47,104-111
  node 5 size: 64507 MB
  node 5 free: 63830 MB
  node 6 cpus: 48-55,112-119
  node 6 size: 64507 MB
  node 6 free: 63911 MB
  node 7 cpus: 56-63,120-127
  node 7 size: 64499 MB
  node 7 free: 60463 MB
  node distances:
  node   0   1   2   3   4   5   6   7
    0:  10  11  11  11  32  32  32  32
    1:  11  10  11  11  32  32  32  32
    2:  11  11  10  11  32  32  32  32
    3:  11  11  11  10  32  32  32  32
    4:  32  32  32  32  10  11  11  11
    5:  32  32  32  32  11  10  11  11
    6:  32  32  32  32  11  11  10  11
    7:  32  32  32  32  11  11  11  10

------------------------------------------------------------
9. /proc/meminfo
   MemTotal:       527931592 kB

------------------------------------------------------------
10. who -r
  run-level 3 Apr 10 20:47

------------------------------------------------------------
11. Systemd service manager version: systemd 249 (249.11-0ubuntu3.12)
  Default Target  Status
  multi-user      degraded

------------------------------------------------------------
12. Failed units, from systemctl list-units --state=failed
    UNIT                  LOAD   ACTIVE SUB    DESCRIPTION
  * fwupd-refresh.service loaded failed failed Refresh fwupd metadata and update motd

------------------------------------------------------------
13. Services, from systemctl list-unit-files
  STATE            UNIT FILES
  enabled          blk-availability console-setup cron dmesg e2scrub_reap finalrd getty@ gpu-manager
                   grub-common grub-initrd-fallback irqbalance keyboard-setup lm-sensors networkd-dispatcher
                   open-vm-tools pollinate rsyslog secureboot-db setvtrgb ssh systemd-networkd systemd-pstore
                   systemd-resolved systemd-timesyncd thermald tuned ua-reboot-cmds ubuntu-advantage udisks2
                   vgauth wpa_supplicant
  enabled-runtime  netplan-ovs-cleanup systemd-fsck-root systemd-remount-fs
  disabled         ModemManager apparmor console-getty debug-shell iscsid lvm2-monitor lxd-agent multipathd
                   nftables open-iscsi rsync serial-getty@ systemd-boot-check-no-failures
```

(Continued on next page)

Platform Notes (Continued)

```
                  systemd-network-generator systemd-sysext systemd-time-wait-sync ufw upower
                  wpa_supplicant-nl80211@ wpa_supplicant-wired@ wpa_supplicant@
  generated       apport
  indirect        uuidd
  masked          NetworkManager NetworkManager-dispatcher NetworkManager-wait-online cryptdisks
                  cryptdisks-early hwclock lvm2 multipath-tools-boot rc rcS screen-cleanup sudo
                  systemd-networkd-wait-online x11-common

------------------------------------------------------------
14. Linux kernel boot-time arguments, from /proc/cmdline
  BOOT_IMAGE=/boot/vmlinuz-5.15.0-102-generic
  root=UUID=593ab29a-c8fe-4d75-821a-b60d5c945311
  ro

------------------------------------------------------------
15. cpupower frequency-info
  analyzing CPU 0:
    Unable to determine current policy
    boost state support:
      Supported: yes
      Active: yes
      Boost States: 0
      Total States: 3
      Pstate-P0:  2600MHz

------------------------------------------------------------
16. tuned-adm active
  Current active profile: latency-performance

------------------------------------------------------------
17. sysctl
  kernel.numa_balancing               1
  kernel.randomize_va_space           0
  vm.compaction_proactiveness        20
  vm.dirty_background_bytes           0
  vm.dirty_background_ratio           3
  vm.dirty_bytes                      0
  vm.dirty_expire_centisecs        3000
  vm.dirty_ratio                      8
  vm.dirty_writeback_centisecs      500
  vm.dirtytime_expire_seconds     43200
  vm.extfrag_threshold              500
  vm.min_unmapped_ratio               1
  vm.nr_hugepages                     0
  vm.nr_hugepages_mempolicy           0
  vm.nr_overcommit_hugepages          0
  vm.swappiness                       1
  vm.watermark_boost_factor       15000
  vm.watermark_scale_factor          10
  vm.zone_reclaim_mode                1

------------------------------------------------------------
18. /sys/kernel/mm/transparent_hugepage
  defrag          [always] defer defer+madvise madvise never
  enabled         [always] madvise never
  hpage_pmd_size  2097152
  shmem_enabled   always within_size advise [never] deny force

------------------------------------------------------------
19. /sys/kernel/mm/transparent_hugepage/khugepaged
```

(Continued on next page)

Platform Notes (Continued)

```
   alloc_sleep_millisecs   60000
   defrag                      1
   max_ptes_none             511
   max_ptes_shared           256
   max_ptes_swap              64
   pages_to_scan            4096
   scan_sleep_millisecs    10000

------------------------------------------------------------
20. OS release
  From /etc/*-release /etc/*-version
  os-release Ubuntu 22.04.4 LTS

------------------------------------------------------------
21. Disk information
SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-aocc320-A1
  Filesystem     Type   Size  Used Avail Use% Mounted on
  tmpfs          tmpfs   80G  3.4G   77G   5% /mnt/ramdisk

------------------------------------------------------------
22. /sys/devices/virtual/dmi/id
    Vendor:         Dell Inc.
    Product:        PowerEdge C6525
    Product Family: PowerEdge
    Serial:         SL5C654

------------------------------------------------------------
23. dmidecode
  Additional information from dmidecode 3.3 follows.  WARNING: Use caution when you interpret this section.
  The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
  determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
  "DMTF SMBIOS" standard.
  Memory:
    16x 80CE863280CE M393A4G43AB3-CWE 32 GB 2 rank 3200


------------------------------------------------------------
24. BIOS
(This section combines info from /sys/devices and dmidecode.)
   BIOS Vendor:       Dell Inc.
   BIOS Version:      2.5.6
   BIOS Date:         10/06/2021
   BIOS Revision:     2.5
```

Compiler Version Notes

```
==============================================================================================================
C               | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)
--------------------------------------------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++             | 508.namd_r(base, peak) 510.parest_r(base, peak)
--------------------------------------------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on LLVM Mirror.Version.13.0.0)
```

(Continued on next page)

Compiler Version Notes (Continued)

```
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
------------------------------------------------------------------------------

==============================================================================
C++, C          | 511.povray_r(base, peak) 526.blender_r(base, peak)
------------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
------------------------------------------------------------------------------

==============================================================================
C++, C, Fortran | 507.cactuBSSN_r(base, peak)
------------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
------------------------------------------------------------------------------

==============================================================================
Fortran         | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)
------------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
------------------------------------------------------------------------------

==============================================================================
Fortran, C      | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
------------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
clang

C++ benchmarks:
clang++

Fortran benchmarks:
flang

Benchmarks using both Fortran and C:
flang clang

Benchmarks using both C and C++:
clang++ clang

Benchmarks using Fortran, C, and C++:
clang++ clang flang

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
526.blender_r: -funsigned-char -D__BOOL_DEFINED -DSPEC_LP64
527.cam4_r: -DSPEC_CASE_FLAG -DSPEC_LP64
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-m64 -flto -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-loop-fusion -O3 -march=znver3 -fveclib=AMDLIBM

(Continued on next page)

Base Optimization Flags (Continued)

C benchmarks (continued):
-ffast-math -fstruct-layout=5 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays
-mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -enable-loop-fusion -z muldefs -lamdlibm -ljemalloc -lflang

C++ benchmarks:
-m64 -std=c++98 -mno-adx -mno-sse4a
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -flto
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-loop-fusion -O3 -march=znver3 -fveclib=AMDLIBM
-ffast-math -mllvm -enable-partial-unswitch
-mllvm -unroll-threshold=100 -finline-aggressive
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000
-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch
-mllvm -extra-vectorizer-passes -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -convert-pow-exp-to-int=false
-mllvm -enable-loop-fusion -z muldefs -lamdlibm -ljemalloc -lflang

Fortran benchmarks:
-m64 -Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp -flto -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-loop-fusion -Hz,1,0x1 -O3 -march=znver3
-fveclib=AMDLIBM -ffast-math -Kieee -Mrecursive
-mllvm -fuse-tile-inner-loop -funroll-loops
-mllvm -extra-vectorizer-passes -mllvm -lsr-in-nested-loop
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -enable-loop-fusion
-mllvm -enable-loopinterchange -mllvm -compute-interchange-order
-z muldefs -lamdlibm -ljemalloc -lflang

Benchmarks using both Fortran and C:
-m64 -Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp -flto -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-loop-fusion -O3 -march=znver3 -fveclib=AMDLIBM
-ffast-math -fstruct-layout=5 -mllvm -unroll-threshold=50

(Continued on next page)

Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):
-mllvm -inline-threshold=1000 -fremap-arrays
-mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -enable-loop-fusion -Hz,1,0x1 -Kieee -Mrecursive
-mllvm -fuse-tile-inner-loop -funroll-loops
-mllvm -extra-vectorizer-passes -mllvm -lsr-in-nested-loop
-mllvm -enable-loopinterchange -mllvm -compute-interchange-order
-z muldefs -lamdlibm -ljemalloc -lflang

Benchmarks using both C and C++:
-m64 -std=c++98 -mno-adx -mno-sse4a
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -flto
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-loop-fusion -O3 -march=znver3 -fveclib=AMDLIBM
-ffast-math -fstruct-layout=5 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays
-mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -enable-loop-fusion -mllvm -enable-partial-unswitch
-mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000 -mllvm -reroll-loops
-mllvm -aggressive-loop-unswitch -mllvm -extra-vectorizer-passes
-mllvm -convert-pow-exp-to-int=false -z muldefs -lamdlibm -ljemalloc
-lflang

Benchmarks using Fortran, C, and C++:
-m64 -std=c++98 -mno-adx -mno-sse4a
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -flto
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-loop-fusion -O3 -march=znver3 -fveclib=AMDLIBM
-ffast-math -fstruct-layout=5 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays
-mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -enable-loop-fusion -mllvm -enable-partial-unswitch
-mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000 -mllvm -reroll-loops
-mllvm -aggressive-loop-unswitch -mllvm -extra-vectorizer-passes

(Continued on next page)

Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):
-mllvm -convert-pow-exp-to-int=false -Hz,1,0x1 -Kieee -Mrecursive
-mllvm -fuse-tile-inner-loop -funroll-loops -mllvm -lsr-in-nested-loop
-mllvm -enable-loopinterchange -mllvm -compute-interchange-order
-z muldefs -lamdlibm -ljemalloc -lflang

Base Other Flags

C benchmarks:
-Wno-unused-command-line-argument

C++ benchmarks:
-Wno-unused-command-line-argument

Fortran benchmarks:
-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:
-Wno-unused-command-line-argument

Benchmarks using both C and C++:
-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:
-Wno-unused-command-line-argument

Peak Compiler Invocation

C benchmarks:
clang

C++ benchmarks:
clang++

Fortran benchmarks:
flang

Benchmarks using both Fortran and C:
flang clang

Benchmarks using both C and C++:
clang++ clang

(Continued on next page)

Peak Compiler Invocation (Continued)

Benchmarks using Fortran, C, and C++:
clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

519.lbm_r: -m64 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math
-fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -flv-function-specialization
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-mllvm -global-vectorize-slp=true
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -lamdlibm -ljemalloc

538.imagick_r: Same as 519.lbm_r

544.nab_r: basepeak = yes

C++ benchmarks:

508.namd_r: -m64 -std=c++98 -mno-adx -mno-sse4a
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false
-Wl,-mllvm -Wl,-enable-licm-vrp -flto
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math
-finline-aggressive -mllvm -unroll-threshold=100
-flv-function-specialization -mllvm -enable-licm-vrp
-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -lamdlibm -ljemalloc

(Continued on next page)

Peak Optimization Flags (Continued)

510.parest_r: -m64 -std=c++98 -mno-adx -mno-sse4a
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false
-Wl,-mllvm -Wl,-enable-licm-vrp -flto
-Wl,-mllvm -Wl,-suppress-fmas
-Wl,-mllvm -Wl,-function-specialize -Ofast -march=znver3
-fveclib=AMDLIBM -ffast-math -finline-aggressive
-mllvm -unroll-threshold=100 -flv-function-specialization
-mllvm -enable-licm-vrp -mllvm -reroll-loops
-mllvm -aggressive-loop-unswitch
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -lamdlibm -ljemalloc

Fortran benchmarks:

503.bwaves_r: -m64 -Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp -flto
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -Mrecursive
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -enable-licm-vrp
-lamdlibm -ljemalloc -lflang

549.fotonik3d_r: -m64 -Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp -flto
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -Kieee
-Mrecursive -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -enable-licm-vrp
-lamdlibm -ljemalloc -lflang

554.roms_r: -m64 -Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp -flto
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -Mrecursive
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -enable-licm-vrp
-Hz,1,0x1 -mllvm -fuse-tile-inner-loop -lamdlibm
-ljemalloc -lflang

(Continued on next page)

Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

521.wrf_r: `basepeak = yes`

527.cam4_r: `-m64 -Wl,-mllvm -Wl,-enable-X86-prefetching -Wl,-mllvm -Wl,-enable-licm-vrp -flto -Wl,-mllvm -Wl,-function-specialize -Wl,-mllvm -Wl,-force-vector-interleave=1 -Ofast -march=znver3 -fveclib=AMDLIBM -ffast-math -fstruct-layout=7 -mllvm -unroll-threshold=50 -fremap-arrays -flv-function-specialization -mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true -mllvm -function-specialize -mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3 -O3 -funroll-loops -mllvm -extra-vectorizer-passes -mllvm -lsr-in-nested-loop -Mrecursive -Hz,1,0x1 -mllvm -enable-loopinterchange -mllvm -compute-interchange-order -lamdlibm -ljemalloc -lflang`

Benchmarks using both C and C++:

511.povray_r: `basepeak = yes`

526.blender_r: `basepeak = yes`

Benchmarks using Fortran, C, and C++:
`-m64 -std=c++98 -mno-adx -mno-sse4a -Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-enable-licm-vrp -flto -Wl,-mllvm -Wl,-function-specialize -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6 -Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast -march=znver3 -fveclib=AMDLIBM -ffast-math -fstruct-layout=7 -mllvm -unroll-threshold=50 -fremap-arrays -flv-function-specialization -mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true -mllvm -function-specialize -mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3 -mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000 -finline-aggressive -mllvm -reroll-loops -mllvm -aggressive-loop-unswitch -mllvm -extra-vectorizer-passes -mllvm -convert-pow-exp-to-int=false -Mrecursive -lamdlibm -ljemalloc -lflang`

Peak Other Flags

C benchmarks:
`-Wno-unused-command-line-argument`

C++ benchmarks:
`-Wno-unused-command-line-argument`

Fortran benchmarks:
`-Wno-unused-command-line-argument`

Benchmarks using both Fortran and C:
`-Wno-unused-command-line-argument`

Benchmarks using both C and C++:
`-Wno-unused-command-line-argument`

Benchmarks using Fortran, C, and C++:
`-Wno-unused-command-line-argument`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2017/flags/aocc320-flags-A1.html`
`http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.2.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2017/flags/aocc320-flags-A1.xml`
`http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.2.xml`

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2024-04-10 21:47:47-0400.
Report generated on 2024-05-07 22:18:02 by CPU2017 PS formatter v6716.
Originally published on 2024-05-07.