



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR850 V3
(2.20 GHz, Intel Xeon Gold 6416H)

SPECrate®2017_int_base = 645

SPECrate®2017_int_peak = Not Run

CPU2017 License: 9017

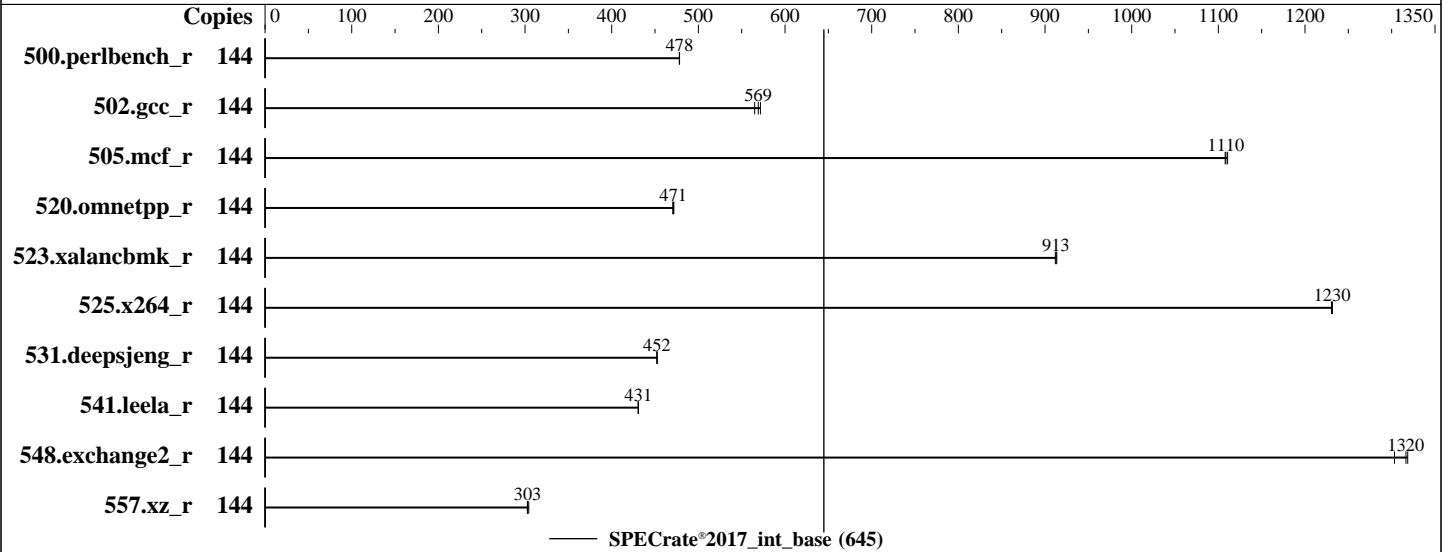
Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: May-2024

Hardware Availability: Jun-2023

Software Availability: Mar-2024



Hardware

CPU Name: Intel Xeon Gold 6416H
 Max MHz: 4200
 Nominal: 2200
 Enabled: 72 cores, 4 chips, 2 threads/core
 Orderable: 2,4 chips
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 45 MB I+D on chip per chip
 Other: None
 Memory: 1 TB (32 x 32 GB 2Rx8 PC5-4800B-R)
 Storage: 1 x 960 GB M.2 NVME SSD
 Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP4
 Kernel 5.14.21-150400.22-default
 Compiler: C/C++: Version 2024.0.2 of Intel oneAPI DPC++/C++ Compiler for Linux;
 Fortran: Version 2024.0.2 of Intel Fortran Compiler for Linux;
 Parallel: No
 Firmware: Lenovo BIOS Version RSE109I 3.11 released Jan-2024
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: Not Applicable
 Other: None
 Power Management: BIOS set to prefer performance at the cost of additional power usage



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Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|-----------------|--------|-------------------|--------------------|-------------------|--------------------|-------------------|--------------------|--------|---------|-------|---------|-------|---------|-------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 500.perlbench_r | 144 | 480 | 478 | <u>479</u> | <u>478</u> | 479 | 478 | | | | | | | |
| 502.gcc_r | 144 | 357 | 572 | 361 | 565 | <u>358</u> | <u>569</u> | | | | | | | |
| 505.mcf_r | 144 | <u>210</u> | <u>1110</u> | 210 | 1110 | 210 | 1110 | | | | | | | |
| 520.omnetpp_r | 144 | 401 | 472 | <u>401</u> | <u>471</u> | 402 | 470 | | | | | | | |
| 523.xalancbmk_r | 144 | <u>167</u> | <u>913</u> | 167 | 912 | 166 | 914 | | | | | | | |
| 525.x264_r | 144 | 205 | 1230 | <u>205</u> | <u>1230</u> | 205 | 1230 | | | | | | | |
| 531.deepsjeng_r | 144 | 365 | 452 | 365 | 452 | <u>365</u> | <u>452</u> | | | | | | | |
| 541.leela_r | 144 | 553 | 431 | 554 | 430 | <u>554</u> | <u>431</u> | | | | | | | |
| 548.exchange2_r | 144 | 289 | 1300 | 286 | 1320 | <u>287</u> | <u>1320</u> | | | | | | | |
| 557.xz_r | 144 | 511 | 304 | 514 | 303 | <u>513</u> | <u>303</u> | | | | | | | |

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
"/home/cpu2017-1.1.9-ic2024.0.2/lib/intel64:/home/cpu2017-1.1.9-ic2024.0.2/lib/ia32:/home/cpu2017-1.1.9-ic2024.0.2/je5.0.1-32"
MALLOC_CONF = "retain:true"
```

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

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General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance and then set it to Custom Mode
C-States set to Legacy
SNC set to SNC2
UPI Link Disable set to Minimum Number of Links Enabled
LLC Prefetch set to Disabled
LLC dead line alloc set to Disable

Sysinfo program /home/cpu2017-1.1.9-ic2024.0.2/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Sat May 4 23:38:01 2024

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

1. uname -a
Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222)
x86_64 x86_64 x86_64 GNU/Linux

2. w
23:38:01 up 1:05, 1 user, load average: 3.01, 65.51, 108.79
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
root tty1 - 22:34 9.00s 0.85s 0.00s /bin/bash ./rate_int.sh

3. Username

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Platform Notes (Continued)

From environment variable \$USER: root

```

-----
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size          (kbytes, -d) unlimited
scheduling priority    (-e) 0
file size              (blocks, -f) unlimited
pending signals        (-i) 4126852
max locked memory      (kbytes, -l) 64
max memory size        (kbytes, -m) unlimited
open files             (-n) 1024
pipe size              (512 bytes, -p) 8
POSIX message queues   (bytes, -q) 819200
real-time priority     (-r) 0
stack size             (kbytes, -s) unlimited
cpu time               (seconds, -t) unlimited
max user processes     (-u) 4126852
virtual memory         (kbytes, -v) unlimited
file locks             (-x) unlimited

```

```

-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 30
login -- root
-bash
/bin/bash ./rate_int.sh
/bin/bash ./rate_int.sh
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=144 -c
  ic2024.0.2-lin-sapphirerapids-rate-20231213.cfg --define smt-on --define cores=72 --define physicalfirst
  --define invoke_with_interleave --define drop_caches --tune base -o all intrate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=144 --configfile
  ic2024.0.2-lin-sapphirerapids-rate-20231213.cfg --define smt-on --define cores=72 --define physicalfirst
  --define invoke_with_interleave --define drop_caches --tune base --output_format all --nopower --runmode
  rate --tune base --size refrate intrate --nopreenv --note-preenv --logfile
  $SPEC/tmp/CPU2017.115/temlogs/preenv.intrate.115.0.log --lognum 115.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9-ic2024.0.2

```

```

-----
6. /proc/cpuinfo
model name      : Intel(R) Xeon(R) Gold 6416H
vendor_id      : GenuineIntel
cpu family     : 6
model          : 143
stepping       : 8
microcode      : 0x2b0004d0
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs
cpu cores      : 18
siblings       : 36
4 physical ids (chips)
144 processors (hardware threads)
physical id 0: core ids 0-17
physical id 1: core ids 0-17
physical id 2: core ids 0-17
physical id 3: core ids 0-17
physical id 0: apicids 0-35
physical id 1: apicids 128-163
physical id 2: apicids 256-291
physical id 3: apicids 384-419

```

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Platform Notes (Continued)

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.37.2:

```

Architecture:          x86_64
CPU op-mode(s):       32-bit, 64-bit
Address sizes:         46 bits physical, 57 bits virtual
Byte Order:           Little Endian
CPU(s):                144
On-line CPU(s) list:  0-143
Vendor ID:            GenuineIntel
Model name:           Intel(R) Xeon(R) Gold 6416H
CPU family:           6
Model:                143
Thread(s) per core:   2
Core(s) per socket:   18
Socket(s):            4
Stepping:             8
BogoMIPS:             4400.00
Flags:                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                    clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                    lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                    nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                    ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1
                    sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
                    lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3
                    invpcid_single intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced
                    tpr_shadow vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle
                    avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
                    avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
                    xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                    cqm_mbm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida
                    arat pln pts avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes
                    vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid
                    bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear serialize
                    tsxldtrk pconfig arch_lbr avx512_fp16 amx_tile flush_lld arch_capabilities
Virtualization:       VT-x
L1d cache:           3.4 MiB (72 instances)
L1i cache:           2.3 MiB (72 instances)
L2 cache:            144 MiB (72 instances)
L3 cache:            180 MiB (4 instances)
NUMA node(s):        8
NUMA node0 CPU(s):   0-8,72-80
NUMA node1 CPU(s):   9-17,81-89
NUMA node2 CPU(s):   18-26,90-98
NUMA node3 CPU(s):   27-35,99-107
NUMA node4 CPU(s):   36-44,108-116
NUMA node5 CPU(s):   45-53,117-125
NUMA node6 CPU(s):   54-62,126-134
NUMA node7 CPU(s):   63-71,135-143
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:   Not affected
Vulnerability Mds:    Not affected
Vulnerability Meltdown: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling

```

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Platform Notes (Continued)

Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

| NAME | ONE-SIZE | ALL-SIZE | WAYS | TYPE | LEVEL | SETS | PHY-LINE | COHERENCY-SIZE |
|------|----------|----------|------|-------------|-------|-------|----------|----------------|
| L1d | 48K | 3.4M | 12 | Data | 1 | 64 | 1 | 64 |
| L1i | 32K | 2.3M | 8 | Instruction | 1 | 64 | 1 | 64 |
| L2 | 2M | 144M | 16 | Unified | 2 | 2048 | 1 | 64 |
| L3 | 45M | 180M | 15 | Unified | 3 | 49152 | 1 | 64 |

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 8 nodes (0-7)
node 0 cpus: 0-8,72-80
node 0 size: 128682 MB
node 0 free: 127389 MB
node 1 cpus: 9-17,81-89
node 1 size: 129019 MB
node 1 free: 128132 MB
node 2 cpus: 18-26,90-98
node 2 size: 129019 MB
node 2 free: 128311 MB
node 3 cpus: 27-35,99-107
node 3 size: 129019 MB
node 3 free: 128311 MB
node 4 cpus: 36-44,108-116
node 4 size: 129019 MB
node 4 free: 128313 MB
node 5 cpus: 45-53,117-125
node 5 size: 128984 MB
node 5 free: 128278 MB
node 6 cpus: 54-62,126-134
node 6 size: 129019 MB
node 6 free: 128306 MB
node 7 cpus: 63-71,135-143
node 7 size: 128972 MB
node 7 free: 128242 MB
node distances:
node  0  1  2  3  4  5  6  7
0:  10 12 21 21 31 31 21 21
1:  12 10 21 21 31 31 21 21
2:  21 21 10 12 21 21 31 31
3:  21 21 12 10 21 21 31 31
4:  31 31 21 21 10 12 21 21
5:  31 31 21 21 12 10 21 21
6:  21 21 31 31 21 21 10 12
7:  21 21 31 31 21 21 12 10

```

9. /proc/meminfo

MemTotal: 1056498528 kB

10. who -r

run-level 3 May 4 22:34

11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)

Default Target Status

(Continued on next page)



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Platform Notes (Continued)

multi-user running

12. Services, from `systemctl list-unit-files`

| STATE | UNIT FILES |
|-----------------|---|
| enabled | YaST2-Firstboot YaST2-Second-Stage apparmor auditd chronyd cron getty@ haveged irqbalance issue-generator kbdsettings klog lvm2-monitor nscd nvme-fc-boot-connections postfix purge-kernels rollback rsyslog smartd sshd wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny |
| enabled-runtime | systemd-remount-fs |
| disabled | autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info firewallld gpm grub2-once haveged-switch-root ipmi ipmievf issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap nvme-fc-autoconnect rdisc rpcbind rpmconfigcheck rsyncd serial-getty@ smartd_generate_opts snmpd snmptrapd systemd-boot-check-no-failures systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd |
| generated | ntp_sync |
| indirect | wickedd |

13. Linux kernel boot-time arguments, from `/proc/cmdline`

```
BOOT_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default
root=UUID=39b62cad-8791-4e9f-8882-c2af12880769
splash=silent
mitigations=auto
quiet
security=apparmor
```

14. `cpupower frequency-info`

```
analyzing CPU 0:
Unable to determine current policy
boost state support:
Supported: yes
Active: yes
```

15. `sysctl`

| | |
|------------------------------|-------|
| kernel.numa_balancing | 1 |
| kernel.randomize_va_space | 2 |
| vm.compaction_proactiveness | 20 |
| vm.dirty_background_bytes | 0 |
| vm.dirty_background_ratio | 10 |
| vm.dirty_bytes | 0 |
| vm.dirty_expire_centisecs | 3000 |
| vm.dirty_ratio | 20 |
| vm.dirty_writeback_centisecs | 500 |
| vm.dirtytime_expire_seconds | 43200 |
| vm.extfrag_threshold | 500 |
| vm.min_unmapped_ratio | 1 |
| vm.nr_hugepages | 0 |
| vm.nr_hugepages_mempolicy | 0 |
| vm.nr_overcommit_hugepages | 0 |
| vm.swappiness | 60 |
| vm.watermark_boost_factor | 15000 |
| vm.watermark_scale_factor | 10 |
| vm.zone_reclaim_mode | 0 |

16. `/sys/kernel/mm/transparent_hugepage`

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Platform Notes (Continued)

```
defrag          always defer defer+madvise [madvise] never
enabled         [always] madvise never
hpage_pmd_size  2097152
shmem_enabled   always within_size advise [never] deny force
```

```
-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                 1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs   10000
```

```
-----
18. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP4
```

```
-----
19. Disk information
SPEC is set to: /home/cpu2017-1.1.9-ic2024.0.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/nvme0nlp2  xfs   894G  155G  739G  18% /
```

```
-----
20. /sys/devices/virtual/dmi/id
Vendor:          Lenovo
Product:         ThinkSystem SR850 V3
Product Family: ThinkSystem
Serial:          None
```

```
-----
21. dmidecode
Additional information from dmidecode 3.2 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
  1x SK Hynix HMC88AEBRA107N 32 GB 2 rank 4800
  8x SK Hynix HMC88AEBRA115N 32 GB 2 rank 4800
 13x SK Hynix HMC88AEBRA168N 32 GB 2 rank 4800
 10x SK Hynix HMC88AEBRA173N 32 GB 2 rank 4800
```

```
-----
22. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor:          Lenovo
BIOS Version:         RSE109I-3.11
BIOS Date:            01/05/2024
BIOS Revision:        3.11
Firmware Revision:    3.10
```

Compiler Version Notes

```
=====
C | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base) 557.xz_r(base)
```

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Compiler Version Notes (Continued)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====
C++ | 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base) 541.leela_r(base)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====
Fortran | 548.exchange2_r(base)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64



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Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/opt/intel/oneapi/compiler/2024.0/lib -lqkmalloc
```

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/opt/intel/oneapi/compiler/2024.0/lib -lqkmalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/opt/intel/oneapi/compiler/2024.0/lib -lqkmalloc
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-AA.html>

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-AA.xml>

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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