



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Hewlett Packard Enterprise

(Test Sponsor: HPE)

### ProLiant ML30 Gen11

(3.50 GHz, Intel Xeon 6325P)

SPECrate®2017\_int\_base = 55.9

SPECrate®2017\_int\_peak = 58.0

CPU2017 License: 3

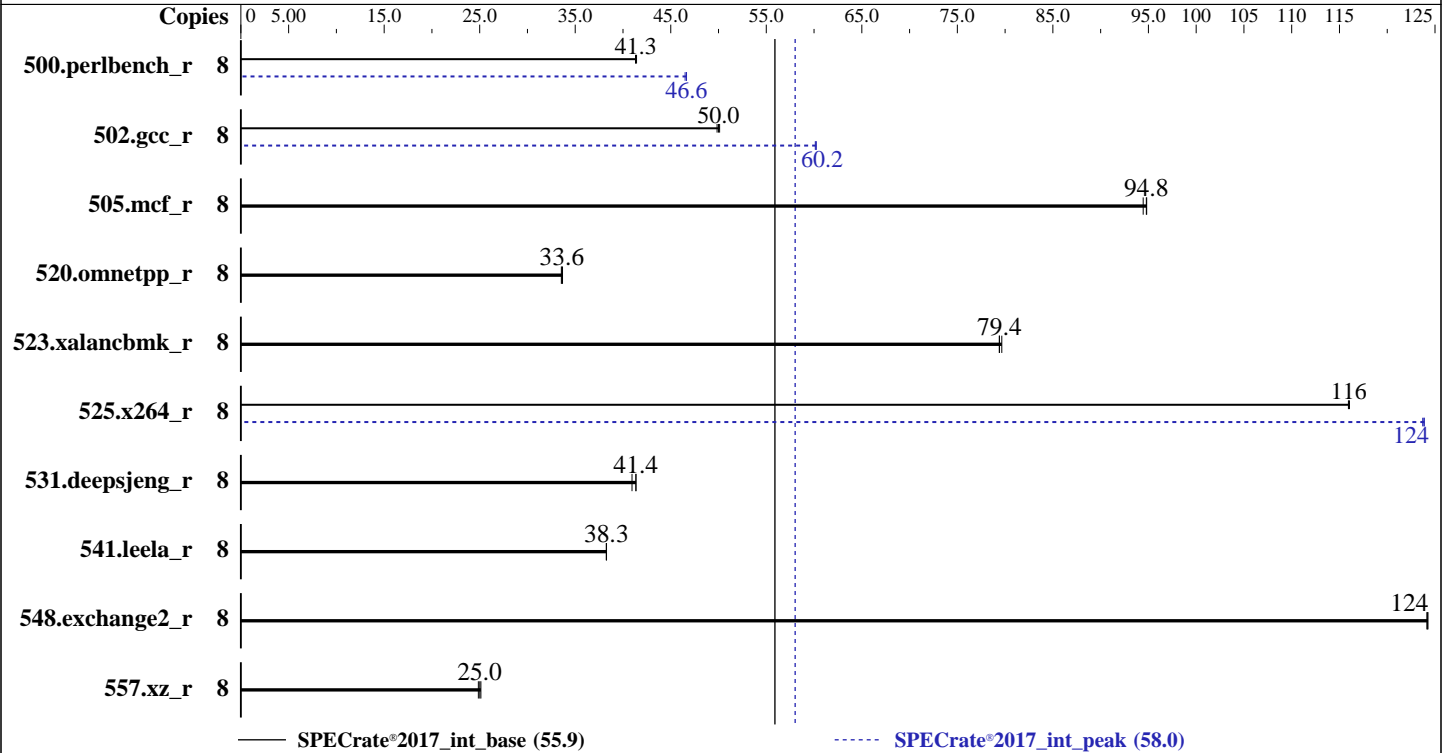
Test Sponsor: HPE

Tested by: HPE

Test Date: Mar-2025

Hardware Availability: Mar-2025

Software Availability: Jun-2024



### Hardware

CPU Name: Intel Xeon 6325P  
 Max MHz: 5200  
 Nominal: 3500  
 Enabled: 4 cores, 1 chip, 2 threads/core  
 Orderable: 1 chip  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 2 MB I+D on chip per core  
 L3: 12 MB I+D on chip per chip  
 Other: None  
 Memory: 64 GB (2 x 32 GB 2Rx8 PC5-5600B-E, running at 4400, orderable using HPE part# P64339-B21)  
 Storage: 1 x 480 GB SATA SSD  
 Other: CPU Cooling: Air

### Software

OS: SUSE Linux Enterprise Server 15 SP6  
 Kernel 6.4.0-150600.21-default  
 Compiler: C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;  
 Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;  
 Parallel: No  
 Firmware: HPE BIOS Version v2.10 12/06/2024 released Dec-2024  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS set to prefer performance at the cost of additional power usage



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant ML30 Gen11

(3.50 GHz, Intel Xeon 6325P)

SPECrate®2017\_int\_base = 55.9

SPECrate®2017\_int\_peak = 58.0

CPU2017 License: 3  
Test Sponsor: HPE  
Tested by: HPE

Test Date: Mar-2025  
Hardware Availability: Mar-2025  
Software Availability: Jun-2024

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	8	307	41.4	<b><u>308</u></b>	<b><u>41.3</u></b>	308	41.3	8	<b><u>273</u></b>	<b><u>46.6</u></b>	274	46.5	273	46.6
502.gcc_r	8	226	50.1	227	49.8	<b><u>227</u></b>	<b><u>50.0</u></b>	8	188	60.1	188	60.2	<b><u>188</u></b>	<b><u>60.2</u></b>
505.mcf_r	8	<b><u>136</u></b>	<b><u>94.8</u></b>	137	94.5	136	94.8	8	<b><u>136</u></b>	<b><u>94.8</u></b>	137	94.5	136	94.8
520.omnetpp_r	8	312	33.7	<b><u>312</u></b>	<b><u>33.6</u></b>	313	33.5	8	312	33.7	<b><u>312</u></b>	<b><u>33.6</u></b>	313	33.5
523.xalancbmk_r	8	106	79.4	<b><u>106</u></b>	<b><u>79.4</u></b>	106	79.7	8	106	79.4	<b><u>106</u></b>	<b><u>79.4</u></b>	106	79.7
525.x264_r	8	<b><u>121</u></b>	<b><u>116</u></b>	121	116	121	116	8	113	124	<b><u>113</u></b>	<b><u>124</u></b>	113	124
531.deepsjeng_r	8	224	40.9	<b><u>222</u></b>	<b><u>41.4</u></b>	222	41.4	8	224	40.9	<b><u>222</u></b>	<b><u>41.4</u></b>	222	41.4
541.leela_r	8	346	38.3	346	38.3	<b><u>346</u></b>	<b><u>38.3</u></b>	8	346	38.3	346	38.3	<b><u>346</u></b>	<b><u>38.3</u></b>
548.exchange2_r	8	169	124	<b><u>169</u></b>	<b><u>124</u></b>	169	124	8	169	124	<b><u>169</u></b>	<b><u>124</u></b>	169	124
557.xz_r	8	348	24.9	344	25.1	<b><u>345</u></b>	<b><u>25.0</u></b>	8	348	24.9	344	25.1	<b><u>345</u></b>	<b><u>25.0</u></b>

SPECrate®2017\_int\_base = 55.9

SPECrate®2017\_int\_peak = 58.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-32"  
MALLOC\_CONF = "retain:true"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4  
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.  
jemalloc, a general purpose malloc implementation built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5 sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant ML30 Gen11**

(3.50 GHz, Intel Xeon 6325P)

**SPECrate®2017\_int\_base = 55.9**

**SPECrate®2017\_int\_peak = 58.0**

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Mar-2025  
**Hardware Availability:** Mar-2025  
**Software Availability:** Jun-2024

## Platform Notes

### BIOS Configuration:

Workload Profile set to General Throughput Compute  
Thermal Configuration set to Maximum Cooling  
Enhanced Processor Performance Profile set to Enabled

Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Tue Mar 4 03:00:05 2025

SUT (System Under Test) info as seen by some common utilities.

### Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent\_hugepage
17. /sys/kernel/mm/transparent\_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

```
1. uname -a
Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)
x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
03:00:05 up 2 min, 3 users, load average: 0.04, 0.07, 0.03
USER      TTY      FROM          LOGIN@      IDLE        JCPU       PCPU       WHAT
```

```
3. Username
From environment variable $USER: root
```

```
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size               (blocks, -f) unlimited
pending signals         (-i) 256726
max locked memory      (kbytes, -l) 8192
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant ML30 Gen11**

**(3.50 GHz, Intel Xeon 6325P)**

**SPECrate®2017\_int\_base = 55.9**

**SPECrate®2017\_int\_peak = 58.0**

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Mar-2025  
**Hardware Availability:** Mar-2025  
**Software Availability:** Jun-2024

## Platform Notes (Continued)

```

max memory size      (kbytes, -m) unlimited
open files           (-n) 1024
pipe size            (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority   (-r) 0
stack size           (kbytes, -s) unlimited
cpu time             (seconds, -t) unlimited
max user processes   (-u) 256726
virtual memory       (kbytes, -v) unlimited
file locks           (-x) unlimited

```

```

-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize=31
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@notty
bash -c cd $SPEC/ && $SPEC/intrate.sh
runccpu --nobuild --action validate --define default-platform-flags --define numcopies=8 -c
  ic2024.1-lin-core-avx2-rate-20240308.cfg --define smt-on --define cores=4 --define physicalfirst --define
no-numa --tune base,peak -o all --define drop_caches intrate
runccpu --nobuild --action validate --define default-platform-flags --define numcopies=8 --configfile
  ic2024.1-lin-core-avx2-rate-20240308.cfg --define smt-on --define cores=4 --define physicalfirst --define
no-numa --tune base,peak --output_format all --define drop_caches --nopower --runmode rate --tune
base:peak --size refrate intrate --nopreenv --note-preenv --logfile
  $SPEC/tmp/CPU2017.003/templogs/preenv.intrate.003.0.log --lognum 003.0 --from_runccpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017

```

```

-----
6. /proc/cpuinfo
model name      : Intel(R) Xeon(R) 6325P
vendor_id      : GenuineIntel
cpu family     : 6
model          : 183
stepping       : 1
microcode      : 0x12c
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb bhi
cpu cores      : 4
siblings       : 8
1 physical ids (chips)
8 processors (hardware threads)
physical id 0: core ids 0-3
physical id 0: apicids 0-7
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.

```

```

-----
7. lscpu

From lscpu from util-linux 2.39.3:
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         46 bits physical, 48 bits virtual
Byte Order:            Little Endian
CPU(s):                8
On-line CPU(s) list:   0-7
Vendor ID:              GenuineIntel
BIOS Vendor ID:        Intel(R) Corporation
Model name:             Intel(R) Xeon(R) 6325P

```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Hewlett Packard Enterprise

(Test Sponsor: HPE)

### ProLiant ML30 Gen11

(3.50 GHz, Intel Xeon 6325P)

## SPECrate®2017\_int\_base = 55.9

## SPECrate®2017\_int\_peak = 58.0

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Mar-2025  
**Hardware Availability:** Mar-2025  
**Software Availability:** Jun-2024

## Platform Notes (Continued)

```

BIOS Model name: Intel(R) Xeon(R) 6325P To Be Filled By O.E.M. CPU @ 3.5GHz
BIOS CPU family: 179
CPU family: 6
Model: 183
Thread(s) per core: 2
Core(s) per socket: 4
Socket(s): 1
Stepping: 1
BogoMIPS: 6988.80
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good
nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni
pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdc_m sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb sssd
ibrs ibpb stibp ibrs_enhanced tpr_shadow flexpriority ept vpid ept_ad
fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid rdseed adx smap
clflushopt clwb intel_pt sha_ni xsaveopt xsavec xgetbv1 xsaves
split_lock_detect user_shstk avx_vnni dtherm ida arat pln pts hfi
vnm1 umip pku ospke waitpkg gfni vaes vpclmulqdq tme rdpid movdiri
movdir64b fsrm md_clear serialize pconfig arch_lbr ibt flush_lld
arch_capabilities

Virtualization: VT-x
L1d cache: 192 KiB (4 instances)
L1i cache: 128 KiB (4 instances)
L2 cache: 8 MiB (4 instances)
L3 cache: 12 MiB (1 instance)
NUMA node(s): 1
NUMA node0 CPU(s): 0-7
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability Lltf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling;
PBRSE-eIBRS SW sequence; BHI BHI_DIS_S

Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	192K	12	Data	1	64	1	64
L1i	32K	128K	8	Instruction	1	64	1	64
L2	2M	8M	16	Unified	2	2048	1	64
L3	12M	12M	6	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 1 nodes (0)
node 0 cpus: 0-7
node 0 size: 64206 MB
node 0 free: 63601 MB

```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant ML30 Gen11**

(3.50 GHz, Intel Xeon 6325P)

**SPECrate®2017\_int\_base = 55.9**

**SPECrate®2017\_int\_peak = 58.0**

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Mar-2025  
**Hardware Availability:** Mar-2025  
**Software Availability:** Jun-2024

## Platform Notes (Continued)

node distances:  
node 0  
0: 10

-----  
9. /proc/meminfo  
MemTotal: 65747640 kB

-----  
10. who -r  
run-level 3 Mar 4 02:57

-----  
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)  
Default Target Status  
multi-user running

-----  
12. Services, from systemctl list-unit-files  
STATE UNIT FILES  
enabled apparmor auditd cron getty@ irqbalance issue-generator kbdsettings lvm2-monitor postfix  
purge-kernels rollback sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4  
wickedd-dhcp6 wickedd-nanny  
enabled-runtime systemd-remount-fs  
disabled blk-availability boot-sysctl ca-certificates chrony-wait chronyd console-getty debug-shell  
grub2-once haveged hwloc-dump-hwdata issue-add-ssh-keys kexec-load lunmask rpmconfigcheck  
serial-getty@ systemd-boot-check-no-failures systemd-confext systemd-network-generator  
systemd-sysext systemd-time-wait-sync systemd-timesyncd  
indirect systemd-userdbd wickedd

-----  
13. Linux kernel boot-time arguments, from /proc/cmdline  
BOOT\_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default  
root=UUID=1601d4e3-aed0-470e-8bd5-82680fe83224  
splash=silent  
resume=/dev/disk/by-uuid/e928ac5a-0714-4827-a56e-3fd30f6224d5  
mitigations=auto  
quiet  
security=apparmor

-----  
14. cpupower frequency-info  
analyzing CPU 0:  
Unable to determine current policy  
boost state support:  
Supported: yes  
Active: yes

-----  
15. sysctl  
kernel.numa\_balancing 0  
kernel.randomize\_va\_space 2  
vm.compaction\_proactiveness 20  
vm.dirty\_background\_bytes 0  
vm.dirty\_background\_ratio 10  
vm.dirty\_bytes 0  
vm.dirty\_expire\_centisecs 3000  
vm.dirty\_ratio 20  
vm.dirty\_writeback\_centisecs 500  
vm.dirtytime\_expire\_seconds 43200

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant ML30 Gen11**

(3.50 GHz, Intel Xeon 6325P)

**SPECrate®2017\_int\_base = 55.9**

**SPECrate®2017\_int\_peak = 58.0**

**CPU2017 License:** 3

**Test Sponsor:** HPE

**Tested by:** HPE

**Test Date:** Mar-2025

**Hardware Availability:** Mar-2025

**Software Availability:** Jun-2024

## Platform Notes (Continued)

```

vm.extfrag_threshold          500
vm.min_unmapped_ratio        1
vm.nr_hugepages               0
vm.nr_hugepages_mempolicy    0
vm.nr_overcommit_hugepages   0
vm.swappiness                 60
vm.watermark_boost_factor    15000
vm.watermark_scale_factor    10
vm.zone_reclaim_mode         0

```

```

-----
16. /sys/kernel/mm/transparent_hugepage
defrag          always defer defer+madvice [madvice] never
enabled         [always] madvice never
hpage_pmd_size 2097152
shmem_enabled   always within_size advise [never] deny force

```

```

-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag                 1
max_ptes_none         511
max_ptes_shared       256
max_ptes_swap         64
pages_to_scan         4096
scan_sleep_millisecs 10000

```

```

-----
18. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP6

```

```

-----
19. Disk information
SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/sdb4  xfs  342G  68G  274G  20% /home

```

```

-----
20. /sys/devices/virtual/dmi/id
Vendor:      HPE
Product:     ProLiant ML30 Gen11
Product Family: ProLiant
Serial:      LXVBT01BVHZ03N

```

```

-----
21. dmidecode
Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
  2x Hynix HMC88AGBEA084N 32 GB 2 rank 5600, configured at 4400

```

```

-----
22. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor:      HPE
BIOS Version:     2.10

```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant ML30 Gen11**

(3.50 GHz, Intel Xeon 6325P)

**SPECrate®2017\_int\_base = 55.9**

**SPECrate®2017\_int\_peak = 58.0**

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Mar-2025  
**Hardware Availability:** Mar-2025  
**Software Availability:** Jun-2024

## Platform Notes (Continued)

BIOS Date: 12/06/2024  
BIOS Revision: 2.10  
Firmware Revision: 1.62

## Compiler Version Notes

=====  
C | 502.gcc\_r(peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.  
=====

=====  
C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.  
=====

=====  
C | 502.gcc\_r(peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.  
=====

=====  
C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.  
=====

=====  
C++ | 520.omnetpp\_r(base, peak) 523.xalancbmk\_r(base, peak) 531.deepsjeng\_r(base, peak)  
| 541.leela\_r(base, peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.  
=====

=====  
Fortran | 548.exchange2\_r(base, peak)  
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.  
=====

## Base Compiler Invocation

C benchmarks:  
icx

(Continued on next page)





# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant ML30 Gen11

(3.50 GHz, Intel Xeon 6325P)

SPECrate®2017\_int\_base = 55.9

SPECrate®2017\_int\_peak = 58.0

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Mar-2025

Hardware Availability: Mar-2025

Software Availability: Jun-2024

## Base Compiler Invocation (Continued)

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Base Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX2 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc
```

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX2 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xCORE-AVX2 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc
```



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant ML30 Gen11

(3.50 GHz, Intel Xeon 6325P)

SPECrate®2017\_int\_base = 55.9

SPECrate®2017\_int\_peak = 58.0

**CPU2017 License:** 3

**Test Sponsor:** HPE

**Tested by:** HPE

**Test Date:** Mar-2025

**Hardware Availability:** Mar-2025

**Software Availability:** Jun-2024

## Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Peak Portability Flags

```

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

```

## Peak Optimization Flags

C benchmarks:

```

500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2 -flto
-Ofast -ffast-math -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-strict-overflow
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

502.gcc_r: -m32 -L/opt/intel/oneapi/compiler/2024.1/lib32 -std=gnu89
-Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2 -flto
-Ofast -ffast-math -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -L/usr/local/jemalloc32-5.0.1/lib
-ljemalloc

505.mcf_r: basepeak = yes

```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant ML30 Gen11

(3.50 GHz, Intel Xeon 6325P)

SPECrate®2017\_int\_base = 55.9

SPECrate®2017\_int\_peak = 58.0

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Mar-2025

Hardware Availability: Mar-2025

Software Availability: Jun-2024

## Peak Optimization Flags (Continued)

```
525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-alias
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmallo
```

557.xz\_r: basepeak = yes

C++ benchmarks:

520.omnetpp\_r: basepeak = yes

523.xalancbmk\_r: basepeak = yes

531.deepsjeng\_r: basepeak = yes

541.leela\_r: basepeak = yes

Fortran benchmarks:

548.exchange2\_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-CatlowRefresh-rev1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-CatlowRefresh-rev1.0.xml>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.9 on 2025-03-04 03:00:05-0500.

Report generated on 2025-03-26 10:34:47 by CPU2017 PDF formatter v6716.

Originally published on 2025-03-25.