SPEC CPU®2017 Integer Speed Result

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Dell Inc.

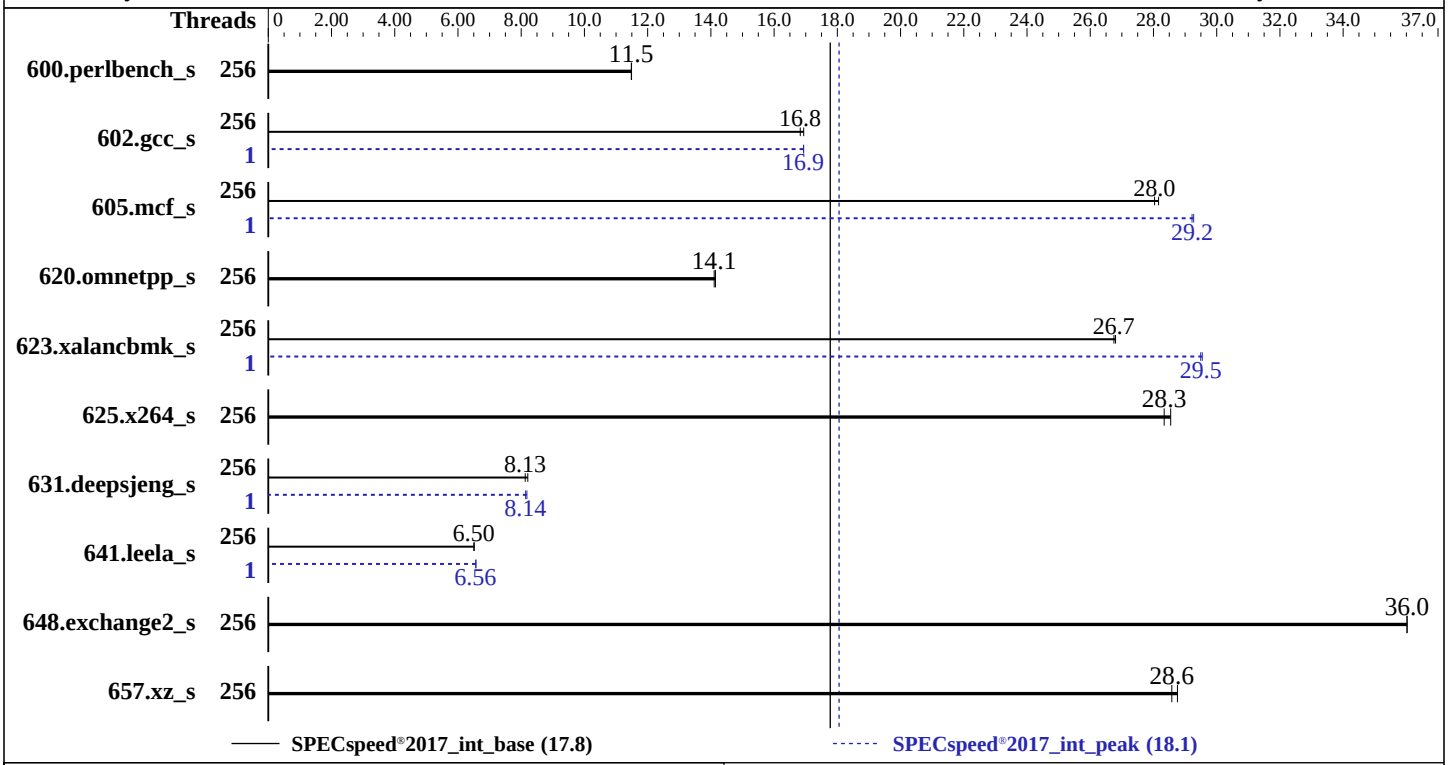
PowerEdge R6725 (AMD EPYC 9755 128-Core Processor)

SPECspeed®2017_int_base = 17.8

SPECspeed®2017_int_peak = 18.1

CPU2017 License: 6573
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Apr-2025
Hardware Availability: Mar-2025
Software Availability: Oct-2024



Hardware

CPU Name:	AMD EPYC 9755
Max MHz:	4100
Nominal:	2700
Enabled:	256 cores, 2 chips
Orderable:	1,2 chips
Cache L1:	32 KB I + 48 KB D on chip per core
L2:	1 MB I+D on chip per core
L3:	512 MB I+D on chip per chip, 32 MB shared / 8 cores
Other:	None
Memory:	1536 GB (24 x 64 GB 2Rx4 PC5-6400B-R)
Storage:	130 GB on tmpfs
Other:	CPU Cooling: Air

Software

OS:	Ubuntu 24.04 LTS 6.8.0-44-generic
Compiler:	C/C++/Fortran: Version 5.0.0 of AOCC
Parallel:	Yes
Firmware:	Version 1.1.3 released Feb-2025
File System:	tmpfs
System State:	Run level 5 (graphical multi-user)
Base Pointers:	64-bit
Peak Pointers:	64-bit
Other:	None
Power Management:	BIOS and OS set to prefer performance at the cost of additional power usage.

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Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	256	154	11.5	**155**	**11.5**			256	154	11.5	**155**	**11.5**		
602.gcc_s	256	235	16.9	**237**	**16.8**			1	235	16.9	**235**	**16.9**		
605.mcf_s	256	168	28.2	**168**	**28.0**			1	**162**	**29.2**	161	29.3		
620.omnetpp_s	256	115	14.1	**116**	**14.1**			256	115	14.1	**116**	**14.1**		
623.xalancbmk_s	256	**53.0**	**26.7**	52.9	26.8			1	**48.0**	**29.5**	48.0	29.5		
625.x264_s	256	**62.2**	**28.3**	61.8	28.5			256	**62.2**	**28.3**	61.8	28.5		
631.deepsjeng_s	256	175	8.21	**176**	**8.13**			1	175	8.18	**176**	**8.14**		
641.leela_s	256	**262**	**6.50**	262	6.52			1	260	6.57	**260**	**6.56**		
648.exchange2_s	256	**81.6**	**36.0**	81.6	36.0			256	**81.6**	**36.0**	81.6	36.0		
657.xz_s	256	215	28.8	**216**	**28.6**			256	215	28.8	**216**	**28.6**		

SPECspeed®2017_int_base = 17.8

SPECspeed®2017_int_peak = 18.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

```
The AMD64 AOCC Compiler Suite is available at
http://developer.amd.com/amd-aocc/
```

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
GOMP_CPU_AFFINITY = "0-255"
LD_LIBRARY_PATH =
     "/mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.3/amd_speed_aocc500_znver5_A_lib/lib:/mnt/ramdisk/cpu201
     7-1.1.9-aocc500-znerv5_A1.3/amd_speed_aocc500_znver5_A_lib/lib32:"
LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"
MALLOC_CONF = "retain:true"
OMP_DYNAMIC = "false"
OMP_SCHEDULE = "static"
OMP_STACKSIZE = "128M"
OMP_THREAD_LIMIT = "256"
```

General Notes

```
Binaries were compiled on a system with 2x AMD EPYC 9D64 CPU + 500GiB Memory using Ubuntu 22.04

 Benchmark run from a 130 GB ramdisk created with the cmd: "mount -t tmpfs -o size=130G tmpfs /mnt/ramdisk"
```

Platform Notes

```
BIOS Settings:
                   Logical Processor : Disabled
            Virtualization Technology : Disabled
                NUMA Nodes Per Socket : 4

                        System Profile : Custom
                              C-States : Disabled
                  Memory Patrol Scrub : Disabled
   PCI ASPM L1 Link Power Management : Disabled
     Periodic Directory Rinse Tuning : Blended
                  Determinism Control : Manual
                   Determinism Slider : Power Determinism
                       Optimizer Mode : Enabled
 Algorithm Performance Boost Disable : Enabled
                  Adaptive Allocation : Enabled
                   Dram Refresh Delay : Performance
                  DIMM Self Healing -
       on Uncorrectable Memory Error : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.3/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on SLR6704-R6725 Mon Apr 21 22:57:46 2025

SUT (System Under Test) info as seen by some common utilities.

------------------------------------------------------------
Table of contents
------------------------------------------------------------
 1. uname -a
 2. w
 3. Username
 4. ulimit -a
 5. sysinfo process ancestry
 6. /proc/cpuinfo
 7. lscpu
 8. numactl --hardware
```

(Continued on next page)

Platform Notes (Continued)

```
 9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 255 (255.4-1ubuntu8)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS
------------------------------------------------------------

------------------------------------------------------------
1. uname -a
  Linux SLR6704-R6725 6.8.0-44-generic #44-Ubuntu SMP PREEMPT_DYNAMIC Tue Aug 13 13:35:26 UTC 2024 x86_64
  x86_64 x86_64 GNU/Linux

------------------------------------------------------------
2. w
   22:57:46 up 3 min,  1 user,  load average: 0.23, 0.08, 0.03
  USER     TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT
  root     tty1     -                22:54   58.00s  1.33s  0.43s /bin/bash ./amd_speed_aocc500_znver5_A1.sh

------------------------------------------------------------
3. Username
  From environment variable $USER:  root

------------------------------------------------------------
4. ulimit -a
  time(seconds)        unlimited
  file(blocks)         unlimited
  data(kbytes)         unlimited
  stack(kbytes)        unlimited
  coredump(blocks)     0
  memory(kbytes)       unlimited
  locked memory(kbytes) 2097152
  process              6188991
  nofiles              1024
  vmemory(kbytes)      unlimited
  locks                unlimited
  rtprio               0

------------------------------------------------------------
5. sysinfo process ancestry
 /sbin/init
 /bin/login -p --
 -bash
 /bin/bash /home/DellFiles/bin/DELL_speed.sh
 /bin/bash /home/DellFiles/bin/dell-run-main.sh speed
 /bin/bash /home/DellFiles/bin/dell-run-main.sh speed
 /bin/bash /home/DellFiles/bin/AMD/dell-run-speccpu.sh speed --define DL-VERS=6.2 --output_format
   html,pdf,txt
 python3 ./run_amd_speed_aocc500_znver5_A1.py
 /bin/bash ./amd_speed_aocc500_znver5_A1.sh
 runcpu --config amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 2 --define
```

(Continued on next page)

Platform Notes (Continued)

```
   DL-BIOS-NPS=4 --define DL-VERS=6.2 --output_format html,pdf,txt intspeed
 runcpu --configfile amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 2 --define
   DL-BIOS-NPS=4 --define DL-VERS=6.2 --output_format html,pdf,txt --nopower --runmode speed --tune base:peak
   --size test:train:refspeed intspeed --nopreenv --note-preenv --logfile
   $SPEC/tmp/CPU2017.001/templogs/preenv.intspeed.001.0.log --lognum 001.0 --from_runcpu 2
 specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.3

------------------------------------------------------------
6. /proc/cpuinfo
    model name      : AMD EPYC 9755 128-Core Processor
    vendor_id       : AuthenticAMD
    cpu family      : 26
    model           : 2
    stepping        : 1
    microcode       : 0xb00211e
    bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
    TLB size        : 192 4K pages
    cpu cores       : 128
    siblings        : 128
    2 physical ids (chips)
    256 processors (hardware threads)
    physical id 0: core ids
    0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119,128-135,144-151,160-167,176-183,192-199,208-215,224-231,
    240-247
    physical id 1: core ids
    0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119,128-135,144-151,160-167,176-183,192-199,208-215,224-231,
    240-247
    physical id 0: apicids
    0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119,128-135,144-151,160-167,176-183,192-199,208-215,224-231,
    240-247
    physical id 1: apicids
    256-263,272-279,288-295,304-311,320-327,336-343,352-359,368-375,384-391,400-407,416-423,432-439,448-455,4
    64-471,480-487,496-503
  Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
  virtualized systems.  Use the above data carefully.

------------------------------------------------------------
7. lscpu

From lscpu from util-linux 2.39.3:
  Architecture:                         x86_64
  CPU op-mode(s):                       32-bit, 64-bit
  Address sizes:                        52 bits physical, 57 bits virtual
  Byte Order:                           Little Endian
  CPU(s):                               256
  On-line CPU(s) list:                  0-255
  Vendor ID:                            AuthenticAMD
  BIOS Vendor ID:                       AMD
  Model name:                           AMD EPYC 9755 128-Core Processor
  BIOS Model name:                      AMD EPYC 9755 128-Core Processor                CPU @ 2.7GHz
  BIOS CPU family:                      107
  CPU family:                           26
  Model:                                2
  Thread(s) per core:                   1
  Core(s) per socket:                   128
  Socket(s):                            2
  Stepping:                             1
  Frequency boost:                      enabled
  CPU(s) scaling MHz:                   66%
```

(Continued on next page)

Platform Notes (Continued)

```
   CPU max MHz:                          4121.1909
   CPU min MHz:                          1500.0000
   BogoMIPS:                             5392.65
   Flags:                                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                                         pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
                                         rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
                                         extd_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
                                         sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm
                                         cmp_legacy extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
                                         osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
                                         perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2
                                         ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmi1 avx2
                                         smep bmi2 invpcid cqm rdt_a avx512f avx512dq rdseed adx smap
                                         avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
                                         xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                                         cqm_mbm_local user_shstk avx_vnni avx512_bf16 clzero irperf
                                         xsaveerptr rdpru wbnoinvd amd_ppin cppc arat npt lbrv svm_lock
                                         nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter
                                         pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl vnmi
                                         avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq
                                         avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect
                                         movdiri movdir64b overflow_recov succor smca avx512_vp2intersect
                                         flush_l1d debug_swap
   L1d cache:                            12 MiB (256 instances)
   L1i cache:                            8 MiB (256 instances)
   L2 cache:                             256 MiB (256 instances)
   L3 cache:                             1 GiB (32 instances)
   NUMA node(s):                         8
   NUMA node0 CPU(s):                    0-31
   NUMA node1 CPU(s):                    32-63
   NUMA node2 CPU(s):                    64-95
   NUMA node3 CPU(s):                    96-127
   NUMA node4 CPU(s):                    128-159
   NUMA node5 CPU(s):                    160-191
   NUMA node6 CPU(s):                    192-223
   NUMA node7 CPU(s):                    224-255
   Vulnerability Gather data sampling:   Not affected
   Vulnerability Itlb multihit:          Not affected
   Vulnerability L1tf:                   Not affected
   Vulnerability Mds:                    Not affected
   Vulnerability Meltdown:               Not affected
   Vulnerability Mmio stale data:        Not affected
   Vulnerability Reg file data sampling: Not affected
   Vulnerability Retbleed:               Not affected
   Vulnerability Spec rstack overflow:   Not affected
   Vulnerability Spec store bypass:      Mitigation; Speculative Store Bypass disabled via prctl
   Vulnerability Spectre v1:             Mitigation; usercopy/swapgs barriers and __user pointer sanitization
   Vulnerability Spectre v2:             Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP
                                         disabled; RSB filling; PBRSB-eIBRS Not affected; BHI Not affected
   Vulnerability Srbds:                  Not affected
   Vulnerability Tsx async abort:        Not affected

 From lscpu --cache:
      NAME ONE-SIZE ALL-SIZE WAYS TYPE        LEVEL  SETS PHY-LINE COHERENCY-SIZE
      L1d       48K      12M   12 Data            1    64        1             64
      L1i       32K       8M    8 Instruction     1    64        1             64
      L2         1M     256M   16 Unified         2  1024        1             64
      L3        32M       1G   16 Unified         3 32768        1             64

 ------------------------------------------------------------
```

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Platform Notes (Continued)

```
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
  available: 8 nodes (0-7)
  node 0 cpus: 0-31
  node 0 size: 192771 MB
  node 0 free: 192105 MB
  node 1 cpus: 32-63
  node 1 size: 193520 MB
  node 1 free: 193154 MB
  node 2 cpus: 64-95
  node 2 size: 193520 MB
  node 2 free: 192806 MB
  node 3 cpus: 96-127
  node 3 size: 193461 MB
  node 3 free: 193126 MB
  node 4 cpus: 128-159
  node 4 size: 193520 MB
  node 4 free: 193104 MB
  node 5 cpus: 160-191
  node 5 size: 193520 MB
  node 5 free: 193151 MB
  node 6 cpus: 192-223
  node 6 size: 193520 MB
  node 6 free: 189310 MB
  node 7 cpus: 224-255
  node 7 size: 193481 MB
  node 7 free: 193130 MB
  node distances:
  node   0   1   2   3   4   5   6   7
    0:  10  12  12  12  32  32  32  32
    1:  12  10  12  12  32  32  32  32
    2:  12  12  10  12  32  32  32  32
    3:  12  12  12  10  32  32  32  32
    4:  32  32  32  32  10  12  12  12
    5:  32  32  32  32  12  10  12  12
    6:  32  32  32  32  12  12  10  12
    7:  32  32  32  32  12  12  12  10

------------------------------------------------------------
9. /proc/meminfo
   MemTotal:       1584454360 kB

------------------------------------------------------------
10. who -r
  run-level 5 Apr 21 22:54

------------------------------------------------------------
11. Systemd service manager version: systemd 255 (255.4-1ubuntu8)
  Default Target  Status
  graphical       running

------------------------------------------------------------
12. Services, from systemctl list-unit-files
  STATE            UNIT FILES
  enabled          ModemManager apparmor apport blk-availability cloud-config cloud-final cloud-init
                   cloud-init-local console-setup cron dmesg e2scrub_reap finalrd getty@ gpu-manager
                   grub-common grub-initrd-fallback keyboard-setup lm-sensors lvm2-monitor multipathd
                   networkd-dispatcher open-iscsi open-vm-tools pollinate rsyslog secureboot-db setvtrgb
                   sysstat systemd-networkd systemd-networkd-wait-online systemd-pstore systemd-resolved
                   systemd-timesyncd thermald tuned ua-reboot-cmds ubuntu-advantage udisks2 ufw vgauth
```

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Platform Notes (Continued)

```
  enabled-runtime  netplan-ovs-cleanup systemd-fsck-root systemd-remount-fs
  disabled         console-getty debug-shell iscsid nftables rsync serial-getty@ ssh
                   systemd-boot-check-no-failures systemd-confext systemd-network-generator
                   systemd-networkd-wait-online@ systemd-pcrlock-file-system systemd-pcrlock-firmware-code
                   systemd-pcrlock-firmware-config systemd-pcrlock-machine-id systemd-pcrlock-make-policy
                   systemd-pcrlock-secureboot-authority systemd-pcrlock-secureboot-policy systemd-sysext
                   systemd-time-wait-sync upower
  indirect         systemd-sysupdate systemd-sysupdate-reboot uuidd
  masked           cryptdisks cryptdisks-early hwclock multipath-tools-boot screen-cleanup sudo x11-common

------------------------------------------------------------
13. Linux kernel boot-time arguments, from /proc/cmdline
  BOOT_IMAGE=/boot/vmlinuz-6.8.0-44-generic
  root=UUID=8458ae54-58cc-4621-9289-b1d743fde503
  ro

------------------------------------------------------------
14. cpupower frequency-info
  analyzing CPU 12:
    current policy: frequency should be within 1.50 GHz and 2.70 GHz.
                    The governor "performance" may decide which speed to use
                    within this range.
    boost state support:
      Supported: yes
      Active: yes
      Boost States: 0
      Total States: 3
      Pstate-P0:  2800MHz

------------------------------------------------------------
15. tuned-adm active
  Current active profile: latency-performance

------------------------------------------------------------
16. sysctl
  kernel.numa_balancing               1
  kernel.randomize_va_space           0
  vm.compaction_proactiveness        20
  vm.dirty_background_bytes           0
  vm.dirty_background_ratio           3
  vm.dirty_bytes                      0
  vm.dirty_expire_centisecs        3000
  vm.dirty_ratio                      8
  vm.dirty_writeback_centisecs      500
  vm.dirtytime_expire_seconds     43200
  vm.extfrag_threshold              500
  vm.min_unmapped_ratio               1
  vm.nr_hugepages                     0
  vm.nr_hugepages_mempolicy           0
  vm.nr_overcommit_hugepages          0
  vm.swappiness                       1
  vm.watermark_boost_factor       15000
  vm.watermark_scale_factor          10
  vm.zone_reclaim_mode                1

------------------------------------------------------------
17. /sys/kernel/mm/transparent_hugepage
  defrag          [always] defer defer+madvise madvise never
  enabled         [always] madvise never
  hpage_pmd_size  2097152
```

(Continued on next page)

Platform Notes (Continued)

```
  shmem_enabled   always within_size advise [never] deny force

------------------------------------------------------------
18. /sys/kernel/mm/transparent_hugepage/khugepaged
  alloc_sleep_millisecs   60000
  defrag                      1
  max_ptes_none             511
  max_ptes_shared           256
  max_ptes_swap              64
  pages_to_scan            4096
  scan_sleep_millisecs    10000

------------------------------------------------------------
19. OS release
  From /etc/*-release /etc/*-version
  os-release Ubuntu 24.04 LTS

------------------------------------------------------------
20. Disk information
SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.3
  Filesystem     Type   Size  Used Avail Use% Mounted on
  tmpfs          tmpfs  130G  3.3G  127G   3% /mnt/ramdisk

------------------------------------------------------------
21. /sys/devices/virtual/dmi/id
    Vendor:          Dell Inc.
    Product:         PowerEdge R6725
    Product Family: PowerEdge
    Serial:          SLR6704

------------------------------------------------------------
22. dmidecode
  Additional information from dmidecode 3.5 follows.  WARNING: Use caution when you interpret this section.
  The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
  determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
  "DMTF SMBIOS" standard.
  Memory:
    24x 80AD000080AD HMCG94AHBRA283N 64 GB 2 rank 6400


------------------------------------------------------------
23. BIOS
(This section combines info from /sys/devices and dmidecode.)
   BIOS Vendor:       Dell Inc.
   BIOS Version:      1.1.3
   BIOS Date:         02/25/2025
   BIOS Revision:     1.1
```

Compiler Version Notes

```
==============================================================================================================
C       | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak)
        | 657.xz_s(base, peak)
--------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
--------------------------------------------------------------------------------------------------------------
```

(Continued on next page)

Compiler Version Notes (Continued)

```
==============================================================================================================
C++     | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak)
        | 641.leela_s(base, peak)
--------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
Fortran | 648.exchange2_s(base, peak)
--------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
--------------------------------------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
clang

C++ benchmarks:
clang++

Fortran benchmarks:
flang

Base Portability Flags

600.perlbench_s: -DSPEC_LINUX_X64 -DSPEC_LP64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LINUX -DSPEC_LP64
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-allow-multiple-definition -Wl,-mllvm -Wl,-extra-inliner -O3
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP
-flto -fremap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fopenmp=libomp -lomp -lamdlibm
-lflang -lamdalloc
```

C++ benchmarks:
```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -mllvm -unroll-threshold=100 -zopt
-fvirtual-function-elimination -fvisibility=hidden -fopenmp=libomp
-lomp -lamdlibm -lflang -lamdalloc-ext
```

Fortran benchmarks:
```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-iv-split -Wl,-mllvm -Wl,-inline-recursion=4
-Wl,-mllvm -Wl,-lsr-in-nested-loop -O3 -march=znver5 -fveclib=AMDLIBM
-ffast-math -fopenmp -flto -mllvm -optimize-strided-mem-cost
-mllvm -unroll-aggressive -mllvm -unroll-threshold=150 -fopenmp=libomp
-lomp -lamdlibm -lflang -lamdalloc
```

Base Other Flags

C benchmarks:
```
-Wno-return-type -Wno-unused-command-line-argument
```

C++ benchmarks:
```
-Wno-unused-command-line-argument
```

Fortran benchmarks:
```
-Wno-unused-command-line-argument
```

Peak Compiler Invocation

C benchmarks:
clang

C++ benchmarks:
clang++

Fortran benchmarks:
flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

600.perlbench_s: basepeak = yes

602.gcc_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-allow-multiple-definition
-Wl,-mllvm -Wl,-extra-inliner -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -flto
-DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang

605.mcf_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-extra-inliner -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -flto
-DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang

625.x264_s: basepeak = yes

(Continued on next page)

Peak Optimization Flags (Continued)

657.xz_s: `basepeak = yes`

C++ benchmarks:

620.omnetpp_s: `basepeak = yes`

623.xalancbmk_s: `-m64 -std=c++14`
`-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6`
`-Wl,-mllvm -Wl,-reduce-array-computations=3`
`-Wl,-mllvm -Wl,-do-block-reorder=advanced -Ofast`
`-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp`
`-flto -DSPEC_OPENMP -mllvm -reduce-array-computations=3`
`-mllvm -unroll-threshold=100 -zopt`
`-fvirtual-function-elimination -fvisibility=hidden`
`-mllvm -do-block-reorder=advanced -fopenmp=libomp -lomp`
`-lamdlibm -lamdalloc-ext -lflang`

631.deepsjeng_s: `-m64 -std=c++14`
`-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6`
`-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast`
`-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp`
`-flto -DSPEC_OPENMP -mllvm -reduce-array-computations=3`
`-mllvm -unroll-threshold=100 -zopt`
`-fvirtual-function-elimination -fvisibility=hidden`
`-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang`

641.leela_s: `Same as 631.deepsjeng_s`

Fortran benchmarks:

648.exchange2_s: `basepeak = yes`

Peak Other Flags

C benchmarks:
`-Wno-return-type -Wno-unused-command-line-argument`

C++ benchmarks:
`-Wno-unused-command-line-argument`

Fortran benchmarks:
`-Wno-unused-command-line-argument`

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html
http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.8.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml
http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.8.xml

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