SPEC CPU®2017 Floating Point Rate Result

Fsas Technologies Inc.

(Test Sponsor: Fujitsu)

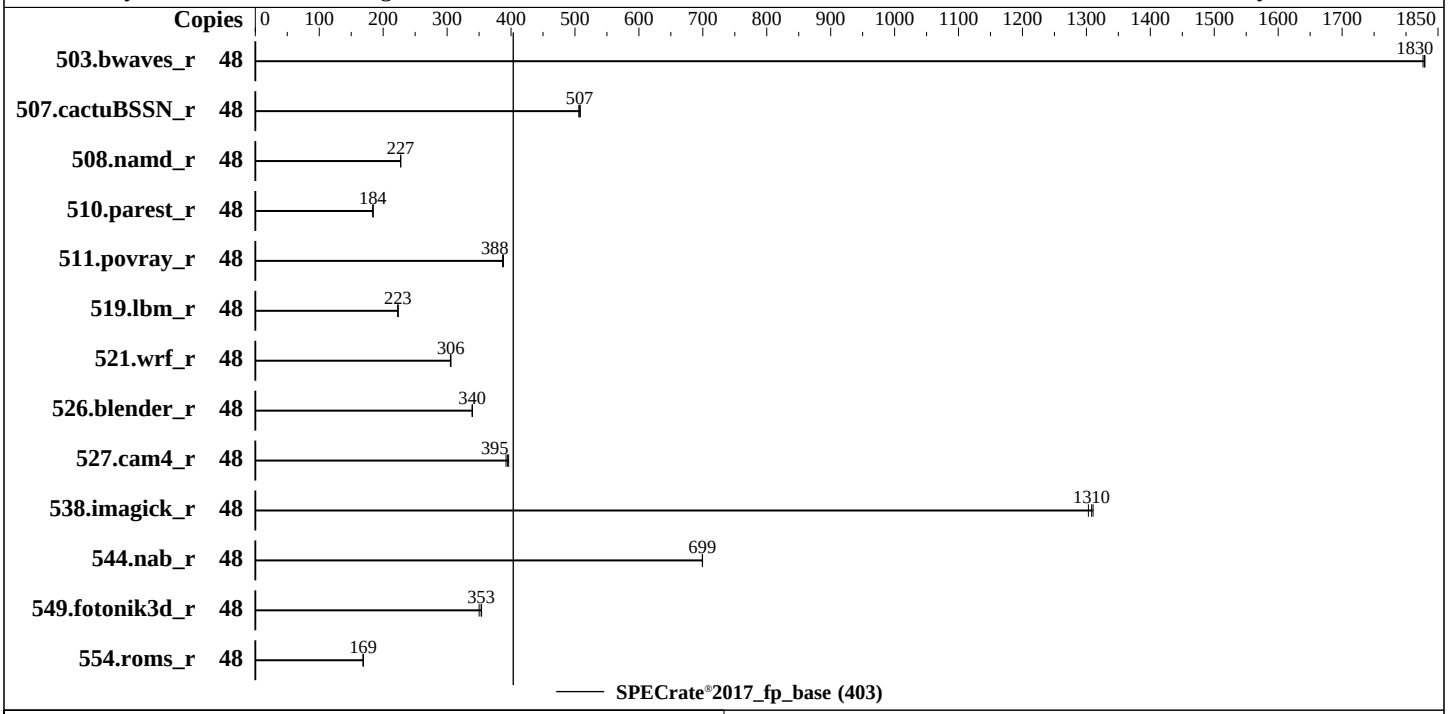
PRIMERGY RX2540 M8, Intel Xeon 6505P, 2.20GHz

SPECrate®2017_fp_base = 403

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fsas Technologies Inc.

Test Date: Sep-2025
Hardware Availability: Oct-2025
Software Availability: Jun-2025



Hardware

CPU Name:	Intel Xeon 6505P
Max MHz:	4100
Nominal:	2200
Enabled:	24 cores, 2 chips, 2 threads/core
Orderable:	1,2 chips
Cache L1:	64 KB I + 48 KB D on chip per core
L2:	2 MB I+D on chip per core
L3:	48 MB I+D on chip per chip
Other:	None
Memory:	512 GB (16 x 32 GB 2Rx8 PC5-6400B-R)
Storage:	1 x SSD PCIe M.2, 960GB
Other:	CPU Cooling: Air

Software

OS:	SUSE Linux Enterprise Server 15 SP7 6.4.0-150700.51-default
Compiler:	C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;
Parallel:	No
Firmware:	Fsas Technologies Inc. BIOS Version V1.0.0.0 R1.1.0 for D4135-A1x. Released Oct-2025
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	Not Applicable
Other:	jemalloc memory allocator V5.0.1
Power Management:	BIOS set to prefer performance at the cost of additional power usage

SPEC CPU®2017 Floating Point Rate Result

Fsas Technologies Inc.

(Test Sponsor: Fujitsu)

PRIMERGY RX2540 M8, Intel Xeon 6505P, 2.20GHz

SPECrate®2017_fp_base = 403

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fsas Technologies Inc.

Test Date: Sep-2025
Hardware Availability: Oct-2025
Software Availability: Jun-2025

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	48	263	1830	264	1830	**263**	**1830**							
507.cactuBSSN_r	48	**120**	**507**	120	506	120	508							
508.namd_r	48	**201**	**227**	201	227	201	227							
510.parest_r	48	**682**	**184**	682	184	683	184							
511.povray_r	48	289	388	**289**	**388**	290	387							
519.lbm_r	48	227	223	227	223	**227**	**223**							
521.wrf_r	48	352	306	352	305	**352**	**306**							
526.blender_r	48	215	340	**215**	**340**	216	339							
527.cam4_r	48	212	396	214	392	**213**	**395**							
538.imagick_r	48	91.1	1310	**91.3**	**1310**	91.6	1300							
544.nab_r	48	116	699	**116**	**699**	115	700							
549.fotonik3d_r	48	534	350	**530**	**353**	528	354							
554.roms_r	48	452	169	**452**	**169**	452	169							

SPECrate®2017_fp_base = 403

SPECrate®2017_fp_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/Benchmark/cpu2017/lib/intel64:/home/Benchmark/cpu2017/je5.0.1-64"
MALLOC_CONF = "retain:true"
```

General Notes

```
Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>       /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
```

(Continued on next page)

General Notes (Continued)

```
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```

Platform Notes

```
BIOS configuration:
Fan Control = Full
CPU Performance Boost = Aggressive

Sysinfo program /home/Benchmark/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Tue Sep 16 07:26:14 2025

SUT (System Under Test) info as seen by some common utilities.

------------------------------------------------------------
Table of contents
------------------------------------------------------------
 1. uname -a
 2. w
 3. Username
 4. ulimit -a
 5. sysinfo process ancestry
 6. /proc/cpuinfo
 7. lscpu
 8. numactl --hardware
 9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.24+suse.148.g83b9060b6e)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS
------------------------------------------------------------

------------------------------------------------------------
1. uname -a
  Linux localhost 6.4.0-150700.51-default #1 SMP PREEMPT_DYNAMIC Wed Apr 30 21:35:43 UTC 2025 (6930611)
  x86_64 x86_64 x86_64 GNU/Linux

------------------------------------------------------------
2. w
   07:26:14 up 4 min,  1 user,  load average: 0.09, 0.10, 0.05
  USER     TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT
  root     tty1     -                07:25   14.00s  1.13s  0.13s -bash

------------------------------------------------------------
```

(Continued on next page)

Platform Notes (Continued)

```
 3. Username
   From environment variable $USER:  root

 ------------------------------------------------------------
 4. ulimit -a
   core file size          (blocks, -c) unlimited
   data seg size           (kbytes, -d) unlimited
   scheduling priority             (-e) 0
   file size               (blocks, -f) unlimited
   pending signals                 (-i) 2062454
   max locked memory       (kbytes, -l) 8192
   max memory size         (kbytes, -m) unlimited
   open files                      (-n) 1024
   pipe size            (512 bytes, -p) 8
   POSIX message queues     (bytes, -q) 819200
   real-time priority              (-r) 0
   stack size              (kbytes, -s) unlimited
   cpu time               (seconds, -t) unlimited
   max user processes              (-u) 2062454
   virtual memory          (kbytes, -v) unlimited
   file locks                      (-x) unlimited

 ------------------------------------------------------------
 5. sysinfo process ancestry
  /usr/lib/systemd/systemd --switched-root --system --deserialize=42
  login -- root
  -bash
  -bash
  runcpu --nobuild --action validate --define default-platform-flags --define numcopies=48 -c
    ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=24 --define physicalfirst
    --define invoke_with_interleave --define drop_caches --tune base -o all fprate
  runcpu --nobuild --action validate --define default-platform-flags --define numcopies=48 --configfile
    ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=24 --define physicalfirst
    --define invoke_with_interleave --define drop_caches --tune base --output_format all --nopower --runmode
    rate --tune base --size refrate fprate --nopreenv --note-preenv --logfile
    $SPEC/tmp/CPU2017.001/templogs/preenv.fprate.001.0.log --lognum 001.0 --from_runcpu 2
  specperl $SPEC/bin/sysinfo
 $SPEC = /home/Benchmark/cpu2017

 ------------------------------------------------------------
 6. /proc/cpuinfo
     model name      : Intel(R) Xeon(R) 6505P
     vendor_id       : GenuineIntel
     cpu family      : 6
     model           : 173
     stepping        : 1
     microcode       : 0x10003c2
     bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs bhi
     cpu cores       : 12
     siblings        : 24
     2 physical ids (chips)
     48 processors (hardware threads)
     physical id 0: core ids 0-11
     physical id 1: core ids 0-11
     physical id 0: apicids 0-23
     physical id 1: apicids 128-151
   Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
   virtualized systems.  Use the above data carefully.

 ------------------------------------------------------------
```

(Continued on next page)

Platform Notes (Continued)

```
 7. lscpu

 From lscpu from util-linux 2.40.4:
   Architecture:                         x86_64
   CPU op-mode(s):                       32-bit, 64-bit
   Address sizes:                        52 bits physical, 57 bits virtual
   Byte Order:                           Little Endian
   CPU(s):                               48
   On-line CPU(s) list:                  0-47
   Vendor ID:                            GenuineIntel
   Model name:                           Intel(R) Xeon(R) 6505P
   CPU family:                           6
   Model:                                173
   Thread(s) per core:                   2
   Core(s) per socket:                   12
   Socket(s):                            2
   Stepping:                             1
   CPU(s) scaling MHz:                   25%
   CPU max MHz:                          4100.0000
   CPU min MHz:                          800.0000
   BogoMIPS:                             4400.00
   Flags:                                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                                         pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
                                         pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good
                                         nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni
                                         pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
                                         xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt
                                         tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm
                                         3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 intel_ppin cdp_l2
                                         ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow flexpriority ept
                                         vpid ept_ad fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid cqm
                                         rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb
                                         intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1
                                         xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
                                         split_lock_detect user_shstk avx_vnni avx512_bf16 wbnoinvd dtherm ida
                                         arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req vnmi avx512vbmi
                                         umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni
                                         avx512_bitalg tme avx512_vpopcntdq la57 rdpid bus_lock_detect
                                         cldemote movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk
                                         pconfig arch_lbr ibt amx_bf16 avx512_fp16 amx_tile amx_int8 flush_l1d
                                         arch_capabilities
   Virtualization:                       VT-x
   L1d cache:                            1.1 MiB (24 instances)
   L1i cache:                            1.5 MiB (24 instances)
   L2 cache:                             48 MiB (24 instances)
   L3 cache:                             96 MiB (2 instances)
   NUMA node(s):                         2
   NUMA node0 CPU(s):                    0-11,24-35
   NUMA node1 CPU(s):                    12-23,36-47
   Vulnerability Gather data sampling:   Not affected
   Vulnerability Itlb multihit:          Not affected
   Vulnerability L1tf:                   Not affected
   Vulnerability Mds:                    Not affected
   Vulnerability Meltdown:               Not affected
   Vulnerability Mmio stale data:        Not affected
   Vulnerability Reg file data sampling: Not affected
   Vulnerability Retbleed:               Not affected
   Vulnerability Spec rstack overflow:   Not affected
   Vulnerability Spec store bypass:      Mitigation; Speculative Store Bypass disabled via prctl
   Vulnerability Spectre v1:             Mitigation; usercopy/swapgs barriers and __user pointer sanitization
```

(Continued on next page)

Platform Notes (Continued)

```
  Vulnerability Spectre v2:             Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling;
                                        PBRSB-eIBRS Not affected; BHI BHI_DIS_S
  Vulnerability Srbds:                  Not affected
  Vulnerability Tsx async abort:        Not affected

From lscpu --cache:
     NAME ONE-SIZE ALL-SIZE WAYS TYPE        LEVEL  SETS PHY-LINE COHERENCY-SIZE
     L1d       48K     1.1M   12 Data            1    64        1             64
     L1i       64K     1.5M   16 Instruction     1    64        1             64
     L2         2M      48M   16 Unified         2  2048        1             64
     L3        48M      96M   16 Unified         3 49152        1             64

------------------------------------------------------------
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0-11,24-35
  node 0 size: 257611 MB
  node 0 free: 256818 MB
  node 1 cpus: 12-23,36-47
  node 1 size: 258029 MB
  node 1 free: 257311 MB
  node distances:
  node   0   1
    0:  10  21
    1:  21  10

------------------------------------------------------------
9. /proc/meminfo
   MemTotal:       528015852 kB

------------------------------------------------------------
10. who -r
  run-level 3 Sep 16 07:22

------------------------------------------------------------
11. Systemd service manager version: systemd 254 (254.24+suse.148.g83b9060b6e)
  Default Target  Status
  multi-user      running

------------------------------------------------------------
12. Services, from systemctl list-unit-files
  STATE            UNIT FILES
  enabled          YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron display-manager getty@ irqbalance
                   issue-generator kbdsettings klog lvm2-monitor nscd nvmefc-boot-connections
                   nvmf-autoconnect postfix purge-kernels rollback rsyslog sep5 smartd sshd systemd-pstore
                   wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
  enabled-runtime  systemd-remount-fs
  disabled         autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
                   chronyd console-getty cups cups-browsed debug-shell ebtables exchange-bmc-os-info
                   firewalld fsidd gpm grub2-once haveged ipmi ipmievd issue-add-ssh-keys kexec-load lunmask
                   man-db-create multipathd nfs nfs-blkmap rpcbind rpmconfigcheck rsyncd serial-getty@
                   smartd_generate_opts snmpd snmptrapd systemd-boot-check-no-failures systemd-confext
                   systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd
                   vncserver@
  indirect         systemd-userdbd wickedd

------------------------------------------------------------
13. Linux kernel boot-time arguments, from /proc/cmdline
  BOOT_IMAGE=/boot/vmlinuz-6.4.0-150700.51-default
```

(Continued on next page)

Platform Notes (Continued)

```
  root=UUID=90732971-989d-4632-82bc-39689e893398
  splash=silent
  mitigations=auto
  quiet
  security=apparmor

------------------------------------------------------------
14. cpupower frequency-info
  analyzing CPU 36:
    current policy: frequency should be within 800 MHz and 4.10 GHz.
                    The governor "powersave" may decide which speed to use
                    within this range.
    boost state support:
      Supported: yes
      Active: yes

------------------------------------------------------------
15. sysctl
  kernel.numa_balancing              1
  kernel.randomize_va_space          2
  vm.compaction_proactiveness       20
  vm.dirty_background_bytes          0
  vm.dirty_background_ratio         10
  vm.dirty_bytes                     0
  vm.dirty_expire_centisecs       3000
  vm.dirty_ratio                    20
  vm.dirty_writeback_centisecs      500
  vm.dirtytime_expire_seconds     43200
  vm.extfrag_threshold              500
  vm.min_unmapped_ratio               1
  vm.nr_hugepages                     0
  vm.nr_hugepages_mempolicy           0
  vm.nr_overcommit_hugepages          0
  vm.swappiness                      60
  vm.watermark_boost_factor       15000
  vm.watermark_scale_factor          10
  vm.zone_reclaim_mode                0

------------------------------------------------------------
16. /sys/kernel/mm/transparent_hugepage
  defrag          always defer defer+madvise [madvise] never
  enabled         [always] madvise never
  hpage_pmd_size  2097152
  shmem_enabled   always within_size advise [never] deny force

------------------------------------------------------------
17. /sys/kernel/mm/transparent_hugepage/khugepaged
  alloc_sleep_millisecs   60000
  defrag                      1
  max_ptes_none             511
  max_ptes_shared           256
  max_ptes_swap              64
  pages_to_scan            4096
  scan_sleep_millisecs    10000

------------------------------------------------------------
18. OS release
  From /etc/*-release /etc/*-version
  os-release SUSE Linux Enterprise Server 15 SP7
```

(Continued on next page)

Platform Notes (Continued)

```
------------------------------------------------------------
19. Disk information
SPEC is set to: /home/Benchmark/cpu2017
  Filesystem     Type  Size  Used Avail Use% Mounted on
  /dev/nvme0n1p3 xfs   911G   43G  869G   5% /home

------------------------------------------------------------
20. /sys/devices/virtual/dmi/id
    Vendor:         Fsas Technologies
    Product:        PRIMERGY RX2540 M8
    Product Family: SERVER
    Serial:         xxxxxxxxxx

------------------------------------------------------------
21. dmidecode
  Additional information from dmidecode 3.6 follows.  WARNING: Use caution when you interpret this section.
  The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
  determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
  "DMTF SMBIOS" standard.
  Memory:
    16x Samsung M321R4GA3EB2-CCPKF 32 GB 2 rank 6400


------------------------------------------------------------
22. BIOS
(This section combines info from /sys/devices and dmidecode.)
   BIOS Vendor:       Fsas Technologies
   BIOS Version:      V1.0.0.0 R1.1.0 for D4135-A1x
   BIOS Date:         09/05/2025
   BIOS Revision:     1.1
   Firmware Revision: 3.1
```

Compiler Version Notes

```
==============================================================================================================
C               | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++             | 508.namd_r(base) 510.parest_r(base)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++, C          | 511.povray_r(base) 526.blender_r(base)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++, C, Fortran | 507.cactuBSSN_r(base)
```

(Continued on next page)

Compiler Version Notes (Continued)

```
------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------------------------------------

============================================================================================================
Fortran         | 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
------------------------------------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------------------------------------

============================================================================================================
Fortran, C      | 521.wrf_r(base) 527.cam4_r(base)
------------------------------------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

Benchmarks using both Fortran and C:
ifx icx

Benchmarks using both C and C++:
icpx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifx

Base Portability Flags

503.bwaves_r: -DSPEC_LP64

(Continued on next page)

Base Portability Flags (Continued)

507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:
-w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both Fortran and C:
-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both C and C++:
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

(Continued on next page)

Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:
`-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs -align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html`
`http://www.spec.org/cpu2017/flags/Fsas-Platform-Settings-V1.0-GNR-RevA.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml`
`http://www.spec.org/cpu2017/flags/Fsas-Platform-Settings-V1.0-GNR-RevA.xml`

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2025-09-15 18:26:14-0400.
Report generated on 2025-10-15 12:52:12 by CPU2017 PDF formatter v6716.
Originally published on 2025-10-14.