



SPEC CPU®2017 Floating Point Rate Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL325 Gen12

(2.40 GHz, AMD EPYC 9535)

SPECrate®2017_fp_base = 693

SPECrate®2017_fp_peak = 747

CPU2017 License: 3

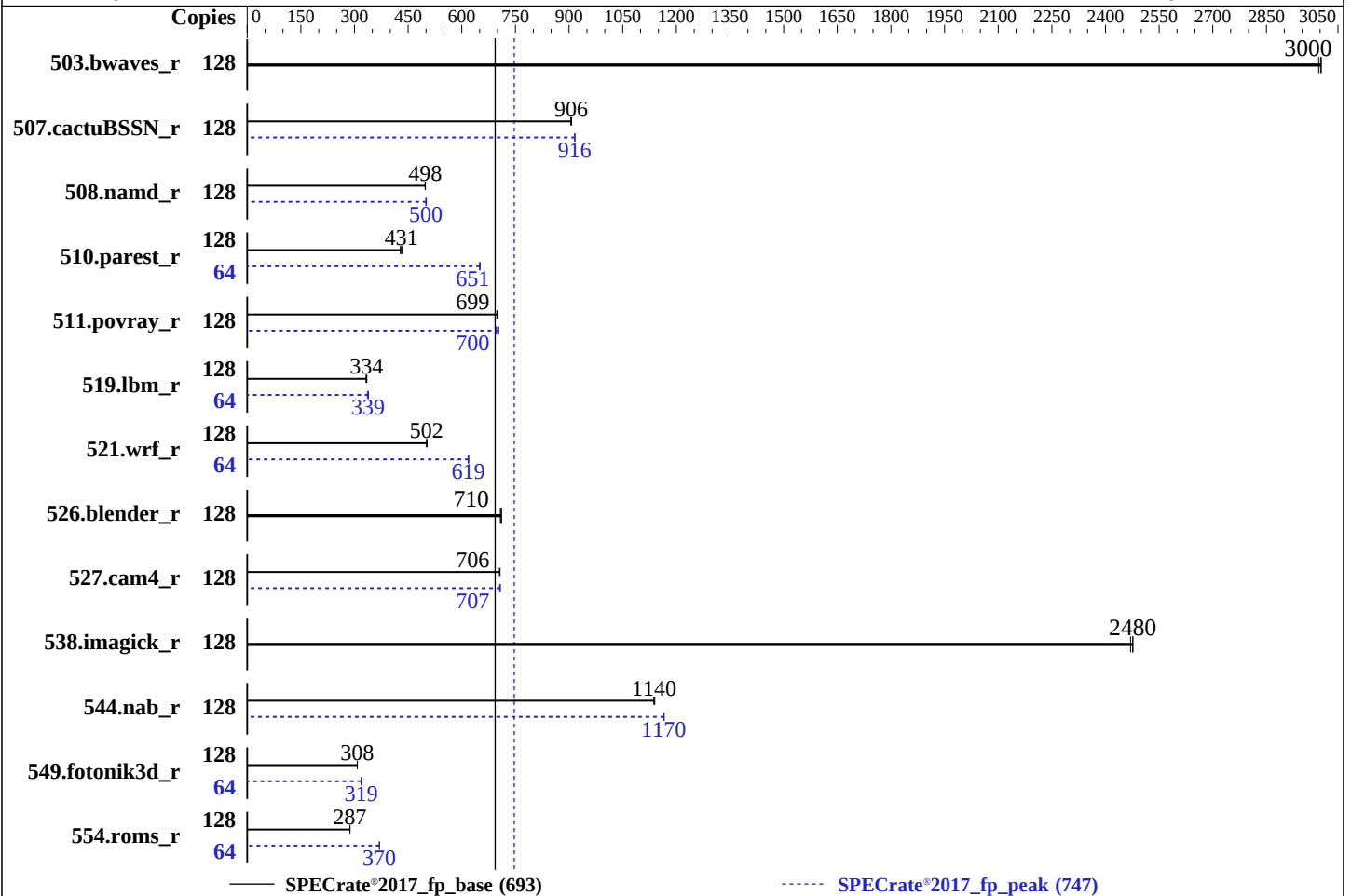
Test Sponsor: HPE

Tested by: HPE

Test Date: Sep-2025

Hardware Availability: Jul-2025

Software Availability: Oct-2024



Hardware

CPU Name: AMD EPYC 9535
Max MHz: 4300
Nominal: 2400
Enabled: 64 cores, 1 chip, 2 threads/core
Orderable: 1 chip
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 256 MB I+D on chip per chip, 16 MB shared / 4 cores
Other: None
Memory: 768 GB (12 x 64 GB 2Rx4 PC5-6400B-R, running at 5200)
Storage: 1 x 3 TB NVMe SSD
Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP6
kernel 6.4.0-150600.21-default
Compiler: C/C++/Fortran: Version 5.0.0 of AOCC
Parallel: No
Firmware: HPE BIOS Version v1.20 07/11/2025 released Jul-2025
File System: ext4
System State: Run level 5 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: None
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	128	428	3000	427	3000	428	3000	128	428	3000	427	3000	428	3000
507.cactuBSSN_r	128	179	907	179	906	179	905	128	177	917	177	916	177	916
508.namd_r	128	244	498	244	498	244	497	128	243	501	243	500	243	500
510.parest_r	128	777	431	774	433	782	428	64	257	651	257	651	258	650
511.povray_r	128	428	699	427	701	428	698	128	429	696	427	700	425	704
519.lbm_r	128	406	332	404	334	403	334	64	199	339	199	339	201	336
521.wrf_r	128	571	502	571	502	570	503	64	231	620	232	619	232	618
526.blender_r	128	274	711	275	710	275	708	128	274	711	275	710	275	708
527.cam4_r	128	317	707	317	706	319	702	128	317	706	317	707	316	708
538.imagick_r	128	129	2480	129	2480	129	2470	128	129	2480	129	2480	129	2470
544.nab_r	128	189	1140	189	1140	189	1140	128	185	1170	185	1170	185	1160
549.fotonik3d_r	128	1621	308	1619	308	1620	308	64	782	319	782	319	782	319
554.roms_r	128	709	287	708	287	708	287	64	275	370	275	369	275	370

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,

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Operating System Notes (Continued)

'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

LD_LIBRARY_PATH =

"/home/cpu2017/amd_rate_aocc500_znver5_A_lib/lib:/home/cpu2017/amd_rate_aocc500_znver5_A_lib/lib32:"

MALLOC_CONF = "retain:true"

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Configurations : Parameters are selected in the order shown below

Workload Profile set to High Performance Compute (HPC)

Determinism Control set to Manual

Performance Determinism set to Power Deterministic

Memory Patrol Scrubbing set to Disabled

ACPI CST C2 Latency set to 18 microseconds

Last-Level Cache (LLC) as NUMA Node set to Enabled

NUMA memory domains per socket set to Four memory domains per socket

Thermal Configuration set to Enhanced CPU Cooling

AMD Periodic Directory Rinse set to Periodic

Workload Profile set to Custom

Power Regulator set to OS Control Mode

Sysinfo program /home/cpu2017/bin/sysinfo

Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197

running on localhost Mon Sep 15 02:11:21 2025

SUT (System Under Test) info as seen by some common utilities.

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- 1. uname -a
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Platform Notes (Continued)

```
10. who -r
11. Systemd service manager version: systemd 254 (254.27+suse.167.g130293e510)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS
```

```
1. uname -a
Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09/1p)
x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
02:11:21 up 3 days, 15:48, 6 users, load average: 70.59, 110.97, 118.94
USER      TTY      FROM            LOGIN@   IDLE   JCPU   PCPU WHAT
root      pts/2    10.79.30.183:S.0 20:35    5:24m  1.01s  0.17s /bin/bash ./amd_rate_aocc500_znver5_A1.sh
root      pts/3    10.79.30.183    20:36    5:34m  0.06s  0.06s -bash
```

```
3. Username
From environment variable $USER: root
```

```
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size               (blocks, -f) unlimited
pending signals         (-i) 3094500
max locked memory       (kbytes, -l) 2097152
max memory size         (kbytes, -m) unlimited
open files              (-n) 1024
pipe size               (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes      (-u) 3094500
virtual memory          (kbytes, -v) unlimited
file locks              (-x) unlimited
```

```
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@pts/0
-bash
screen -S cpu
SCREEN -S cpu
/bin/bash
```

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Platform Notes (Continued)

```
python3 ./run_intrate.py
/bin/bash ./amd_rate_aocc500_znver5_A1.sh
runcpu --config amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 fprate
runcpu --configfile amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode rate --tune base:peak --size test:train:refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.043/temlogs/preenv.fprate.043.0.log --lognum 043.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

6. /proc/cpuinfo

```
model name      : AMD EPYC 9535 64-Core Processor
vendor_id       : AuthenticAMD
cpu family      : 26
model           : 2
stepping        : 1
microcode       : 0xb002151
bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size        : 192 4K pages
cpu cores       : 64
siblings        : 128
1 physical ids (chips)
128 processors (hardware threads)
physical id 0:  core ids 0-63
physical id 0:  apicids 0-127
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.39.3:

```
Architecture:      x86_64
CPU op-mode(s):    32-bit, 64-bit
Address sizes:      52 bits physical, 57 bits virtual
Byte Order:         Little Endian
CPU(s):             128
On-line CPU(s) list: 0-127
Vendor ID:          AuthenticAMD
Model name:         AMD EPYC 9535 64-Core Processor
CPU family:         26
Model:              2
Thread(s) per core: 2
Core(s) per socket: 64
Socket(s):          1
Stepping:           1
Frequency boost:    enabled
CPU(s) scaling MHz: 108%
CPU max MHz:        2400.0000
CPU min MHz:        1500.0000
BogoMIPS:           4799.85
Flags:               fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
extd_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
sse4_1 sse4_2 movbe popcnt aes xsave avx f16c rdrand lahf_lm
cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2
ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmi1 avx2
```

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Platform Notes (Continued)

smp bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap
avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local user_shstk avx_vnni avx512_bf16 clzero irperf
xsaveerptr rdpru wbnoinvd amd_ppin cppc arat npt lbrv svm_lock
nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter
pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl vnmi
avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq
avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect
movdiri movdir64b overflow_recov succor smca fsrm avx512_vp2intersect
flush_l1d debug_swap

AMD-V

Virtualization:

L1d cache:

L1i cache:

L2 cache:

L3 cache:

NUMA node(s):

NUMA node0 CPU(s):

NUMA node1 CPU(s):

NUMA node2 CPU(s):

NUMA node3 CPU(s):

NUMA node4 CPU(s):

NUMA node5 CPU(s):

NUMA node6 CPU(s):

NUMA node7 CPU(s):

NUMA node8 CPU(s):

NUMA node9 CPU(s):

NUMA node10 CPU(s):

NUMA node11 CPU(s):

NUMA node12 CPU(s):

NUMA node13 CPU(s):

NUMA node14 CPU(s):

NUMA node15 CPU(s):

Vulnerability Gather data sampling:

Vulnerability Itlb multihit:

Vulnerability L1tf:

Vulnerability Mds:

Vulnerability Meltdown:

Vulnerability Mmio stale data:

Vulnerability Reg file data sampling:

Vulnerability Retbleed:

Vulnerability Spec rstack overflow:

Vulnerability Spec store bypass:

Vulnerability Spectre v1:

Vulnerability Spectre v2:

Vulnerability Srbds:

Vulnerability Tsx async abort:

3 MiB (64 instances)

2 MiB (64 instances)

64 MiB (64 instances)

256 MiB (16 instances)

16

0-3,64-67

4-7,68-71

8-11,72-75

12-15,76-79

16-19,80-83

20-23,84-87

24-27,88-91

28-31,92-95

32-35,96-99

36-39,100-103

40-43,104-107

44-47,108-111

48-51,112-115

52-55,116-119

56-59,120-123

60-63,124-127

Not affected

Not affected

Not affected

Not affected

Not affected

Not affected

Not affected

Not affected

Not affected

Mitigation; Speculative Store Bypass disabled via prctl

Mitigation; usercopy/swapgs barriers and __user pointer sanitization

Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP

always-on; RSB filling; PBRBS-eIBRS Not affected; BHI Not affected

Not affected

Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	3M	12	Data	1	64	1	64
L1i	32K	2M	8	Instruction	1	64	1	64
L2	1M	64M	16	Unified	2	1024	1	64
L3	16M	256M	16	Unified	3	16384	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 16 nodes (0-15)

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Platform Notes (Continued)

node 0 cpus: 0-3,64-67
node 0 size: 48027 MB
node 0 free: 41153 MB
node 1 cpus: 4-7,68-71
node 1 size: 48381 MB
node 1 free: 47965 MB
node 2 cpus: 8-11,72-75
node 2 size: 48381 MB
node 2 free: 47946 MB
node 3 cpus: 12-15,76-79
node 3 size: 48381 MB
node 3 free: 47952 MB
node 4 cpus: 16-19,80-83
node 4 size: 48381 MB
node 4 free: 47908 MB
node 5 cpus: 20-23,84-87
node 5 size: 48381 MB
node 5 free: 47907 MB
node 6 cpus: 24-27,88-91
node 6 size: 48381 MB
node 6 free: 47853 MB
node 7 cpus: 28-31,92-95
node 7 size: 48381 MB
node 7 free: 47878 MB
node 8 cpus: 32-35,96-99
node 8 size: 48381 MB
node 8 free: 47950 MB
node 9 cpus: 36-39,100-103
node 9 size: 48381 MB
node 9 free: 47955 MB
node 10 cpus: 40-43,104-107
node 10 size: 48381 MB
node 10 free: 47946 MB
node 11 cpus: 44-47,108-111
node 11 size: 48381 MB
node 11 free: 47940 MB
node 12 cpus: 48-51,112-115
node 12 size: 48381 MB
node 12 free: 47899 MB
node 13 cpus: 52-55,116-119
node 13 size: 48381 MB
node 13 free: 47940 MB
node 14 cpus: 56-59,120-123
node 14 size: 48381 MB
node 14 free: 46252 MB
node 15 cpus: 60-63,124-127
node 15 size: 48283 MB
node 15 free: 47812 MB

node distances:

node	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
0:	10	11	11	11	12	12	12	12	12	12	12	12	12	12	12	12
1:	11	10	11	11	12	12	12	12	12	12	12	12	12	12	12	12
2:	11	11	10	11	12	12	12	12	12	12	12	12	12	12	12	12
3:	11	11	11	10	12	12	12	12	12	12	12	12	12	12	12	12
4:	12	12	12	12	10	11	11	11	12	12	12	12	12	12	12	12
5:	12	12	12	12	11	10	11	11	12	12	12	12	12	12	12	12
6:	12	12	12	12	11	11	10	11	12	12	12	12	12	12	12	12
7:	12	12	12	12	11	11	11	10	12	12	12	12	12	12	12	12
8:	12	12	12	12	12	12	12	10	11	11	11	12	12	12	12	12
9:	12	12	12	12	12	12	12	11	10	11	11	12	12	12	12	12

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Platform Notes (Continued)

10:	12	12	12	12	12	12	12	12	12	11	11	10	11	12	12	12	12
11:	12	12	12	12	12	12	12	12	12	11	11	11	10	12	12	12	12
12:	12	12	12	12	12	12	12	12	12	12	12	12	12	10	11	11	11
13:	12	12	12	12	12	12	12	12	12	12	12	12	12	11	10	11	11
14:	12	12	12	12	12	12	12	12	12	12	12	12	12	11	11	10	11
15:	12	12	12	12	12	12	12	12	12	12	12	12	12	11	11	11	10

9. /proc/meminfo

MemTotal: 792223520 kB

10. who -r

run-level 5 Sep 11 10:23

11. Systemd service manager version: systemd 254 (254.27+suse.167.g130293e510)

Default Target	Status
graphical	running

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
-------	------------

enabled	ModemManager YaST2-Firstboot YaST2-Second-Stage apparmor appstream-sync-cache auditd bluetooth cron display-manager getty@ irqbalance iscsi issue-generator kbdsettings klog lvm2-monitor nsd nvme-fc-boot-connections nvme-autoconnect oracle postfix purge-kernels rollback rsyslog sapconf smartd sshd systemctl-logger systemd-pstore virtqemu virtxend wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny wpa_supplicant xencommons
enabled-runtime	systemd-fsck-root systemd-remount-fs
disabled	NetworkManager NetworkManager-dispatcher NetworkManager-wait-online accounts-daemon amavis apache2 apache2@ autofs autostart-initscripts bgpd blk-availability bluetooth-mesh boot-sysctl booth-arbitrator booth@ ca-certificates certmonger chrony-wait chronyd clamav-milter clamd clamonnacc console-getty corosync corosync-notifyd crm_mon ctdb cups cups-browsed ddclient debug-shell dhcpd dhcpd6 dhcrelay dhcrelay6 dirsrv@ dlm dnsmasq drbd drbd-lvchange@ drbd-wait-promotable@ ebttables exchange-bmc-os-info fetchmail firewalld fsid gpm grub2-once haveged hawk hwloc-dump-hwdata ipmi ipmievd ipvsadm iscsi-init iscsid isid issue-add-ssh-keys kexec-load ksm kvm_stat ldirectord libvirt-guests logd lunmask lvmlockd lvmlocks man-db-create mariadb mariadb@ multipathd named nfs nfs-blkmap nfs-server nfs-server nmb openvpn@ ospf6d ospfd ostree-remount pacemaker racoon racoon-setkey radvd rarpd@ ripd ripngd rpcbind rpmconfigcheck rsyncd rtkit-daemon sanlock sbd sbd_remote serial-getty@ smartd-generate_opts smb snmpd snmptrapd spamd spamd speech-dispatcher squid srp_daemon srp_daemon_port@ strongswan strongswan-starter sysstat systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-nspawn@ systemd-sysextd systemd-time-wait-sync systemd-timesyncd tcsd udisks2 update-system-flatpaks upower virtinterfaced virtlockd virtlogd virtnetworkd virtnodeudev virtnwfilterd virtsecret virtstoraged vncserver@ vsftpd wdm winbind wpa_supplicant@ xen-dom0-modules xen-init-dom0 xen-qemu-dom0-disk-backend xen-watchdog xenconsole xendomains xenstored zebra
indirect	pcscd saned@ systemd-userdbd tftpd uidd wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline

BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
root=UUID=31eb4e3c-f432-4a30-b618-dd595ca988cb
splash=silent
mitigations=auto
quiet
security=apparmor

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Test Date: Sep-2025

Hardware Availability: Jul-2025

Software Availability: Oct-2024

Platform Notes (Continued)

14. cpupower frequency-info

analyzing CPU 17:

current policy: frequency should be within 1.50 GHz and 2.40 GHz.

The governor "performance" may decide which speed to use within this range.

boost state support:

Supported: yes

Active: yes

15. sysctl

```
kernel.numa_balancing          0
kernel.randomize_va_space      0
vm.compaction_proactiveness    20
vm.dirty_background_bytes      314572800
vm.dirty_background_ratio      0
vm.dirty_bytes                 0
vm.dirty_expire_centisecs      3000
vm.dirty_ratio                 8
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                   1
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           1
```

16. /sys/kernel/mm/transparent_hugepage

```
defrag      [always] defer defer+madvise madvise never
enabled     [always] madvise never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force
```

17. /sys/kernel/mm/transparent_hugepage/khugepaged

```
alloc_sleep_millisecs 60000
defrag                1
max_ptes_none         511
max_ptes_shared       256
max_ptes_swap         64
pages_to_scan         4096
scan_sleep_millisecs  10000
```

18. OS release

From /etc/*-release /etc/*-version

os-release SUSE Linux Enterprise Server 15 SP6

19. Disk information

SPEC is set to: /home/cpu2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/nvme5n1p3	ext4	2.9T	20G	2.7T	1%	/

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

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ProLiant Compute DL325 Gen12

(2.40 GHz, AMD EPYC 9535)

SPECrate®2017_fp_base = 693

SPECrate®2017_fp_peak = 747

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Sep-2025

Hardware Availability: Jul-2025

Software Availability: Oct-2024

Platform Notes (Continued)

20. /sys/devices/virtual/dmi/id

Vendor: HPE

Product: HPE ProLiant Compute DL325 Gen12

Product Family: ProLiant

Serial: SANJACSCM

21. dmidecode

Additional information from dmidecode 3.6 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

6x Micron MTC40F2046S1RC64BD2 MWFF 64 GB 2 rank 6400, configured at 5200

6x Micron MTC40F2046S1RC64BD2 QSFF 64 GB 2 rank 6400, configured at 5200

22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: HPE

BIOS Version: 1.20

BIOS Date: 07/11/2025

BIOS Revision: 1.20

Firmware Revision: 1.16

Compiler Version Notes

=====
C | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
=====

=====
C++ | 508.namd_r(base, peak) 510.parest_r(base, peak)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
=====

=====
C++, C | 511.povray_r(base, peak) 526.blender_r(base, peak)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
=====

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Software Availability: Oct-2024

Compiler Version Notes (Continued)

=====
C++, C, Fortran | 507.cactuBSSN_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====
Fortran | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====
Fortran, C | 521.wrf_r(base, peak) 527.cam4_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

(Continued on next page)



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Base Compiler Invocation (Continued)

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
526.blender_r: -funsigned-char -DSPEC_LP64
527.cam4_r: -DSPEC_CASE_FLAG -DSPEC_LP64
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3
-march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl

C++ benchmarks:

-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl

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Base Optimization Flags (Continued)

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdalloc
-lflang -ldl
```

Benchmarks using both Fortran and C:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang -ldl
```

Benchmarks using both C and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -lamdlibm -lamdalloc -lflang
-ldl
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -Mrecursive -funroll-loops
```

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Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):

-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang -ldl

Base Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

(Continued on next page)



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Software Availability: Oct-2024

Peak Compiler Invocation (Continued)

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
519.lbm_r: -m64 -Wl, -mllvm -Wl, -align-all-nofallthru-blocks=6
-Wl, -mllvm -Wl, -reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl
```

538.imagick_r: basepeak = yes

```
544.nab_r: -m64 -flto -Wl, -mllvm -Wl, -ldist-scalar-expand
-fenable-aggressive-gather -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl
```

C++ benchmarks:

```
508.namd_r: -m64 -std=c++14
-Wl, -mllvm -Wl, -align-all-nofallthru-blocks=6
-Wl, -mllvm -Wl, -reduce-array-computations=3
-Wl, -mllvm -Wl, -x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl
```

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Peak Optimization Flags (Continued)

```
510.parest_r: -m64 -std=c++14 -flto -Wl,-mllvm -Wl,-suppress-fmas
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl
```

Fortran benchmarks:

```
503.bwaves_r: basepeak = yes
```

```
549.fotonik3d_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-Mrecursive -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -fvector-transform
-fscalar-transform -lamdlibm -lamdalloc -ldl -lflang
```

```
554.roms_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-Mrecursive -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm
-lamdalloc -ldl -lflang
```

Benchmarks using both Fortran and C:

```
521.wrf_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc
-ldl -lflang
```

```
527.cam4_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays
-mllvm -reduce-array-computations=3 -zopt -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
```

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Software Availability: Oct-2024

Peak Optimization Flags (Continued)

527.cam4_r (continued):

-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc
-ldl -lflang

Benchmarks using both C and C++:

511.povray_r: -m64 -std=c++14

-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false
-Wl,-mllvm -Wl,-extra-inliner -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -mllvm -reduce-array-computations=3 -zopt
-mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -lamdlibm
-lamdalloc -ldl

526.blender_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3 -zopt
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000
-faggressive-loop-transform -fvector-transform -fscalar-transform
-Mrecursive -fepilog-vectorization-of-inductions -lamdlibm -lamdalloc
-ldl -lflang

Peak Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

(Continued on next page)



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Peak Other Flags (Continued)

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-Turin-rev1.7.html>

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-Turin-rev1.7.xml>

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

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