



SPEC CPU®2017 Floating Point Rate Result

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Fsas Technologies Inc.

(Test Sponsor: Fujitsu)

SPECrate®2017_fp_base = 611

SPECrate®2017_fp_peak = Not Run

PRIMERGY RX4770 M8, Intel Xeon 6714P, 4.00GHz

CPU2017 License: 19

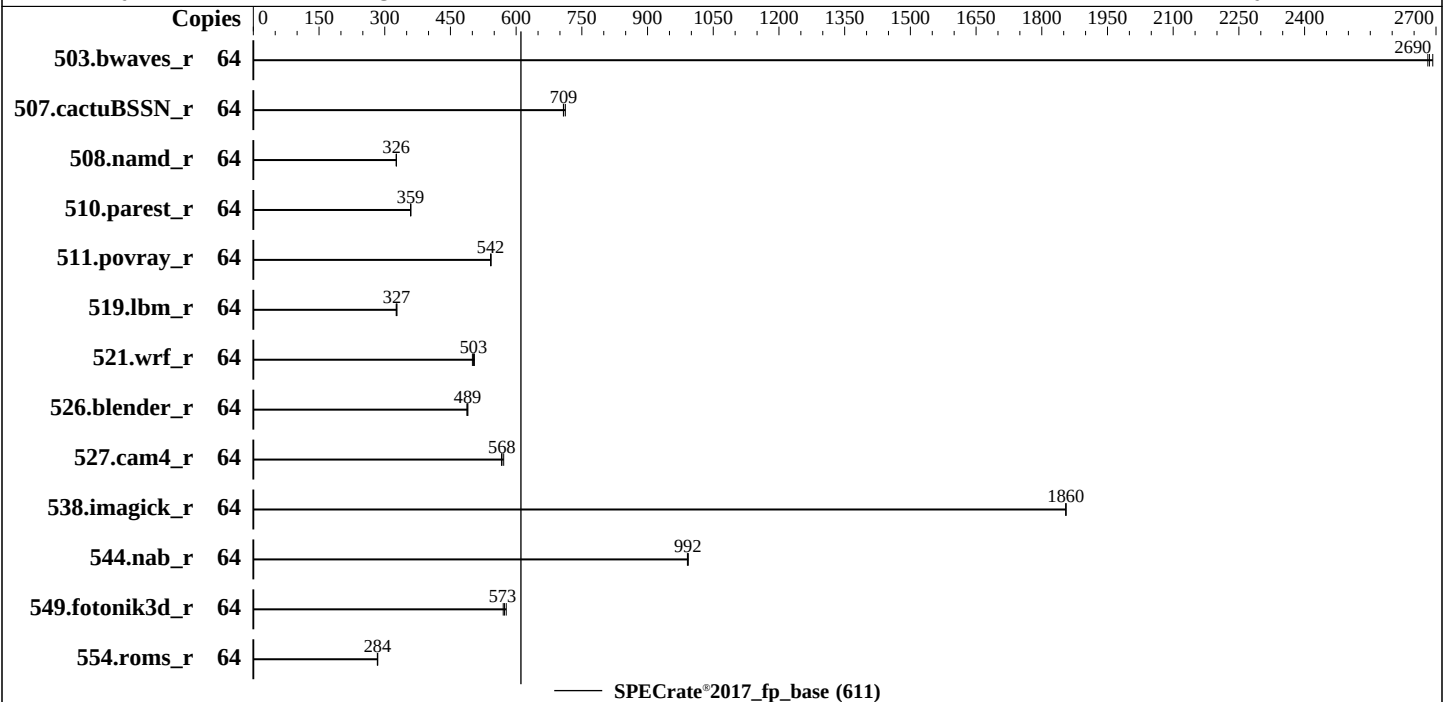
Test Sponsor: Fujitsu

Tested by: Fsas Technologies Inc.

Test Date: Dec-2025

Hardware Availability: Dec-2025

Software Availability: Jun-2025



Hardware

CPU Name: Intel Xeon 6714P
Max MHz: 4300
Nominal: 4000
Enabled: 32 cores, 4 chips, 2 threads/core
Orderable: 2,4 chips
Cache L1: 64 KB I + 48 KB D on chip per core
L2: 2 MB I+D on chip per core
L3: 48 MB I+D on chip per chip
Other: None
Memory: 2 TB (32 x 64 GB 2Rx4 PC5-6400B-R)
Storage: 380 GB on tmpfs
Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP7
6.4.0-150700.51-default
Compiler: C/C++: Version 2024.1 of Intel oneAPI DPC++/C++
Compiler for Linux;
Fortran: Version 2024.1 of Intel Fortran
Compiler for Linux;
Parallel: No
Firmware: Fsas Technologies Inc. BIOS Version V1.0.0.0
R1.1.0 for D4136-A1x. Released Dec-2025
tested as V1.0.0.0 R1.0.0 for D4136-A1x. Oct-2025
File System: tmpfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS set to prefer performance at the cost
of additional power usage



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	64	238	2690	239	2680	<u>239</u>	<u>2690</u>							
507.cactuBSSN_r	64	114	709	114	712	114	708							
508.namd_r	64	186	327	186	326	<u>186</u>	<u>326</u>							
510.parest_r	64	465	360	467	359	<u>466</u>	<u>359</u>							
511.povray_r	64	276	542	275	543	<u>276</u>	<u>542</u>							
519.lbm_r	64	207	326	<u>206</u>	<u>327</u>	206	328							
521.wrf_r	64	287	500	<u>285</u>	<u>503</u>	284	505							
526.blender_r	64	200	488	<u>199</u>	<u>489</u>	199	490							
527.cam4_r	64	196	572	<u>197</u>	<u>568</u>	198	567							
538.imagick_r	64	85.8	1860	<u>85.8</u>	<u>1860</u>	85.8	1850							
544.nab_r	64	<u>109</u>	<u>992</u>	109	991	108	993							
549.fotonik3d_r	64	<u>435</u>	<u>573</u>	437	570	432	577							
554.roms_r	64	358	284	359	283	<u>359</u>	<u>284</u>							

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/mnt/ramdisk/speccpu-24.1/lib/intel64:/mnt/ramdisk/speccpu-24.1/je5.0.1-64"
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.

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Software Availability: Jun-2025

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 380 GB ramdisk created with the command of "mount -t tmpfs -o size=380G tmpfs /mnt/ramdisk" jemalloc, a general purpose malloc implementation built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5 sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

BIOS configuration:
Fan Control = Full
SNC (Sub NUMA) = Enabled
CPU Performance Boost = Aggressive
Homeless Prefetch = Enabled
Loctorem Thresholds Normal = Low
Loctorem Thresholds Empty = Low
LLC Dead Line Alloc = Disabled
APS rocketing = Enabled
ASPM Support = Auto

Sysinfo program /mnt/ramdisk/speccpu-24.1/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Thu Dec 11 09:44:59 2025

SUT (System Under Test) info as seen by some common utilities.

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3. Username
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11. Systemd service manager version: systemd 254 (254.24+suse.148.g83b9060b6e)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. sysctl
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1. uname -a

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Platform Notes (Continued)

Linux localhost 6.4.0-150700.51-default #1 SMP PREEMPT_DYNAMIC Wed Apr 30 21:35:43 UTC 2025 (6930611)
x86_64 x86_64 x86_64 GNU/Linux

2. w
09:44:59 up 5:35, 1 user, load average: 6.02, 39.02, 53.58
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
root tty1 - 04:10 5:33m 2.87s 1.59s -bash

3. Username
From environment variable \$USER: root

4. ulimit -a
core file size (blocks, -c) unlimited
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 8255538
max locked memory (kbytes, -l) 8192
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 8255538
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited

5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
login -- root
-bash
-bash
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=64 -c
ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=32 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base -o all fprate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=64 --configfile
ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=32 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base --output_format all --nopower --runmode
rate --tune base --size refrate fprate --nopreenv --note-preenv --logfile
\$SPEC/tmp/CPU2017.001/temlogs/preenv.fprate.001.0.log --lognum 001.0 --from_runcpu 2
specperl \$SPEC/bin/sysinfo
\$SPEC = /mnt/ramdisk/speccpu-24.1

6. /proc/cpuinfo
model name : Intel(R) Xeon(R) 6714P
vendor_id : GenuineIntel
cpu family : 6
model : 173
stepping : 1
microcode : 0x10003f3
bugs : spectre_v1 spectre_v2 spec_store_bypass swapgs bhi
cpu cores : 8
siblings : 16

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Platform Notes (Continued)

4 physical ids (chips)
64 processors (hardware threads)
physical id 0: core ids 0-7
physical id 1: core ids 0-7
physical id 2: core ids 0-7
physical id 3: core ids 0-7
physical id 0: apicids 0-15
physical id 1: apicids 128-143
physical id 2: apicids 256-271
physical id 3: apicids 384-399

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.40.4:

Architecture:	x86_64
CPU op-mode(s):	32-bit, 64-bit
Address sizes:	52 bits physical, 57 bits virtual
Byte Order:	Little Endian
CPU(s):	64
On-line CPU(s) list:	0-63
Vendor ID:	GenuineIntel
Model name:	Intel(R) Xeon(R) 6714P
CPU family:	6
Model:	173
Thread(s) per core:	2
Core(s) per socket:	8
Socket(s):	4
Stepping:	1
CPU(s) scaling MHz:	22%
CPU max MHz:	4300.0000
CPU min MHz:	800.0000
BogoMIPS:	8000.00
Flags:	fpu_vme_de_pse_tsc_msr_pae_mce_cx8_apic_sep_mtrr_pge_mca_cmov_pat pse36_clflush_dts_acpi_mmx_fxsr_sse_sse2_ss_ht_tm_pbe_syscall_nx pdpe1gb_rdtscp_lm_constant_tsc_art_arch_perfmon_pebs_bts_rep_good nopl_xtopology_nonstop_tsc_cpuid_aperfperf_tsc_known_freq_pni pclmuldq_dtes64_monitor_ds_cpl_vmx_smx_est_tm2_ssse3_sdbg_fma_cx16 xtpr_pdc_m_pcid_dca_sse4_1_sse4_2_x2apic_movbe_popcnt tsc_deadline_timer_aes_xsave_avx_f16c_rdrand_lahf_lm_abm 3dnowprefetch_cpuid_fault_epb_cat_l3_cat_l2_cdp_l3_intel_ppin_cdp_l2 ssbd_mba_ibrs_ibpb_stibp_ibrs_enhanced_tpr_shadow_flexpriority_ept vpid_ept_ad_fsgsbase_tsc_adjust_bmi1_avx2_smep_bmi2_erms_invpcid_cqm rdt_a_avx512f_avx512dq_rdseed_adx_smap_avx512ifma_clflushopt_clwb intel_pt_avx512cd_sha_ni_avx512bw_avx512vl_xsaveopt_xsavec_xgetbv1 xsaves_cqm_llc_cqm_occup_llc_cqm_mbm_total_cqm_mbm_local split_lock_detect_user_shstk_avx_vnni_avx512_bf16_wbnoinvd_dtherm_ida arat_pln_pts_hwp_act_window_hwp_epp_hwp_pkg_req_vnni_avx512vbmi umip_pku_ospke_waitpkg_avx512_vbmi2_gfni_vaes_vpclmuldq_avx512_vnni avx512_bitalg_tme_avx512_vpopcntdq_la57_rdpid_bus_lock_detect cldemote_movdiri_movdir64b_enqcmd_fsrmd_md_clear_serialize_tsxldtrk pconfig_arch_lbr_ibt_amx_bf16_avx512_fp16_amx_tile_amx_int8_flush_l1d arch_capabilities
Virtualization:	VT-x
L1d cache:	1.5 MiB (32 instances)
L1i cache:	2 MiB (32 instances)
L2 cache:	64 MiB (32 instances)
L3 cache:	192 MiB (4 instances)

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Platform Notes (Continued)

NUMA node(s): 4
NUMA node0 CPU(s): 0-7,32-39
NUMA node1 CPU(s): 8-15,40-47
NUMA node2 CPU(s): 16-23,48-55
NUMA node3 CPU(s): 24-31,56-63
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling; PBRSE-IBRS Not affected; BHI BHI_DIS_S
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	1.5M	12	Data	1	64	1	64
L1i	64K	2M	16	Instruction	1	64	1	64
L2	2M	64M	16	Unified	2	2048	1	64
L3	48M	192M	16	Unified	3	49152	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 4 nodes (0-3)
node 0 cpus: 0-7,32-39
node 0 size: 515697 MB
node 0 free: 514704 MB
node 1 cpus: 8-15,40-47
node 1 size: 516091 MB
node 1 free: 514676 MB
node 2 cpus: 16-23,48-55
node 2 size: 516091 MB
node 2 free: 506998 MB
node 3 cpus: 24-31,56-63
node 3 size: 516031 MB
node 3 free: 515127 MB
node distances:
node 0 1 2 3
0: 10 21 21 21
1: 21 10 21 21
2: 21 21 10 21
3: 21 21 21 10

9. /proc/meminfo

MemTotal: 2113445304 kB

10. who -r

run-level 3 Dec 11 04:09

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Platform Notes (Continued)

11. Systemd service manager version: systemd 254 (254.24+suse.148.g83b9060b6e)

Default Target	Status
multi-user	degraded

12. Failed units, from systemctl list-units --state=failed

UNIT	LOAD	ACTIVE	SUB	DESCRIPTION
------	------	--------	-----	-------------

* sep5.service loaded failed failed systemd script to load sep5 driver at boot time

13. Services, from systemctl list-unit-files

STATE	UNIT FILES
-------	------------

enabled	YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron display-manager getty@ irqbalance issue-generator kbdsettings klog lvm2-monitor nscd nvme-fc-boot-connections nvmmf-autoconnect postfix purge-kernels rollback rsyslog sep5 smartd sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
---------	--

enabled-runtime	systemd-remount-fs
-----------------	--------------------

disabled	autofs autostart-initscripts blk-availability boot-sysctl ca-certificates chrony-wait chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info firewalld fsidd gpm grub2-once haveged ipmi ipmievd issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap rpcbind rpmconfigcheck rsyncd serial-getty@ smartd-generate_opts snmpd snmptrapd systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd vncserver@
----------	---

indirect	systemd-userdbd wickedd
----------	-------------------------

14. Linux kernel boot-time arguments, from /proc/cmdline

BOOT_IMAGE=/boot/vmlinuz-6.4.0-150700.51-default
root=UUID=6490486a-f020-45c4-b83e-03e98fc56a3b
splash=silent
mitigations=auto
quiet
security=apparmor

15. cpupower frequency-info

analyzing CPU 1:

current policy: frequency should be within 800 MHz and 4.30 GHz.
The governor "powersave" may decide which speed to use
within this range.

boost state support:

Supported: yes

Active: yes

16. sysctl

kernel.numa_balancing	1
kernel.randomize_va_space	2
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	20
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1
vm.nr_hugepages	0

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Platform Notes (Continued)

```
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                   60
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           0
```

```
-----
17. /sys/kernel/mm/transparent_hugepage
defrag      always defer defer+madvice [madvice] never
enabled     [always] madvice never
hpage_pmd_size  2097152
shmem_enabled always within_size advise [never] deny force
-----
```

```
-----
18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                 1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs   10000
-----
```

```
-----
19. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP7
-----
```

```
-----
20. Disk information
SPEC is set to: /mnt/ramdisk/speccpu-24.1
Filesystem      Type      Size  Used Avail Use% Mounted on
tmpfs           tmpfs     380G  7.9G  373G   3% /mnt/ramdisk
-----
```

```
-----
21. /sys/devices/virtual/dmi/id
Vendor:      Fsas Technologies
Product:     PRIMERGY RX4770 M8
Product Family: SERVER
Serial:      xxxxxxxxxx
-----
```

```
-----
22. dmidecode
Additional information from dmidecode 3.6 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
32x Micron MTC40F2046S1RC64BD2 MWFF 64 GB 2 rank 6400
-----
```

```
-----
23. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor:      Fsas Technologies
BIOS Version:     V1.0.0.0 R1.0.0 for D4136-A1x
BIOS Date:        10/28/2025
BIOS Revision:    1.0
Firmware Revision: 3.6
-----
```




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Compiler Version Notes

=====
C | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
=====

=====
C++ | 508.namd_r(base) 510.parest_r(base)
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
=====

=====
C++, C | 511.povray_r(base) 526.blender_r(base)
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
=====

=====
C++, C, Fortran | 507.cactuBSSN_r(base)
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
=====

=====
Fortran | 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
=====

=====
Fortran, C | 521.wrf_r(base) 527.cam4_r(base)
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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Base Compiler Invocation

C benchmarks:
icx

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SPEC CPU®2017 Floating Point Rate Result

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Fsas Technologies Inc.

(Test Sponsor: Fujitsu)

SPECrate®2017_fp_base = 611

PRIMERGY RX4770 M8, Intel Xeon 6714P, 4.00GHz

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 19

Test Sponsor: Fujitsu

Tested by: Fsas Technologies Inc.

Test Date: Dec-2025

Hardware Availability: Dec-2025

Software Availability: Jun-2025

Base Compiler Invocation (Continued)

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Base Portability Flags

503.bwaves_r: -DSPEC_LP64

507.cactuBSSN_r: -DSPEC_LP64

508.namd_r: -DSPEC_LP64

510.parest_r: -DSPEC_LP64

511.povray_r: -DSPEC_LP64

519.lbm_r: -DSPEC_LP64

521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian

526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char

527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG

538.imagick_r: -DSPEC_LP64

544.nab_r: -DSPEC_LP64

549.fotonik3d_r: -DSPEC_LP64

554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapfirerapids -Ofast -ffast-math

-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4

-Wno-implicit-int -mprefer-vector-width=512 -ljemalloc

-L/usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xsapfirerapids -Ofast

-ffast-math -flto -mfpmath=sse -funroll-loops

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Base Optimization Flags (Continued)

C++ benchmarks (continued):

-qopt-mem-layout-trans=4 -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both Fortran and C:

-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both C and C++:

-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using Fortran, C, and C++:

-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Fsas-Platform-Settings-V1.0-GNR-RevB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Fsas-Platform-Settings-V1.0-GNR-RevB.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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