



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

## NEC Corporation

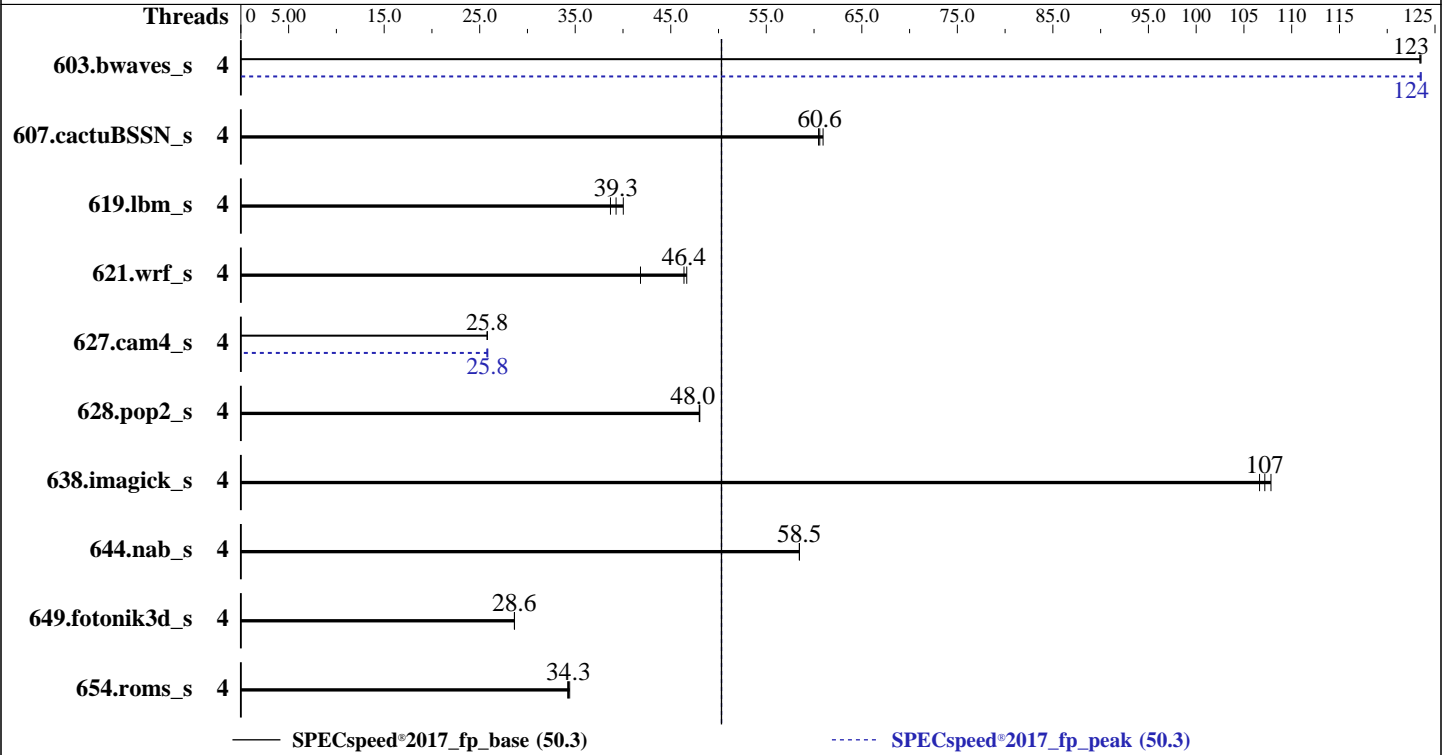
SPECspeed®2017\_fp\_base = 50.3

### Express5800/T110m-S (Intel Xeon 6325P)

SPECspeed®2017\_fp\_peak = 50.3

CPU2017 License: 9006  
Test Sponsor: NEC Corporation  
Tested by: NEC Corporation

Test Date: Jan-2026  
Hardware Availability: Jun-2025  
Software Availability: Apr-2024



### Hardware

CPU Name: Intel Xeon 6325P  
Max MHz: 5200  
Nominal: 3500  
Enabled: 4 cores, 1 chip, 2 threads/core  
Orderable: 1 chips  
Cache L1: 32 KB I + 48 KB D on chip per core  
L2: 2 MB I+D on chip per core  
L3: 12 MB I+D on chip per chip  
Other: None  
Memory: 64 GB (2 x 32 GB 2Rx8 PC5-4800B-E, running at 4400)  
Storage: 1 x 400 GB SAS SSD  
Other: CPU Cooling: Air

### Software

OS: Red Hat Enterprise Linux 9.4 (Plow)  
5.14.0-427.13.1.el9\_4.x86\_64  
Compiler: C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;  
Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;  
Parallel: Yes  
Firmware: NEC BIOS Version F52 v2.62 03/04/2025 released Mar-2025  
File System: ext4  
System State: Run level 5 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 64-bit  
Other: jemalloc memory allocator V5.0.1  
Power Management: BIOS set to prefer performance at the cost of additional power usage



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

## NEC Corporation

SPECSpeed®2017\_fp\_base = 50.3

### Express5800/T110m-S (Intel Xeon 6325P)

SPECSpeed®2017\_fp\_peak = 50.3

**CPU2017 License:** 9006  
**Test Sponsor:** NEC Corporation  
**Tested by:** NEC Corporation

**Test Date:** Jan-2026  
**Hardware Availability:** Jun-2025  
**Software Availability:** Apr-2024

## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	4	<b>478</b>	<b>123</b>	478	124	478	123	4	<b>478</b>	<b>124</b>	478	124	478	123
607.cactuBSSN_s	4	276	60.5	<b>275</b>	<b>60.6</b>	274	60.9	4	276	60.5	<b>275</b>	<b>60.6</b>	274	60.9
619.lbm_s	4	131	40.0	<b>133</b>	<b>39.3</b>	135	38.7	4	131	40.0	<b>133</b>	<b>39.3</b>	135	38.7
621.wrf_s	4	<b>285</b>	<b>46.4</b>	316	41.8	283	46.7	4	<b>285</b>	<b>46.4</b>	316	41.8	283	46.7
627.cam4_s	4	344	25.8	<b>344</b>	<b>25.8</b>	344	25.8	4	344	25.7	343	25.9	<b>343</b>	<b>25.8</b>
628.pop2_s	4	<b>247</b>	<b>48.0</b>	247	48.0	247	48.0	4	<b>247</b>	<b>48.0</b>	247	48.0	247	48.0
638.imagick_s	4	134	108	135	107	<b>135</b>	<b>107</b>	4	134	108	135	107	<b>135</b>	<b>107</b>
644.nab_s	4	299	58.5	299	58.5	<b>299</b>	<b>58.5</b>	4	299	58.5	299	58.5	<b>299</b>	<b>58.5</b>
649.fotonik3d_s	4	318	28.6	<b>318</b>	<b>28.6</b>	318	28.6	4	318	28.6	<b>318</b>	<b>28.6</b>	318	28.6
654.roms_s	4	457	34.4	460	34.2	<b>459</b>	<b>34.3</b>	4	457	34.4	460	34.2	<b>459</b>	<b>34.3</b>

SPECSpeed®2017\_fp\_base = **50.3**

SPECSpeed®2017\_fp\_peak = **50.3**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
```

## Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
MALLOCONF = "retain:true"
OMP_STACKSIZE = "192M"
```

## General Notes

```
Binaries compiled on a system with 2x Intel Xeon 6369P CPU + 64GB RAM
memory using Redhat Enterprise Linux 9.4
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

## NEC Corporation

SPECspeed®2017\_fp\_base = 50.3

### Express5800/T110m-S (Intel Xeon 6325P)

SPECspeed®2017\_fp\_peak = 50.3

**CPU2017 License:** 9006  
**Test Sponsor:** NEC Corporation  
**Tested by:** NEC Corporation

**Test Date:** Jan-2026  
**Hardware Availability:** Jun-2025  
**Software Availability:** Apr-2024

## Platform Notes

### BIOS Settings:

Intel(R) SpeedStep(tm): Disabled  
Energy Efficient Turbo: Disabled

Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on T110m-S2.com Wed Jan 14 23:16:56 2026

SUT (System Under Test) info as seen by some common utilities.

### Table of contents

- 1. uname -a
  - 2. w
  - 3. Username
  - 4. ulimit -a
  - 5. sysinfo process ancestry
  - 6. /proc/cpuinfo
  - 7. lscpu
  - 8. numactl --hardware
  - 9. /proc/meminfo
  - 10. who -r
  - 11. Systemd service manager version: systemd 252 (252-32.el9\_4)
  - 12. Failed units, from systemctl list-units --state=failed
  - 13. Services, from systemctl list-unit-files
  - 14. Linux kernel boot-time arguments, from /proc/cmdline
  - 15. cpupower frequency-info
  - 16. tuned-adm active
  - 17. sysctl
  - 18. /sys/kernel/mm/transparent\_hugepage
  - 19. /sys/kernel/mm/transparent\_hugepage/khugepaged
  - 20. OS release
  - 21. Disk information
  - 22. /sys/devices/virtual/dmi/id
  - 23. dmidecode
  - 24. BIOS
- 
- 1. uname -a  
Linux T110m-S2.com 5.14.0-427.13.1.el9\_4.x86\_64 #1 SMP PREEMPT\_DYNAMIC Wed Apr 10 10:29:16 EDT 2024 x86\_64 x86\_64 x86\_64 GNU/Linux
- 
- 2. w  
23:16:56 up 6 days, 20:23, 2 users, load average: 0.07, 0.02, 0.00

USER	TTY	LOGIN@	IDLE	JCPU	PCPU	WHAT
root	seat0	08Jan26	0.00s	0.00s	0.00s	/usr/libexec/gdm-wayland-session --register-session
gnome-session						
root	tty2	08Jan26	6days	0.01s	0.01s	/usr/libexec/gnome-session-binary
- 
- 3. Username  
From environment variable \$USER: root
- 
- 4. ulimit -a  
real-time non-blocking time (microseconds, -R) unlimited  
core file size (blocks, -c) 0

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

## NEC Corporation

SPECspeed®2017\_fp\_base = 50.3

### Express5800/T110m-S (Intel Xeon 6325P)

SPECspeed®2017\_fp\_peak = 50.3

**CPU2017 License:** 9006  
**Test Sponsor:** NEC Corporation  
**Tested by:** NEC Corporation

**Test Date:** Jan-2026  
**Hardware Availability:** Jun-2025  
**Software Availability:** Apr-2024

## Platform Notes (Continued)

```

data seg size          (kbytes, -d) unlimited
scheduling priority    (-e) 0
file size              (blocks, -f) unlimited
pending signals        (-i) 256447
max locked memory     (kbytes, -l) 8192
max memory size       (kbytes, -m) unlimited
open files             (-n) 1024
pipe size              (512 bytes, -p) 8
POSIX message queues  (bytes, -q) 819200
real-time priority    (-r) 0
stack size             (kbytes, -s) unlimited
cpu time               (seconds, -t) unlimited
max user processes    (-u) 256447
virtual memory         (kbytes, -v) unlimited
file locks             (-x) unlimited

```

```

-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd rhgb --switched-root --system --deserialize 31
/usr/lib/systemd/systemd --user
/usr/libexec/gnome-terminal-server
bash
bash
runcpu --nobuild --action validate --flagsurl=Intel-ic2024-official-linux64.xml,Default-Platform-Flags.xml
-c ic2024.1-lin-core-avx2-speed-20240308n.cfg --define cores=4 --tune base,peak -o all --define smt-on
--define drop_caches --nopower --runmode speed --tune base:peak --size refspeed fpspeed
runcpu --nobuild --action validate --flagsurl Intel-ic2024-official-linux64.xml,Default-Platform-Flags.xml
--configfile ic2024.1-lin-core-avx2-speed-20240308n.cfg --define cores=4 --tune base,peak --output_format
all --define smt-on --define drop_caches --nopower --runmode speed --tune base:peak --size refspeed
--nopower --runmode speed --tune base:peak --size refspeed fpspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.046/templogs/preenv.fpspeed.046.0.log --lognum 046.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017

```

```

-----
6. /proc/cpuinfo
model name      : Intel(R) Xeon(R) 6325P
vendor_id      : GenuineIntel
cpu family     : 6
model          : 183
stepping       : 1
microcode      : 0x12e
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
cpu cores      : 4
siblings       : 8
1 physical ids (chips)
8 processors (hardware threads)
physical id 0: core ids 0-3
physical id 0: apicids 0-7
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.

```

```

-----
7. lscpu
From lscpu from util-linux 2.37.4:
Architecture:      x86_64
CPU op-mode(s):    32-bit, 64-bit
Address sizes:      42 bits physical, 48 bits virtual
Byte Order:         Little Endian

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

## NEC Corporation

SPECspeed®2017\_fp\_base = 50.3

### Express5800/T110m-S (Intel Xeon 6325P)

SPECspeed®2017\_fp\_peak = 50.3

**CPU2017 License:** 9006  
**Test Sponsor:** NEC Corporation  
**Tested by:** NEC Corporation

**Test Date:** Jan-2026  
**Hardware Availability:** Jun-2025  
**Software Availability:** Apr-2024

## Platform Notes (Continued)

```

CPU(s): 8
On-line CPU(s) list: 0-7
Vendor ID: GenuineIntel
BIOS Vendor ID: Intel(R) Corporation
Model name: Intel(R) Xeon(R) 6325P
BIOS Model name: Intel(R) Xeon(R) 6325P
CPU family: 6
Model: 183
Thread(s) per core: 2
Core(s) per socket: 4
Socket(s): 1
Stepping: 1
CPU(s) scaling MHz: 59%
CPU max MHz: 5200.0000
CPU min MHz: 800.0000
BogoMIPS: 6988.80
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb
rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl
xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq
dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm
sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c
rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb ssbd ibrs ibpb stibp
ibrs_enhanced tpr_shadow flexpriority ept vpid ept_ad fsgsbase
tsc_adjust bmi1 avx2 smep bmi2 erms invpcid rdseed adx smap clflushopt
clwb intel_pt sha_ni xsaveopt xsavec xgetbv1 xsaves split_lock_detect
avx_vnni dtherm ida arat pln pts hwp hwp_notify hwp_act_window hwp_epp
hwp_pkg_req hfi vnmi umip pku ospke waitpkg gfni vaes vpclmulqdq tme
rdpid movdiri movdir64b fsrm md_clear serialize pconfig arch_lbr ibt
flush_lld arch_capabilities

Virtualization: VT-x
L1d cache: 192 KiB (4 instances)
L1i cache: 128 KiB (4 instances)
L2 cache: 8 MiB (4 instances)
L3 cache: 12 MiB (1 instance)
NUMA node(s): 1
NUMA node0 CPU(s): 0-7
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS, IBPB conditional, RSB filling,
PBRSE-eIBRS SW sequence
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

```

```

From lscpu --cache:
NAME ONE-SIZE ALL-SIZE WAYS TYPE LEVEL SETS PHY-LINE COHERENCY-SIZE
L1d 48K 192K 12 Data 1 64 1 64
L1i 32K 128K 8 Instruction 1 64 1 64
L2 2M 8M 16 Unified 2 2048 1 64
L3 12M 12M 6 Unified 3 32768 1 64

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

## NEC Corporation

SPECspeed®2017\_fp\_base = 50.3

### Express5800/T110m-S (Intel Xeon 6325P)

SPECspeed®2017\_fp\_peak = 50.3

**CPU2017 License:** 9006  
**Test Sponsor:** NEC Corporation  
**Tested by:** NEC Corporation

**Test Date:** Jan-2026  
**Hardware Availability:** Jun-2025  
**Software Availability:** Apr-2024

## Platform Notes (Continued)

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 1 nodes (0)
node 0 cpus: 0-7
node 0 size: 64170 MB
node 0 free: 62576 MB
node distances:
node 0
0: 10
```

9. /proc/meminfo

```
MemTotal: 65710776 kB
```

10. who -r

```
run-level 5 Jan 8 02:54
```

11. Systemd service manager version: systemd 252 (252-32.el9\_4)

```
Default Target Status
graphical degraded
```

12. Failed units, from systemctl list-units --state=failed

UNIT	LOAD	ACTIVE	SUB	DESCRIPTION
* NetworkManager-wait-online.service	loaded	failed	failed	Network Manager Wait Online
* sep5.service	loaded	failed	failed	systemd script to load sep5 driver at boot time

13. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	ModemManager NetworkManager NetworkManager-dispatcher NetworkManager-wait-online accounts-daemon atd auditd avahi-daemon bluetooth chronyd crond cups dbus-broker firewalld gdm getty@ insights-client-boot irqbalance iscsi-onboot iscsi-starter kdump libstoragemgmt low-memory-monitor lvm2-monitor mcelog mdmonitor microcode multipathd nis-domainname nvme-fc-boot-connections ostree-remount power-profiles-daemon qemu-guest-agent rhsmcertd rsyslog rtkit-daemon selinux-autorelabel-mark sep5 smartd sshd sssd switcheroo-control systemd-boot-update systemd-network-generator tuned udisks2 upower vgauthd vmtoolsd
enabled-runtime	systemd-fsck-root systemd-remount-fs
disabled	arp-ethers blk-availability brltty canberra-system-bootup canberra-system-shutdown canberra-system-shutdown-reboot chrony-wait chronyd-restricted cni-dhcp console-getty cpupower cups-browsed dbus-daemon debug-shell dnf-system-upgrade dnsmasq iprdump iprinit iprupdate iscsi-init iscsid iscsiui kpatch kvm_stat ledmon man-db-restart-cache-update netavark-dhcp-proxy netavark-firewalld-reload nftables nvme-autoconnect ostree-readonly-sysroot-migration ostree-state-overlay@ pesign podman podman-auto-update podman-clean-transient podman-kube@ podman-restart psacct ras-mc-ctl rasdaemon rdisc rhcd rhsm rhsm-facts rpmdb-rebuild selinux-check-proper-disable serial-getty@ speech-dispatcherd sshd-keygen@ systemd-boot-check-no-failures systemd-pstore systemd-sysextr wpa_supplicant
indirect	iscsi spice-vdagentd sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo systemd-sysupdate systemd-sysupdate-reboot

14. Linux kernel boot-time arguments, from /proc/cmdline

```
BOOT_IMAGE=(hd0,gpt2)/vmlinuz-5.14.0-427.13.1.el9_4.x86_64
root=UUID=cf7049f7-fc44-4900-ad7c-53a67c4484e1
ro
resume=UUID=912b545c-ab45-4cdd-8e03-31ef75b941cd
rhgb
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

## NEC Corporation

SPECspeed®2017\_fp\_base = 50.3

### Express5800/T110m-S (Intel Xeon 6325P)

SPECspeed®2017\_fp\_peak = 50.3

**CPU2017 License:** 9006  
**Test Sponsor:** NEC Corporation  
**Tested by:** NEC Corporation

**Test Date:** Jan-2026  
**Hardware Availability:** Jun-2025  
**Software Availability:** Apr-2024

## Platform Notes (Continued)

quiet

```

-----
15. cpupower frequency-info
    analyzing CPU 3:
        current policy: frequency should be within 800 MHz and 5.20 GHz.
                        The governor "powersave" may decide which speed to use
                        within this range.
    boost state support:
        Supported: yes
        Active: yes
-----

```

```

-----
16. tuned-adm active
    It seems that tuned daemon is not running, preset profile is not activated.
    Preset profile: throughput-performance
-----

```

```

-----
17. sysctl
    kernel.numa_balancing          0
    kernel.randomize_va_space      2
    vm.compaction_proactiveness     20
    vm.dirty_background_bytes       0
    vm.dirty_background_ratio       10
    vm.dirty_bytes                  0
    vm.dirty_expire_centisecs       3000
    vm.dirty_ratio                  20
    vm.dirty_writeback_centisecs    500
    vm.dirtytime_expire_seconds     43200
    vm.extfrag_threshold            500
    vm.min_unmapped_ratio           1
    vm.nr_hugepages                 0
    vm.nr_hugepages_mempolicy        0
    vm.nr_overcommit_hugepages      0
    vm.swappiness                   60
    vm.watermark_boost_factor       15000
    vm.watermark_scale_factor        10
    vm.zone_reclaim_mode            0
-----

```

```

-----
18. /sys/kernel/mm/transparent_hugepage
    defrag          always defer defer+madvise [madvise] never
    enabled         [always] madvise never
    hpage_pmd_size  2097152
    shmem_enabled   always within_size advise [never] deny force
-----

```

```

-----
19. /sys/kernel/mm/transparent_hugepage/khugepaged
    alloc_sleep_millisecs  60000
    defrag                  1
    max_ptes_none          511
    max_ptes_shared        256
    max_ptes_swap          64
    pages_to_scan          4096
    scan_sleep_millisecs   10000
-----

```

```

-----
20. OS release
    From /etc/*-release /etc/*-version
    os-release   Red Hat Enterprise Linux 9.4 (Plow)
-----

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

## NEC Corporation

SPECspeed®2017\_fp\_base = 50.3

### Express5800/T110m-S (Intel Xeon 6325P)

SPECspeed®2017\_fp\_peak = 50.3

**CPU2017 License:** 9006  
**Test Sponsor:** NEC Corporation  
**Tested by:** NEC Corporation

**Test Date:** Jan-2026  
**Hardware Availability:** Jun-2025  
**Software Availability:** Apr-2024

## Platform Notes (Continued)

redhat-release Red Hat Enterprise Linux release 9.4 (Plow)  
system-release Red Hat Enterprise Linux release 9.4 (Plow)

-----  
21. Disk information  
SPEC is set to: /home/cpu2017  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/sda3 ext4 320G 70G 234G 24% /  
-----

22. /sys/devices/virtual/dmi/id  
Vendor: NEC  
Product: Express5800/T110m-S [N8100-3025Y]  
Serial: 0123456  
-----

23. dmidecode  
Additional information from dmidecode 3.5 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.  
Memory:  
2x Samsung M324R4GA3BB0-CQKOD 32 GB 2 rank 4800, configured at 4400  
-----

24. BIOS  
(This section combines info from /sys/devices and dmidecode.)  
BIOS Vendor: American Megatrends International, LLC.  
BIOS Version: F52  
BIOS Date: 03/04/2025  
BIOS Revision: 15.52  
-----

## Compiler Version Notes

=====  
C | 619.lbm\_s(base, peak) 638.imagick\_s(base, peak) 644.nab\_s(base, peak)  
-----

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.  
-----

=====  
C++, C, Fortran | 607.cactuBSSN\_s(base, peak)  
-----

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.  
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.  
-----

=====  
Fortran | 603.bwaves\_s(base, peak) 649.fotonik3d\_s(base, peak) 654.roms\_s(base, peak)  
-----

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.  
-----

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

**NEC Corporation**

SPECspeed®2017\_fp\_base = 50.3

Express5800/T110m-S (Intel Xeon 6325P)

SPECspeed®2017\_fp\_peak = 50.3

**CPU2017 License:** 9006  
**Test Sponsor:** NEC Corporation  
**Tested by:** NEC Corporation

**Test Date:** Jan-2026  
**Hardware Availability:** Jun-2025  
**Software Availability:** Apr-2024

## Compiler Version Notes (Continued)

```

=====
Fortran, C      | 621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)
=====
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
=====

```

## Base Compiler Invocation

C benchmarks:  
icx

Fortran benchmarks:  
ifx

Benchmarks using both Fortran and C:  
ifx icx

Benchmarks using Fortran, C, and C++:  
icpx icx ifx

## Base Portability Flags

```

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

```

## Base Optimization Flags

C benchmarks:  
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

**NEC Corporation**

SPECspeed®2017\_fp\_base = 50.3

Express5800/T110m-S (Intel Xeon 6325P)

SPECspeed®2017\_fp\_peak = 50.3

CPU2017 License: 9006

Test Sponsor: NEC Corporation

Tested by: NEC Corporation

Test Date: Jan-2026

Hardware Availability: Jun-2025

Software Availability: Apr-2024

## Base Optimization Flags (Continued)

C benchmarks (continued):

```
-DSPEC_OPENMP -Wno-implicit-int -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -Wno-implicit-int -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using Fortran, C, and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP -Wno-implicit-int
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

## Peak Compiler Invocation

C benchmarks:

```
icx
```

Fortran benchmarks:

```
ifx
```

Benchmarks using both Fortran and C:

```
ifx icx
```

Benchmarks using Fortran, C, and C++:

```
icpx icx ifx
```

## Peak Portability Flags

Same as Base Portability Flags



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

## NEC Corporation

SPECspeed®2017\_fp\_base = 50.3

### Express5800/T110m-S (Intel Xeon 6325P)

SPECspeed®2017\_fp\_peak = 50.3

**CPU2017 License:** 9006  
**Test Sponsor:** NEC Corporation  
**Tested by:** NEC Corporation

**Test Date:** Jan-2026  
**Hardware Availability:** Jun-2025  
**Software Availability:** Apr-2024

## Peak Optimization Flags

C benchmarks:

619.lbm\_s: basepeak = yes

638.imagick\_s: basepeak = yes

644.nab\_s: basepeak = yes

Fortran benchmarks:

```
603.bwaves_s: -w -m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc
```

649.fotonik3d\_s: basepeak = yes

654.roms\_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf\_s: basepeak = yes

```
627.cam4_s: -w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP
-Wno-implicit-int -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc
```

628.pop2\_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN\_s: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

<http://www.spec.org/cpu2017/flags/NEC-Platform-Settings-V1.0-T110m-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/NEC-Platform-Settings-V1.0-T110m-RevA.xml>



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

## NEC Corporation

SPECspeed®2017\_fp\_base = 50.3

Express5800/T110m-S (Intel Xeon 6325P)

SPECspeed®2017\_fp\_peak = 50.3

**CPU2017 License:** 9006

**Test Sponsor:** NEC Corporation

**Tested by:** NEC Corporation

**Test Date:** Jan-2026

**Hardware Availability:** Jun-2025

**Software Availability:** Apr-2024

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.9 on 2026-01-14 09:16:55-0500.  
Report generated on 2026-05-19 17:27:22 by CPU2017 PDF formatter v6716.  
Originally published on 2026-05-19.