



SPEC CPU®2017 Integer Rate Result

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Supermicro

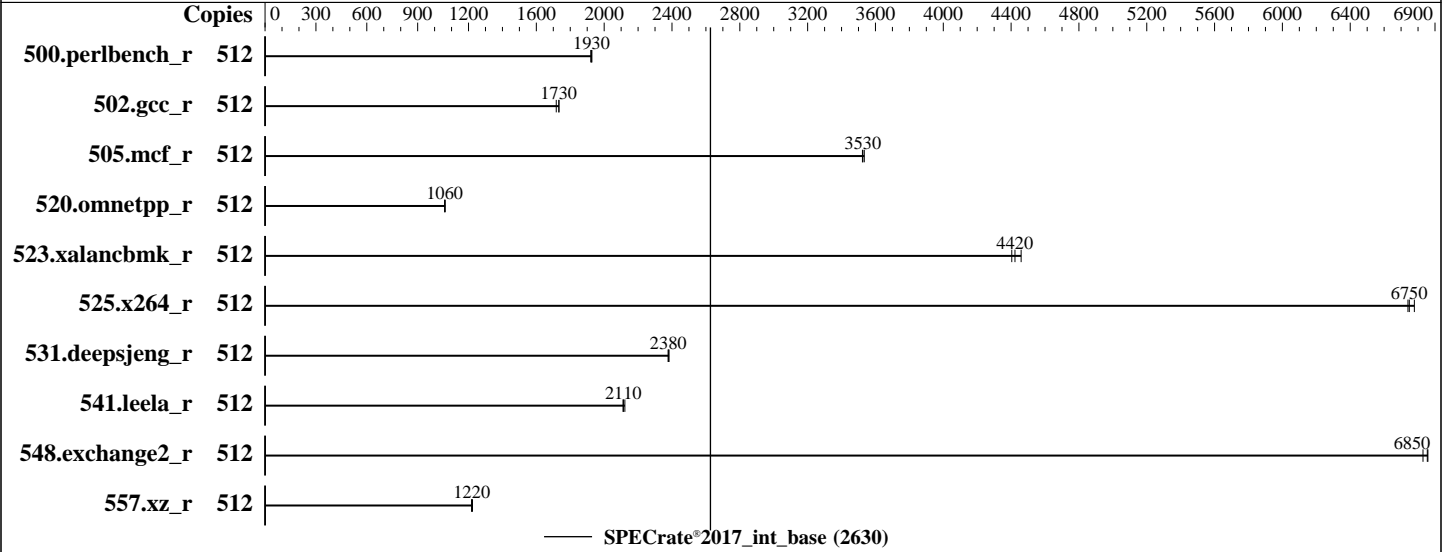
Hyper A+ Server AS -2126HS-TN
(H14DSH , AMD EPYC 9755)

SPECrate®2017_int_base = 2630

SPECrate®2017_int_peak = Not Run

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Mar-2026
Hardware Availability: Oct-2024
Software Availability: Dec-2024



Hardware

CPU Name: AMD EPYC 9755
Max MHz: 4100
Nominal: 2700
Enabled: 256 cores, 2 chips, 2 threads/core
Orderable: 2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 512 MB I+D on chip per chip, 32 MB shared / 8 cores
Other: None
Memory: 1536 GB (24 x 64 GB 2Rx4 PC5-6400B-R)
Storage: 1 x 1.8 TB NVMe SSD
Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP5
5.14.21-150500.55.88-default
Compiler: C/C++/Fortran: Version 5.0.0 of AOCC
Parallel: No
Firmware: Version 1.7a released Nov-2025
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other: None
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	512	424	1920	<u>423</u>	<u>1930</u>	423	1930							
502.gcc_r	512	422	1720	<u>418</u>	<u>1730</u>	418	1730							
505.mcf_r	512	234	3530	235	3520	<u>235</u>	<u>3530</u>							
520.omnetpp_r	512	634	1060	632	1060	<u>634</u>	<u>1060</u>							
523.xalancbmk_r	512	121	4460	<u>122</u>	<u>4420</u>	123	4400							
525.x264_r	512	132	6780	133	6740	<u>133</u>	<u>6750</u>							
531.deepsjeng_r	512	<u>247</u>	<u>2380</u>	246	2380	247	2380							
541.leela_r	512	400	2120	402	2110	<u>401</u>	<u>2110</u>							
548.exchange2_r	512	196	6860	196	6830	<u>196</u>	<u>6850</u>							
557.xz_r	512	<u>453</u>	<u>1220</u>	454	1220	452	1220							

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.



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Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =  
    "/home/cpu2017-1.1.9/amd_rate_aocc500_znver5_A_lib/lib:/home/cpu2017-1.1.9/amd_rate_aocc500_znver5_A_1  
    ib/lib32:"  
MALLOC_CONF = "retain:true"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS settings:

```
NUMA Nodes Per Socket = NPS4  
Determinism Control = Manual  
Determinism Enable = Power  
TDP control = Manual  
TDP = 500  
Package Power Limit Control = Manual  
Package Power Limit = 500  
TSME = Disabled  
Memory Target Speed = DDR6400
```

```
Sysinfo program /home/cpu2017-1.1.9/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Wed Mar 18 09:33:14 2026
```

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.17+suse.228.gcba4725678)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged

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Platform Notes (Continued)

- 18. OS release
- 19. Disk information
- 20. /sys/devices/virtual/dmi/id
- 21. dmidecode
- 22. BIOS

```
1. uname -a
Linux localhost 5.14.21-150500.55.88-default #1 SMP PREEMPT_DYNAMIC Fri Dec 6 16:43:20 UTC 2024 (e102a89)
x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
09:33:14 up 1 day, 23:37, 1 user, load average: 0.14, 0.03, 0.01
USER      TTY      FROM          LOGIN@      IDLE        JCPU        PCPU        WHAT
root      tty1     -              Mon09       26.00s     1.26s     0.25s /bin/bash ./amd_rate_aocc500_znver5_A1.sh
```

```
3. Username
From environment variable $USER: root
```

```
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size                (blocks, -f) unlimited
pending signals         (-i) 6187948
max locked memory       (kbytes, -l) 2097152
max memory size         (kbytes, -m) unlimited
open files              (-n) 1024
pipe size                (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes      (-u) 6187948
virtual memory          (kbytes, -v) unlimited
file locks              (-x) unlimited
```

```
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 30
login -- root
-bash
python3 ./run_amd_rate_aocc500_znver5_A1.py
/bin/bash ./amd_rate_aocc500_znver5_A1.sh
runcpu --config amd_rate_aocc500_znver5_A1.cfg --tune base --reportable --iterations 3 intrate
runcpu --configfile amd_rate_aocc500_znver5_A1.cfg --tune base --reportable --iterations 3 --nopower
--runmode rate --tune base --size test:train:refrate intrate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9
```

```
6. /proc/cpuinfo
model name      : AMD EPYC 9755 128-Core Processor
vendor_id      : AuthenticAMD
cpu family     : 26
```

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Platform Notes (Continued)

```
model          : 2
stepping       : 1
microcode      : 0xb00215a
bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size       : 192 4K pages
cpu cores      : 128
siblings       : 256
2 physical ids (chips)
512 processors (hardware threads)
physical id 0: core ids 0-127
physical id 1: core ids 0-127
physical id 0: apicids 0-255
physical id 1: apicids 256-511
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.37.4:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         52 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                512
On-line CPU(s) list:   0-511
Vendor ID:             AuthenticAMD
Model name:            AMD EPYC 9755 128-Core Processor
CPU family:            26
Model:                 2
Thread(s) per core:    2
Core(s) per socket:    128
Socket(s):              2
Stepping:              1
Frequency boost:       enabled
CPU max MHz:           2700.0000
CPU min MHz:           1500.0000
BogoMIPS:              5392.11
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
extd_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm
cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpeext
perfctr_llc mwaitx cpb cat_l3 cdp_l3 invpcid_single hw_pstate ssbd
mba perfmon_v2 ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase
tsc_adjust bmi1 avx2 smep bmi2 erms invpcid cqm rdt_a avx512f
avx512dq rdseed adx smap avx512ifma clflushopt clwb avx512cd sha_ni
avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc
cqm_occup_llc cqm_mbm_total cqm_mbm_local avx_vnni avx512_bf16 clzero
irperf xsaveerptr rdpru wbnoinvd amd_ppin cppc amd_ibpb_ret arat npt
lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid
decodeassists pausefilter pfthreshold avic v_vmsave_vmload vgif
x2avic v_spec_ctrl avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes
vpclmulqdq avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid
bus_lock_detect movdiri movdir64b overflow_recov succor smca fsrm
avx512_vp2intersect flush_lld

Virtualization:        AMD-V
Lld cache:             12 MiB (256 instances)
```

(Continued on next page)



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Platform Notes (Continued)

```

L1i cache:                8 MiB (256 instances)
L2 cache:                 256 MiB (256 instances)
L3 cache:                 1 GiB (32 instances)
NUMA node(s):             8
NUMA node0 CPU(s):       0-31,256-287
NUMA node1 CPU(s):       32-63,288-319
NUMA node2 CPU(s):       64-95,320-351
NUMA node3 CPU(s):       96-127,352-383
NUMA node4 CPU(s):       128-159,384-415
NUMA node5 CPU(s):       160-191,416-447
NUMA node6 CPU(s):       192-223,448-479
NUMA node7 CPU(s):       224-255,480-511
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit:       Not affected
Vulnerability Lltf:                 Not affected
Vulnerability Mds:                   Not affected
Vulnerability Meltdown:             Not affected
Vulnerability Mmio stale data:      Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed:             Not affected
Vulnerability Spec rstack overflow:  Not affected
Vulnerability Spec store bypass:    Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1:           Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:           Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP
always-on; RSB filling; PBR SB-eIBRS Not affected, BHI Not affected

Vulnerability Srbds:               Not affected
Vulnerability Tsx async abort:      Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	12M	12	Data	1	64	1	64
L1i	32K	8M	8	Instruction	1	64	1	64
L2	1M	256M	16	Unified	2	1024	1	64
L3	32M	1G	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 8 nodes (0-7)
node 0 cpus: 0-31,256-287
node 0 size: 192955 MB
node 0 free: 191683 MB
node 1 cpus: 32-63,288-319
node 1 size: 193519 MB
node 1 free: 190342 MB
node 2 cpus: 64-95,320-351
node 2 size: 193519 MB
node 2 free: 192094 MB
node 3 cpus: 96-127,352-383
node 3 size: 193519 MB
node 3 free: 192201 MB
node 4 cpus: 128-159,384-415
node 4 size: 193473 MB
node 4 free: 192268 MB
node 5 cpus: 160-191,416-447
node 5 size: 193519 MB
node 5 free: 192276 MB
node 6 cpus: 192-223,448-479
node 6 size: 193519 MB
node 6 free: 192263 MB

```

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Platform Notes (Continued)

```

node 7 cpus: 224-255,480-511
node 7 size: 192985 MB
node 7 free: 191760 MB
node distances:
node  0  1  2  3  4  5  6  7
0:  10  12  12  12  32  32  32  32
1:  12  10  12  12  32  32  32  32
2:  12  12  10  12  32  32  32  32
3:  12  12  12  10  32  32  32  32
4:  32  32  32  32  10  12  12  12
5:  32  32  32  32  12  10  12  12
6:  32  32  32  32  12  12  10  12
7:  32  32  32  32  12  12  12  10

```

```

-----
9. /proc/meminfo
   MemTotal:      1584143088 kB

```

```

-----
10. who -r
    run-level 3 Mar 16 09:55

```

```

-----
11. Systemd service manager version: systemd 249 (249.17+suse.228.gcba4725678)
    Default Target   Status
    multi-user       running

```

```

-----
12. Services, from systemctl list-unit-files
    STATE          UNIT FILES
    enabled        YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron display-manager getty@ irqbalance
                   issue-generator kbdsettings kdump kdump-early klog lvm2-monitor nscd
                   nvme-fc-boot-connections postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore
                   wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
    enabled-runtime systemd-remount-fs
    disabled       autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
                   chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info
                   firewallld gpm grub2-once haveged haveged-switch-root hwloc-dump-hwdata ipmi ipmievd
                   issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap
                   nvme-fc-autoconnect rpcbind rpmconfigcheck rsyncd serial-getty@ smartd-generate_opts snmpd
                   snmptrapd systemd-boot-check-no-failures systemd-network-generator systemd-sysext
                   systemd-time-wait-sync systemd-timesyncd udisks2 vncserver@
    indirect       wickedd

```

```

-----
13. Linux kernel boot-time arguments, from /proc/cmdline
    BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.55.88-default
    root=UUID=e5c64239-798d-46c5-9c98-515a018f8ffc
    splash=silent
    resume=/dev/disk/by-uuid/6e51c0c2-1efc-4d1f-ba74-ed8beb0879e3
    mitigations=auto
    quiet
    security=apparmor
    crashkernel=308M,high
    crashkernel=72M,low

```

```

-----
14. cpupower frequency-info
    analyzing CPU 0:
    current policy: frequency should be within 1.50 GHz and 2.70 GHz.

```

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Platform Notes (Continued)

The governor "performance" may decide which speed to use within this range.

boost state support:
Supported: yes
Active: yes

```
-----
15. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space      0
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio      10
vm.dirty_bytes                  0
vm.dirty_expire_centisecs      3000
vm.dirty_ratio                  8
vm.dirty_writeback_centisecs    500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                 0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                   1
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           1
-----
```

```
-----
16. /sys/kernel/mm/transparent_hugepage
defrag          [always] defer defer+madvise madvise never
enabled        [always] madvise never
hpage_pmd_size 2097152
shmem_enabled  always within_size advise [never] deny force
-----
```

```
-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                  1
max_ptes_none           511
max_ptes_shared         256
max_ptes_swap           64
pages_to_scan           4096
scan_sleep_millisecs   10000
-----
```

```
-----
18. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP5
-----
```

```
-----
19. Disk information
SPEC is set to: /home/cpu2017-1.1.9
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/nvme0n1p3 xfs   1.3T  4.4G  1.3T   1% /home
-----
```

```
-----
20. /sys/devices/virtual/dmi/id
Vendor:          Supermicro
Product:         Super Server
-----
```

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Platform Notes (Continued)

Product Family: SMC H14
Serial: 0123456789

21. dmidecode

Additional information from dmidecode 3.6 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
24x Samsung M321R8GA0EB2-CCPPC 64 GB 2 rank 6400

22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: American Megatrends International, LLC.
BIOS Version: 1.7a
BIOS Date: 11/18/2025
BIOS Revision: 5.35

Compiler Version Notes

C | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base) 557.xz_r(base)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

C++ | 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base) 541.leela_r(base)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

Fortran | 548.exchange2_r(base)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

Base Compiler Invocation

C benchmarks:
clang

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Test Date: Mar-2026
Hardware Availability: Oct-2024
Software Availability: Dec-2024

Base Compiler Invocation (Continued)

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Base Portability Flags

```
500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

Base Optimization Flags

C benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather
-Wl,-mllvm -Wl,-extra-inliner -z muldefs -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lflang
-lamdalloc-ext -ldl
```

C++ benchmarks:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=advanced -z muldefs -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -fno-PIE -no-pie
-fvirtual-function-elimination -fvisibility=hidden
-mllvm -do-block-reorder=advanced -lamdlibm -lflang -lamdalloc-ext
-ldl
```

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

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Supermicro

Hyper A+ Server AS -2126HS-TN
(H14DSH , AMD EPYC 9755)

SPECrate®2017_int_base = 2630

SPECrate®2017_int_peak = Not Run

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Mar-2026
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Base Optimization Flags (Continued)

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -z muldefs -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fltto
-fepilog-vectorization-of-inductions -mllvm -optimize-strided-mem-cost
-floop-transform -mllvm -unroll-aggressive -mllvm -unroll-threshold=500
-lamplib -lflang -lamdalloc -ldl
```

Base Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-Turin-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-Turin-revH.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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