



SPEC CPU®2017 Floating Point Rate Result

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Supermicro

WIO A+ Server AS -1115SV-WTNRT
(H13SVW-NT, AMD EPYC 8325P)

SPECrate®2017_fp_base = 468

SPECrate®2017_fp_peak = 468

CPU2017 License: 001176

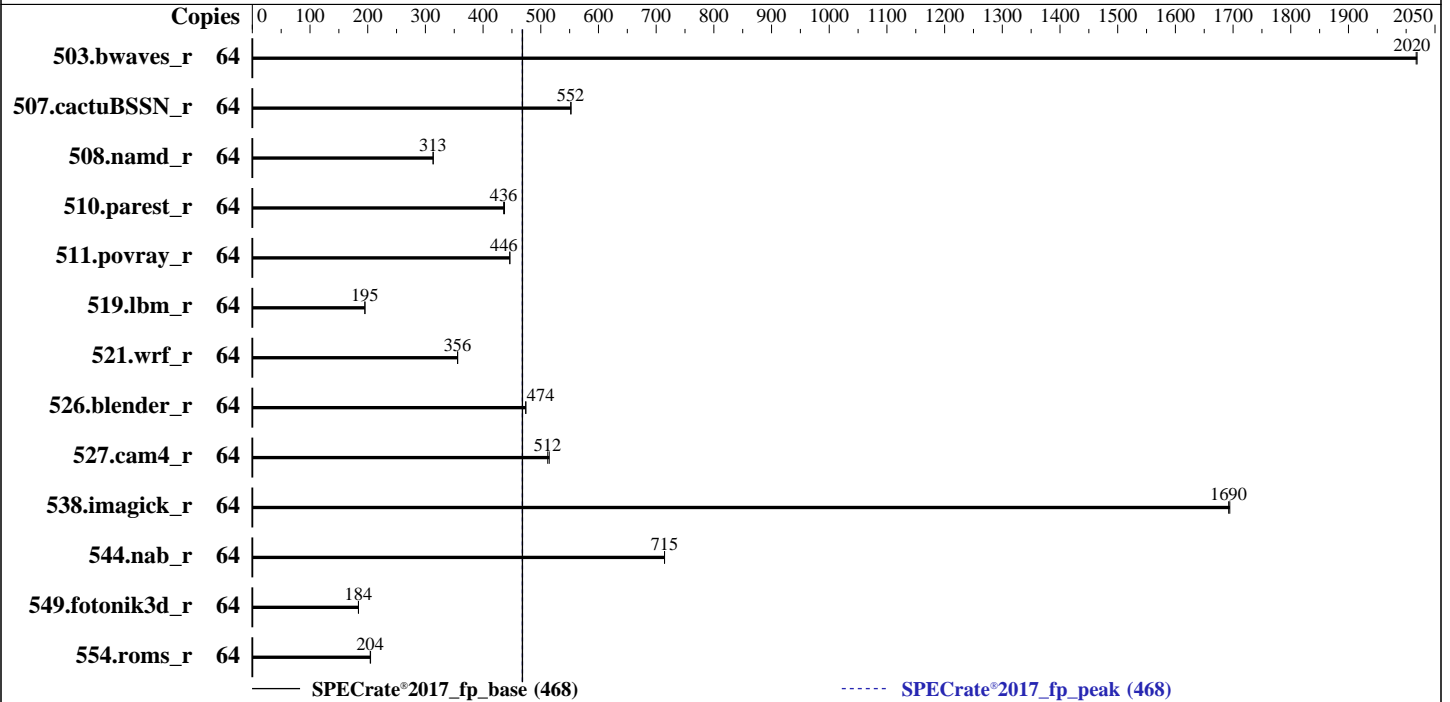
Test Sponsor: Supermicro

Tested by: Supermicro

Test Date: May-2026

Hardware Availability: May-2026

Software Availability: Mar-2026



Hardware

CPU Name: AMD EPYC 8325P
 Max MHz: 4500
 Nominal: 2700
 Enabled: 32 cores, 1 chip, 2 threads/core
 Orderable: 1 chip
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 256 MB I+D on chip per chip, 32 MB shared / 4 cores
 Other: None
 Memory: 384 GB (6 x 64 GB 2Rx4 PC5-6400B-R)
 Storage: 1 x 480 GB NVMe SSD
 Other: CPU Cooling: Air

Software

OS: Ubuntu 24.04.3 LTS
 6.8.0-110-generic
 Compiler: C/C++/Fortran: Version 5.0.0 of AOCC
 Parallel: No
 Firmware: Version 2.0 released Mar-2026
 File System: ext4
 System State: Run level 5 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: None
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	64	318	2020	<u>318</u>	<u>2020</u>			64	318	2020	<u>318</u>	<u>2020</u>		
507.cactuBSSN_r	64	147	553	<u>147</u>	<u>552</u>			64	147	553	<u>147</u>	<u>552</u>		
508.namd_r	64	194	314	<u>194</u>	<u>313</u>			64	194	314	<u>194</u>	<u>313</u>		
510.parest_r	64	383	437	<u>384</u>	<u>436</u>			64	383	437	<u>384</u>	<u>436</u>		
511.povray_r	64	335	446	<u>335</u>	<u>446</u>			64	335	446	<u>335</u>	<u>446</u>		
519.lbm_r	64	346	195	<u>346</u>	<u>195</u>			64	346	195	<u>346</u>	<u>195</u>		
521.wrf_r	64	<u>403</u>	<u>356</u>	403	356			64	<u>403</u>	<u>356</u>	403	356		
526.blender_r	64	<u>206</u>	<u>474</u>	206	474			64	<u>206</u>	<u>474</u>	206	474		
527.cam4_r	64	<u>219</u>	<u>512</u>	218	515			64	<u>219</u>	<u>512</u>	218	515		
538.imagick_r	64	93.9	1690	<u>94.1</u>	<u>1690</u>			64	93.9	1690	<u>94.1</u>	<u>1690</u>		
544.nab_r	64	151	715	<u>151</u>	<u>715</u>			64	151	715	<u>151</u>	<u>715</u>		
549.fotonik3d_r	64	1355	184	<u>1356</u>	<u>184</u>			64	1355	184	<u>1356</u>	<u>184</u>		
554.roms_r	64	<u>498</u>	<u>204</u>	496	205			64	<u>498</u>	<u>204</u>	496	205		

SPECrate®2017_fp_base = **468**

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,

(Continued on next page)



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Operating System Notes (Continued)

```
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
"/spec/cpu2017aoccal.5/amd_rate_aocc500_znver5_A_lib/lib:/spec/cpu2017aoccal.5/amd_rate_aocc500_znver5_A_lib/lib32:"
MALLOC_CONF = "retain:true"

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS settings:
NUMA Nodes Per Socket = NPS2
Determinism Control = Manual
Determinism Enable = Power
TDP control = Manual
TDP = 225
Package Power Limit Control = Manual
Package Power Limit = 225
TSME = Disabled
SMEE = Disabled

```
Sysinfo program /spec/cpu2017aoccal.5/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on smc9027sorano-u24-os Mon May 4 19:11:41 2026
```

SUT (System Under Test) info as seen by some common utilities.

----- Table of contents -----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo

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Platform Notes (Continued)

```

10. who -r
11. Systemd service manager version: systemd 255 (255.4-1ubuntu8.14)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

```

```

-----
1. uname -a
Linux smc9027sorano-u24-os 6.8.0-110-generic #110-Ubuntu SMP PREEMPT_DYNAMIC Thu Mar 19 15:09:20 UTC 2026
x86_64 x86_64 x86_64 GNU/Linux

```

```

-----
2. w
 19:11:41 up 6 min,  1 user,  load average: 0.00, 0.03, 0.00
USER      TTY      FROM          LOGIN@      IDLE        JCPU   PCPU   WHAT
root             10.23.202.142  19:07        6:29    0.00s   0.05s  sshd: root@pts/0

```

```

-----
3. Username
From environment variable $USER:  root

```

```

-----
4. ulimit -a
time(seconds)      unlimited
file(blocks)       unlimited
data(kbytes)       unlimited
stack(kbytes)      unlimited
coredump(blocks)   0
memory(kbytes)     unlimited
locked memory(kbytes) 2097152
process            1545502
nofiles            1024
vmemory(kbytes)    unlimited
locks              unlimited
rtprio             0

```

```

-----
5. sysinfo process ancestry
/sbin/init
SCREEN -S cpu
/bin/bash
python3 ./run_amd_rate_aocc500_znver5_A1.py
/bin/bash ./amd_rate_aocc500_znver5_A1.sh
runcpu --config amd_rate_aocc500_znver5_A1.cfg --tune base --reportable --iterations 2 fprate
runcpu --configfile amd_rate_aocc500_znver5_A1.cfg --tune base --reportable --iterations 2 --nopower
--runmode rate --tune base --size test:train:refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.003/templogs/preenv.fprate.003.0.log --lognum 003.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /spec/cpu2017aoccal.5

```

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Platform Notes (Continued)

```

6. /proc/cpuinfo
model name      : AMD EPYC 8325P 32-Core Processor
vendor_id      : AuthenticAMD
cpu family     : 26
model          : 2
stepping       : 1
microcode      : 0xb002162
bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size       : 192 4K pages
cpu cores     : 32
siblings       : 64
1 physical ids (chips)
64 processors (hardware threads)
physical id 0: core ids 0-3,8-11,16-19,24-27,32-35,40-43,48-51,56-59
physical id 0: apicids 0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.

```

7. lscpu

From lscpu from util-linux 2.39.3:

```

Architecture:                x86_64
CPU op-mode(s):              32-bit, 64-bit
Address sizes:                52 bits physical, 57 bits virtual
Byte Order:                   Little Endian
CPU(s):                       64
On-line CPU(s) list:         0-63
Vendor ID:                    AuthenticAMD
BIOS Vendor ID:              Advanced Micro Devices, Inc.
Model name:                   AMD EPYC 8325P 32-Core Processor
BIOS Model name:             AMD EPYC 8325P 32-Core Processor           Unknown CPU @
                               2.7GHz
BIOS CPU family:             107
CPU family:                   26
Model:                        2
Thread(s) per core:          2
Core(s) per socket:          32
Socket(s):                    1
Stepping:                     1
Frequency boost:              enabled
CPU(s) scaling MHz:          66%
CPU max MHz:                  4498.2422
CPU min MHz:                  1500.0000
BogoMIPS:                     5391.50
Flags:                         fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                               pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt
                               pdpe1gb rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl
                               nonstop_tsc cpuid extd_apicid aperfmperf rapl pni pclmulqdq
                               monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe popcnt aes
                               xsave avx fl6c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy
                               abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce
                               topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb
                               cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2 ibrs ibpb stibp
                               ibrs_enhanced vmmcall fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms
                               invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma
                               clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
                               xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
                               user_shstk avx_vnni avx512_bf16 clzero irperf xsaveerptr rdpru
                               wbnoinvd amd_ppin cppc amd_ibpb_ret arat npt lbrv svm_lock

```

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Platform Notes (Continued)

```

Virtualization:
L1d cache:
L1i cache:
L2 cache:
L3 cache:
NUMA node(s):
NUMA node0 CPU(s):
NUMA node1 CPU(s):
Vulnerability Gather data sampling:
Vulnerability Indirect target selection:
Vulnerability Itlb multihit:
Vulnerability L1tf:
Vulnerability Mds:
Vulnerability Meltdown:
Vulnerability Mmio stale data:
Vulnerability Reg file data sampling:
Vulnerability Retbleed:
Vulnerability Spec rstack overflow:
Vulnerability Spec store bypass:
Vulnerability Spectre v1:
Vulnerability Spectre v2:
Vulnerability Srbds:
Vulnerability Tsa:
Vulnerability Tsx async abort:
Vulnerability Vmscape:
nrip_save tsc_scale vmcb_clean flushbyasid decodeassists
pausefilter pfthreshold avic v_vmsave_vmload vgif x2avic
v_spec_ctrl vnmi avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes
vpclmulqdq avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid
bus_lock_detect movdiri movdir64b overflow_recov succor smca fsmr
avx512_vp2intersect flush_lld debug_swap srsr_user_kernel_no
AMD-V
1.5 MiB (32 instances)
1 MiB (32 instances)
32 MiB (32 instances)
256 MiB (8 instances)
2
0-15,32-47
16-31,48-63
Not affected
Not affected
Not affected
Not affected
Not affected
Not affected
Not affected
Not affected
Not affected
Not affected
Mitigation; Speculative Store Bypass disabled via prctl
Mitigation; usercopy/swapgs barriers and __user pointer
sanitization
Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP
always-on; PBRSE-eIBRS Not affected; BHI Not affected
Not affected
Not affected
Not affected
Not affected

```

```

From lscpu --cache:
NAME ONE-SIZE ALL-SIZE WAYS TYPE LEVEL SETS PHY-LINE COHERENCY-SIZE
L1d 48K 1.5M 12 Data 1 64 1 64
L1i 32K 1M 8 Instruction 1 64 1 64
L2 1M 32M 16 Unified 2 1024 1 64
L3 32M 256M 16 Unified 3 32768 1 64

```

```

8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0-15,32-47
node 0 size: 193030 MB
node 0 free: 192135 MB
node 1 cpus: 16-31,48-63
node 1 size: 193426 MB
node 1 free: 192734 MB
node distances:
node 0 1
0: 10 12
1: 12 10

```

```

9. /proc/meminfo
MemTotal: 395732092 kB

```

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Platform Notes (Continued)

10. who -r
run-level 5 May 4 19:05

11. Systemd service manager version: systemd 255 (255.4-1ubuntu8.14)
Default Target Status
graphical running

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	ModemManager apparmor apport blk-availability cloud-config cloud-final cloud-init cloud-init-local console-setup cron dmesg e2scrub_reap finalrd getty@ gpu-manager grub-common grub-initrd-fallback keyboard-setup lvm2-monitor multipathd networkd-dispatcher nvme-fc-boot-connections nvme-autoconnect open-iscsi open-vm-tools pollinate rsyslog secureboot-db setvtrgb snapd sysstat systemd-networkd systemd-networkd-wait-online systemd-pstore systemd-resolved systemd-timesyncd thermald ua-reboot-cmds ubuntu-advantage udisks2 ufw unattended-upgrades vgauth
enabled-runtime	netplan-ovs-cleanup systemd-fsck-root systemd-remount-fs
disabled	console-getty debug-shell ipmievd iscsid nftables rsync serial-getty@ ssh systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-networkd-wait-online@ systemd-pcrlock-file-system systemd-pcrlock-firmware-code systemd-pcrlock-firmware-config systemd-pcrlock-machine-id systemd-pcrlock-make-policy systemd-pcrlock-secureboot-authority systemd-pcrlock-secureboot-policy systemd-sysext systemd-time-wait-sync upower
generated	openipmi
indirect	systemd-sysupdate systemd-sysupdate-reboot uuid
masked	cryptdisks cryptdisks-early hwclock multipath-tools-boot screen-cleanup sudo x11-common

13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-6.8.0-110-generic
root=UUID=1bee86d1-7232-471a-ab77-0c9fc9498fal
ro

14. cpupower frequency-info
analyzing CPU 17:
current policy: frequency should be within 1.50 GHz and 2.70 GHz.
The governor "performance" may decide which speed to use within this range.

boost state support:
Supported: yes
Active: yes
Boost States: 0
Total States: 3
Pstate-P0: 2700MHz

15. sysctl

kernel.numa_balancing	1
kernel.randomize_va_space	0
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	8
vm.dirty_writeback_centisecs	500

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Platform Notes (Continued)

```

vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold        500
vm.min_unmapped_ratio      1
vm.nr_hugepages             0
vm.nr_hugepages_mempolicy  0
vm.nr_overcommit_hugepages 0
vm.swappiness               1
vm.watermark_boost_factor   15000
vm.watermark_scale_factor   10
vm.zone_reclaim_mode       1

```

```

-----
16. /sys/kernel/mm/transparent_hugepage
defrag      [always] defer+madvise madvise never
enabled     [always] madvise never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force

```

```

-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag                1
max_ptes_none         511
max_ptes_shared       256
max_ptes_swap         64
pages_to_scan         4096
scan_sleep_millisecs 10000

```

```

-----
18. OS release
From /etc/*-release /etc/*-version
os-release Ubuntu 24.04.3 LTS

```

```

-----
19. Disk information
SPEC is set to: /spec/cpu2017aoccal.5
Filesystem Type Size Used Avail Use% Mounted on
/dev/nvme1n1p2 ext4 457G 28G 406G 7% /

```

```

-----
20. /sys/devices/virtual/dmi/id
Vendor: Supermicro
Product: Super Server
Product Family: SMC H13
Serial: 0123456789

```

```

-----
21. dmidecode
Additional information from dmidecode 3.5 follows. WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
5x Micron Technology MTC40F2046S1RC64BD2 MWFF 64 GB 2 rank 6400
1x Micron Technology MTC40F2046S1RC64BD2 MXCC 64 GB 2 rank 6400

```

```

-----
22. BIOS
(This section combines info from /sys/devices and dmidecode.)

```

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Platform Notes (Continued)

BIOS Vendor: American Megatrends International, LLC.
BIOS Version: 2.0
BIOS Date: 03/23/2026
BIOS Revision: 5.42

Compiler Version Notes

=====
C | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
=====

=====
C++ | 508.namd_r(base) 510.parest_r(base)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
=====

=====
C++, C | 511.povray_r(base) 526.blender_r(base)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
=====

=====
C++, C, Fortran | 507.cactuBSSN_r(base)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
=====

=====
Fortran | 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

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WIO A+ Server AS -1115SV-WTNRT
(H13SVW-NT , AMD EPYC 8325P)

SPECrate®2017_fp_base = 468

SPECrate®2017_fp_peak = 468

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: May-2026
Hardware Availability: May-2026
Software Availability: Mar-2026

Compiler Version Notes (Continued)

Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

Fortran, C | 521.wrf_r(base) 527.cam4_r(base)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

Base Compiler Invocation

C benchmarks:
clang

C++ benchmarks:
clang++

Fortran benchmarks:
flang

Benchmarks using both Fortran and C:
flang clang

Benchmarks using both C and C++:
clang++ clang

Benchmarks using Fortran, C, and C++:
clang++ clang flang

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64

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Base Portability Flags (Continued)

521.wrf_r: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
526.blender_r: -funsigned-char -DSPEC_LP64
527.cam4_r: -DSPEC_CASE_FLAG -DSPEC_LP64
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3
-march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl

C++ benchmarks:

-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl

Fortran benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdalloc
-lflang -ldl

Benchmarks using both Fortran and C:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching

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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):

```
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamplibm -lamdalloc -lflang -ldl
```

Benchmarks using both C and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -lamplibm -lamdalloc -lflang
-ldl
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamplibm -lamdalloc -lflang -ldl
```

Base Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

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Base Other Flags (Continued)

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

Peak Optimization Flags

C benchmarks:

519.lbm_r: basepeak = yes

538.imagick_r: basepeak = yes

544.nab_r: basepeak = yes

C++ benchmarks:

508.namd_r: basepeak = yes

510.parest_r: basepeak = yes

Fortran benchmarks:

503.bwaves_r: basepeak = yes

549.fotonik3d_r: basepeak = yes

554.roms_r: basepeak = yes

Benchmarks using both Fortran and C:

521.wrf_r: basepeak = yes

527.cam4_r: basepeak = yes

Benchmarks using both C and C++:

511.povray_r: basepeak = yes

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Peak Optimization Flags (Continued)

526.blender_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

507.cactuBSSN_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-Sorano-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-Sorano-revB.xml>

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Tested with SPEC CPU®2017 v1.1.9 on 2026-05-04 15:11:41-0400.

Report generated on 2026-05-19 17:29:07 by CPU2017 PDF formatter v6716.

Originally published on 2026-05-19.